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CRR-2

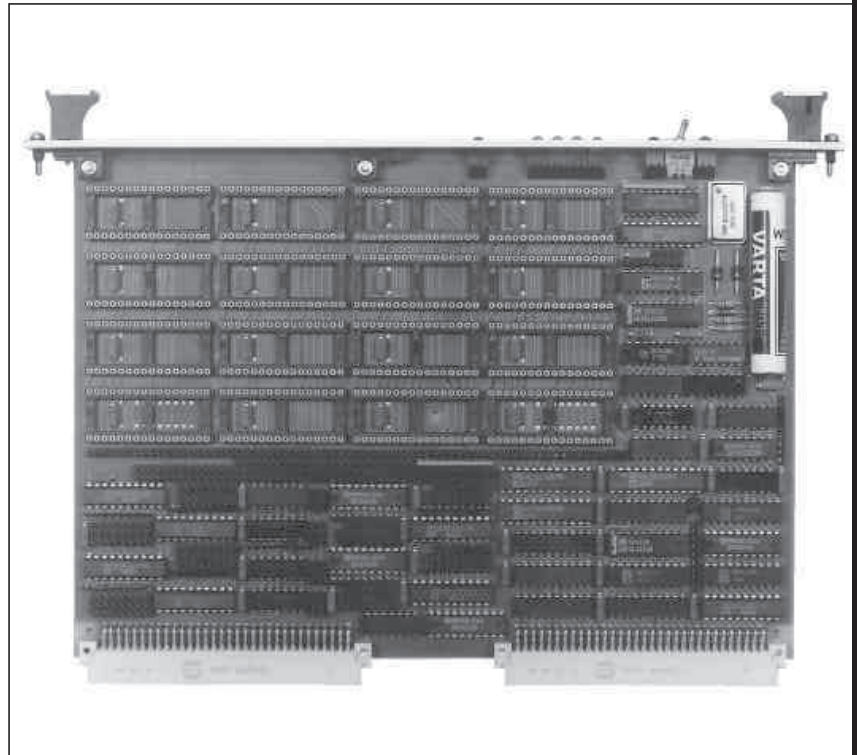
Multi-Purpose Memory Board

The CRR-2 is ideally suited for applications requiring large amounts of non-volatile, solid-state storage. Software developers can maintain their system environment by storing application code and data, *external to the host CPU*, during system power-down and reset cycles. This eliminates the need to re-load code from disk or across a network, after power is re-applied.

Industrial users will find the CRR-2 beneficial during servicing and repair, as a system's configuration can be preserved while power is removed and maintenance work undertaken. CRR-2 can also be used to store multiple configurations, allowing the host system to be reconfigured quickly and easily, and without the need for local disk or network connections.



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Device types that can be fitted to the CRR-2 include SRAM, ROM, EPROM, E²PROM and 5V only FLASH.

Device access times for each group are link-selectable in steps of 50ns, from 50 to 400ns.

Most types of E²PROM and FLASH devices can be programmed on-board.

Provision is made for an optional on-board battery back-up facility to retain SRAM data during power-down or under power failure conditions.

Features

- ▶ Sixteen 32-pin JEDEC sites configured as 4 groups of 4
- ▶ Supports SRAM, ROM, EPROM, E²PROM and 5V FLASH devices
- ▶ Accepts 32 pin and 28 pin devices
- ▶ 32 bit address and data
- ▶ Base address and mode of operation for each group independently configurable
- ▶ Device access times configurable between 50 and 400ns
- ▶ Battery back-up option for SRAM

Memory Devices

The CRR-2 can accept 28 or 32 pin JEDEC compatible devices from 8K x 8 up to 1 M x 8. These may be SRAM, ROM, EPROM, E²PROM or 5V FLASH.

Each bank can be fitted with different device types, although devices within a bank must all be of one type.

The access time for the devices within a bank can be set between 50 and 400 ns in 50ns intervals.

VMEbus Base Address

The base address of each bank can be individually configured. The base address of a bank should be placed on a bank size boundary (e.g. 512 Kbyte boundary for 128K x 8 devices) or part of the memory will not be visible from VME.

Write Protection

Each bank can be individually write protected. Write cycles to a protected bank result in a bus error.

E²PROM/FLASH Programming

E²PROMs can be programmed on-board providing they have data polling and are +5V only operation.

Front Panel

The front panel contains four yellow LEDs to show when each bank is being accessed. Bus errors are indicated by means of a red LED on the front panel. The RUN/LOCAL switch can be used to isolate the CRR-2 from the VMEbus. In the RUN position, the board may be accessed from VME. A green LED indicates that the board is enabled. In the LOCAL position, the board is isolated from the VMEbus, and a red LED indicates this condition.

Standby Voltage Selection

Each bank can be individually configured to use one of two standby voltage options: +5V STDBY from the VMEbus or an optional on-board battery. Switch-over to the selected standby power is automatic in the event of power loss. Only banks containing SRAM should be configured to use a standby power source.

The battery used on the CRR-2 is capable of retaining data in a bank of SRAMs that each draw 10µA for approximately 4 years. The total current drawn from the battery must not exceed 500µA in order that the standby voltage does not drop below 2V.



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UK

Radstone Technology PLC
Water Lane, Towcester,
Northants NN12 6JN
Telephone: (01327) 359444
Sales Support: (01327) 350715
Fax: (01327) 359662
E-mail Addresses:
Sales: sales@radstone.co.uk
Support: support@radstone.co.uk

USA

Radstone Technology Corporation
50 Craig Road
Montvale, NJ 07645-1709
Telephone: (800) 368-2738
Fax: (201) 391-2899
E-mail: radstone@radstone.com

Northeastern Region: (201) 391-2700
Southeastern Region: (703) 481-9834
North Central Region: (847) 304-0202
South Central Region: (214) 479-9040
Northwestern Region: (408) 727-4795
Southwestern Region: (909) 944 2560

FRANCE

Radstone Technology SA
Miniparc
6, avenue des Andes
91940 Les Ulis
Telephone: (1) 64.46.04.03
Fax: (1) 69.28.03.40

AUSTRALIA

Radstone Technology Pty Ltd
PO Box 59
1A First Avenue
Eastwood, N.S.W. 2122
Telephone: (02) 874-9143
Fax: (02) 874 9150

TAIWAN

Radstone Technology
F1 4 No 542-4
Chung Cheng Road
Hsintien
Taipei
Telephone: (2) 218 5819
Fax: (2) 218 6248



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