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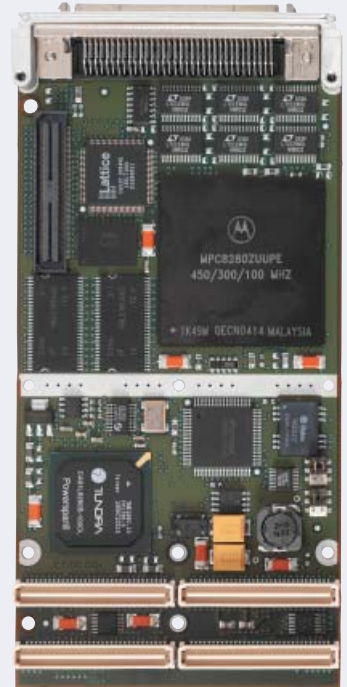
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EPMCQ2

EmPower Serial Communications PMC



- PowerQUICC II based PMC
- High performance serial comms
- Fully rugged
- Extensive Radstone software support
- Key features
 - 4 high speed sync/async channels
 - 2 low traffic async channels
 - Single 10/100 Base-T Ethernet port
 - 4 general purpose I/O Lines
 - 32 MBytes SDRAM
 - 16 MBytes Flash
 - 64-bit/66 MHz PCI 2.1 compatible
 - Monarch capable
 - Deployed Test firmware



A key member of Radstone's EmPower™ family, which offers a range of cost-conscious, comms-orientated products, EPMCQ2 provides a cost-effective solution for applications requiring multiple high speed serial comms interfaces. Fully rugged for defense and aerospace applications, EPMCQ2 also offers significant PowerPC processing capability all within an industry standard PMC form factor.

EPMCQ2 offers four high speed serial channels, capable of RS232, 422, 485 operation (software selectable), and configurable for synchronous or asynchronous operation. A further two channels provide an additional asynchronous capability useful for development debug and deployed operation on lower traffic lines. A single 10/100 Base-T Ethernet channel completes the array of communications options. EPMCQ2's onboard processing power (embedded 603e core) and memory resources are provided such that the protocol overheads associated with typical serial communication formats can be off-loaded from the host CPU.

COTS software support includes Radstone's industry leading Deployed Test software and async and sync (HDLC) operation under popular operating systems including VxWorks and INTEGRITY.



RADSTONE
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Why settle for less

Features			
Processor	MPC8280, PowerQUICC II	Taking advantage of Freescale's 0.13-micron process, the next generation of PowerQUICC II devices offers significant performance increases and power savings over the current generation PowerQUICC II devices, with speeds of up to 450 MHz and 300 MHz in the core and comms. processor respectively - at less than 2 Watts. Core : 450 MHz 603e embedded PowerPC with internal 16 KBytes data and 16 KBytes instruction caches plus floating point unit. Communications Processor Module (CPM) – runs at 300 MHz and off-loads peripheral tasks from the embedded PowerPC core, providing support for multiple communications protocols including 10/100 Mbps Ethernet and HDLC channels EPMCQ2 typical power consumption = 5 to 6W	
Main memory	32 MBytes SDRAM	The PowerQUICC II is interfaced to the main memory via a 64bit data bus (main memory = 64-bit, flash memory = 16-bit) running at 100 Mhz	
Flash memory	16 MBytes Flash		
Dual port RAM	32 KBytes in PowerQUICC II		
Ethernet interfaces	10/100 Base-T port	Ethernet port provided from the PowerQUICC II	
Serial ports	COM 1,2	RS232, provided from the PowerQUICC II	
	COM 3,4,5,6,	Async/Sync capable, provided from the PowerQUICC II. All channels are independently software selectable to be RS23 /422 or 485	
Discrete digital I/O	4-bits	4-bits of TTL compatible discrete digital I/O capable of generating an interrupt	
DMA engines	4 available	4 DMA controllers are available for efficiently moving large blocks of data	
PCI	Powerspan II	PCI to 60x bridge providing 64-bit/66 MHz PCI 2.1 compliant	
Monarch mode	Monarch and non-Monarch operation	The EPMCQ2 can be used in both processor PMC Monarch and non-Monarch modes, acting as the host for the local PCI bus or as a peripheral on the local PCI bus	
JTAG interface	On-card connector	A JTAG header is accessible for both factory test and software debug purposes	
Connectors	Front and Rear I/O options available	The EPMCQ2 is available as either EPMCQ2R targeted at rear I/O applications from ruggedization level 1 to 5 or EPMCQ2F targeted at front I/O application from Level 1 to 3	EPMCQ2R – optional front connector (15way micro-D type) for 2 debug channels and Ethernet (also available from rear)
			EPMCQ2F – front connector (68 way Honda SCSI) for all six serial ports, GPIO and Ethernet port
Software	Full Radstone software support	Includes full initialization Built-in test (BIT), a VxWorks BSP and COMMS module support including Fast Async, HDLC and PCI Transport (PCIT). INTEGRITY support is available from our technology partner Green Hills Systems Inc.	

Radstone Ruggedization Levels

EPMCQ2R is available in Radstone's five ruggedization levels for use in both convection- and conduction-cooled environments.

EPMCQ2F is available in Radstone's ruggedization levels 1 to 3 for use in convection-cooled environments

See Radstone's Ruggedization data sheet (RT184) for further details.

Ruggedization Levels	1	2	3	4	5
Cooling Method	Convection			Conduction	
Conformal Coat	Optional	Standard	Standard	Standard	
High Temp Operational	55°C @ 300ft/min	65°C @ 300ft/min	75°C @ 300ft/min	75°C	85°C
				At thermal interface	
Low Temp Operational	0°C	-20°C	-40°C	-40°C	

Table 1: Radstone's Ruggedization Levels

Optional Micro-D connector (debug serial ports and Ethernet)

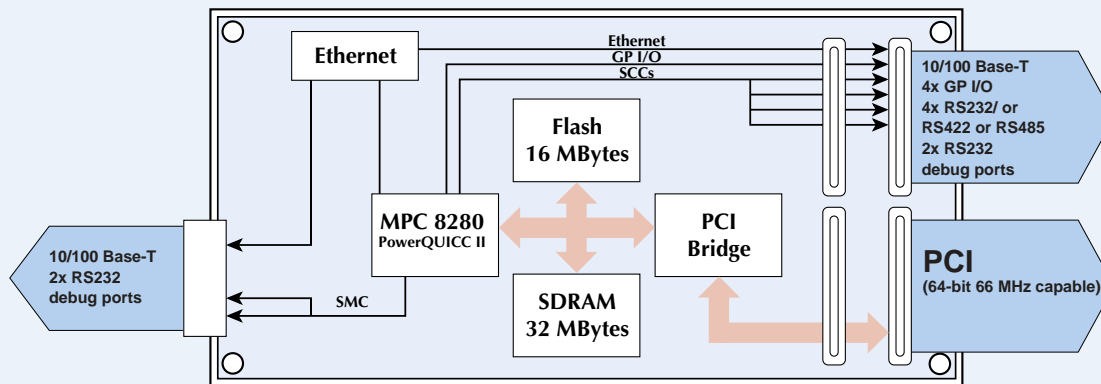
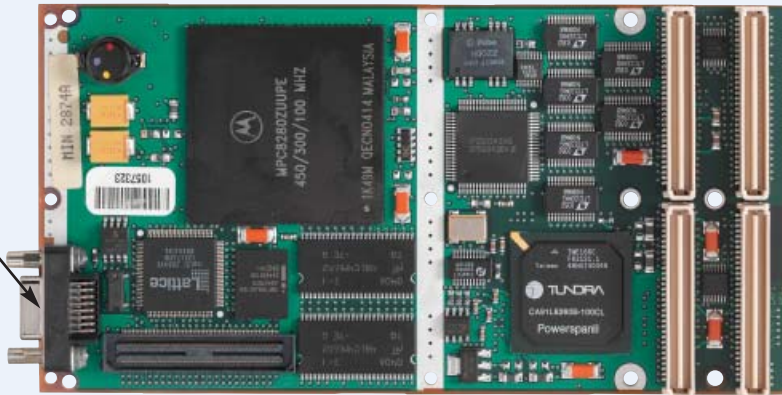


Figure 1: EPMCQ2R Image and Functional Block Diagram

68-way Honda SCSII connector (all 6 serial ports, GPIO and Ethernet)

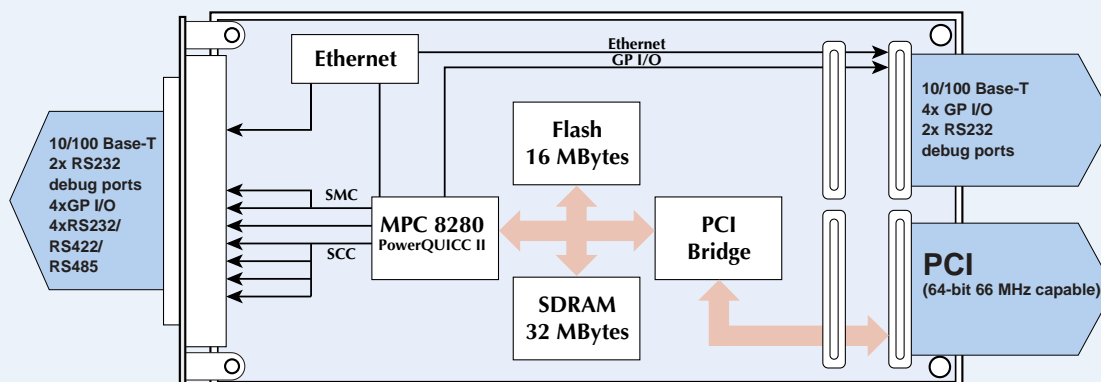
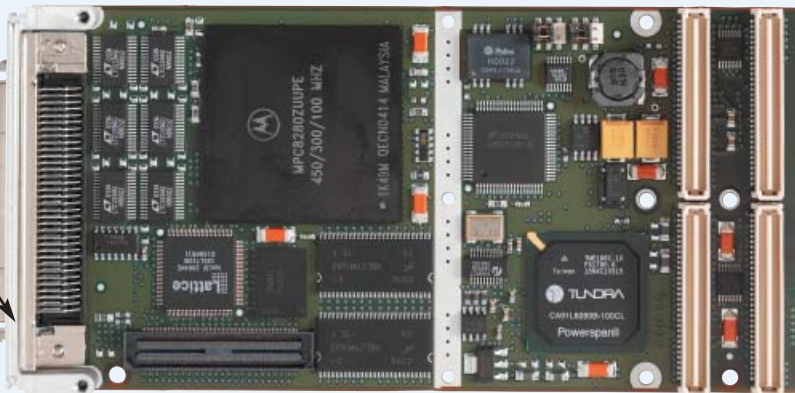


Figure 2: EPMCQ2F Image and Functional Block Diagram

Software model with VxWorks

The PowerQUICC II delivers substantial compute bandwidth for flexible communications processing on the EPMCQ2. This bandwidth can be liberated for deterministic real-time multi-tasking by Wind River Systems' VxWorks Operating System.

Radstone provides a fully featured board support package (BSP) that supports VxWorks 5.5 (Tornado 2.2) and VxWorks 5.4 (Tornado 2.0), plus all generic VxWorks sub-releases subsequent to these main versions. In addition to standard serial tty support, VxWorks TCP/IP network connectivity is provided via the EPMCQ2 Ethernet port.

Three protocol modules designed to run above VxWorks are also offered, allowing communication-orientated applications a flying start in development. These are:-

1. A Fast Async module for non-synchronous, high-speed, block-orientated serial port operation.
2. An HDLC module for synchronous, high-speed, block-orientated serial port operation.
3. A PCI Transport (PCIT) module for host-board control and optimized data-transfer across PCI.

These modules allow for software configurable operation covering a wide range of baud rates and characteristics, so most applications can use them 'as is'. Source code is available for customer adaptation to less mainstream system configurations.

The PCIT module provides a host board with multi-threaded application access to the EPMCQ2's resources. It comprises a section that runs on the EPMCQ2, and a section that runs on the host board into which the EPMCQ2 is plugged (the host must also be running VxWorks). For straightforward user familiarization, basic tasks are supplied which show example application access to PCIT functions.

Simultaneous host-board connections to HDLC and Fast Async channels may be formed, for mixed operation of differing line-types on individual serial ports. Any application tasks local to the EPMCQ2 can also form port connections using the same API. This can be useful in development, for adjusting the balance of application loading between host and PMC without having to make significant code alterations. The PCIT performs data transfers across the PCIbus using DMA. Application tasks on the host and EPMCQ2 may also interact via the PCIT; its interface is generic and not limited just to use with the other Radstone protocol modules.

Software delivery with VxWorks

Radstone provides the VxWorks BSP, protocol modules, and BIT (see below) as part of the support and services under a Software Maintenance Agreement. There is also a modest run-time charge per target for the protocol modules. The precise Agreement depends on the facilities required, and also any Radstone host-board software that may be needed with the above. Agreements are compulsory for the first year and optionally renewable for annual periods thereafter. Contact your local sales channel for details. The VxWorks / Tornado II Operating System itself is obtained from Wind River Systems.

Software model with INTEGRITY

With the maximum reliability, royalty-free INTEGRITY Operating System from Green Hills Systems Inc., a similar functional model is supported. In this case all software support is available direct from our technology partner, Green Hills Systems Inc.(GHS).

The same Fast Async and HDLC protocols are supported, and the GHS 'Connection Manager' fulfils the same host/PMC connection function as Radstone's PCI Transport module.

Radstone built-in test software (BIT)

An optional, high-coverage, initialization BIT resides within its own Flash device on the EPMCQ2. BIT also stores its test results in this same Flash device (in addition to giving a visual indication of failure). If the host board into which the EPMCQ2 is plugged is also a Radstone board running BIT, results data for the EPMCQ2 will automatically be co-ordinated with results for the host, or indeed results for any other Radstone boards in the whole (VME or CPCI or PCI connected) system. An application task can directly access this data once initialization has completed.

Onboard BIT is ordered via an option field in the hardware part number for the EPMCQ2. A BIT Maintenance Agreement, providing support and any updates that may be relevant (e.g. to combat obsolescence issues or introduce compatibility with newer Radstone products etc.) is compulsory for the first year after purchase of the hardware with BIT loaded. BIT for the EmPower family of products (which includes the EPMCQ2) is known as EPBIT. For those customers who wish to absorb Radstone BIT into a proprietary system-test harness, or make other changes not catered for by BIT's wide range of configurability, EPBIT source code is available as a separate item.

Specification

Function	Hardware Features
4 x SCC	Synchronous & Isosynchronous RS422/RS485 modes to 13 MBaud Asynchronous RS422 / RS485 modes to 1 MBaud Synchronous, Isosynchronous & Asynchronous in RS232 mode to 500 KBaud SCC electrical mode and data mode independently software configurable
2 X SMC	Asynchronous 120 Kbaud – RS232
1 X Ethernet	10/100 Base-T
4 X GPI/O	3.3V (5.0V tolerant)

Standard Ordering Information

Sales Code	Description
EPMCQ2R-133x	Level 1 PMC : 450 MHz PowerQuicc 2 (8280) with 300 MHz comms. processor and 100 MHz memory interface. Single 10/100 Base-T Ethernet port, four high speed RS232/422/485 serial ports, two low speed RS232 ports, four GPIO lines
EPMCQ2R-233x	Level 2 : as above with conformal coating – air-cooled
EPMCQ2R-333x	Level 3 : as above with conformal coating – air-cooled
EPMCQ2R-433x	Level 4 : as above with conformal coating – conduction-cooled
EPMCQ2R-533x	Level 5 : as above with conformal coating – conduction-cooled
EPMCQ2F-133x	Level 1 PMC : 450 MHz PowerQuicc 2 (8280) with 300 MHz comms. processor and 100 MHz memory interface. Single 10/100 Base-T Ethernet port, four high speed RS232/422/485 serial ports, two low speed RS232 ports, four GPIO lines
EPMCQ2F-233x	Level 2 : as above with conformal coating – air-cooled
EPMCQ2F-333x	Level 3 : as above with conformal coating – air-cooled

x=software option

NOTE: EPMCQ2R is available on shortest lead time. Consult factory for current lead times on this and the EPMCQ2F which is likely to be offered on longer lead time.

The standard ordering information (above) defines the standard build variant. Consult your local Radstone sales office for availability of further build options.

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