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User's Manual

Bulletin 1745 Timer-Counter Access Terminal

Catalog No. 1745-TCAT

Price: $25.00

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IMPORTANT INFORMATION

Solid state equipment has operational characteristics differing from those of electromechanical equipment. Because of this, and also because of the wide variety of uses for solid state equipment, all persons responsible for applying this equipment must satisfy themselves that each intended application of this equipment is acceptable.

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WARNING and CAUTION Boxes

WARNINGS indicate that people may be hurt if procedures are not followed properly. CAUTIONS indicate that machinery may be damaged or economic loss can occur if procedures are not followed properly.

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- Identify a possible trouble spot
- Tell what causes the trouble
- Give the result of improper action
- Tell how to avoid trouble

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Chapter 1 Introduction

The Timer/Counter Access Terminal (TCAT) is used with the SLC 100 Programmable Controller. It is designed for mounting in a panel cutout, usually the door of the controller enclosure.

With the SLC 100 controller in the Run mode, the TCAT provides access to programmed timer, counter, and sequencer data. This allows production, supervisory, and maintenance people to monitor this data "on-line".

Data can also be modified to accomodate a process or part change. A keyswitch helps prevent unauthorized modifications.

This manual points out important features of the TCAT, then goes on to discuss operating details as they apply to timer/counter/sequencer data, and the monitoring of I/O addresses.

You don't need a detailed knowledge of programming to use the TCAT.

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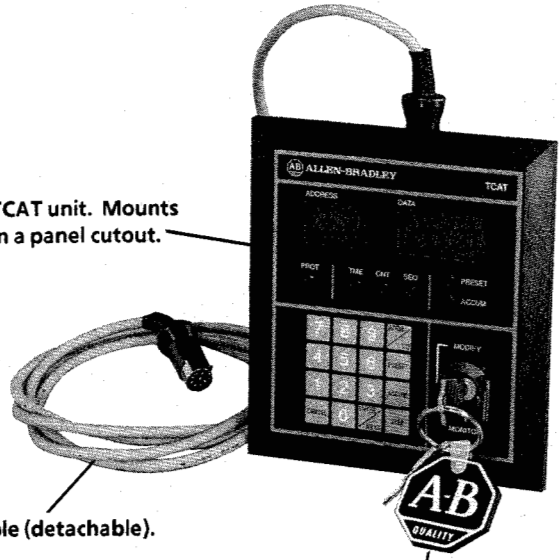
Chapter 2 Equipment Overview

Chapter Objectives

This chapter introduces you to the Timer/Counter Access Terminal (we'll call it TCAT) and the equipment it is used with. Important features are pointed out.

The Equipment

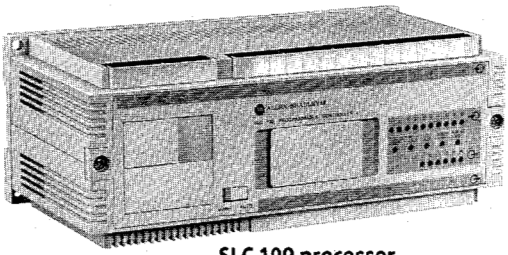
Components of the system are illustrated below.



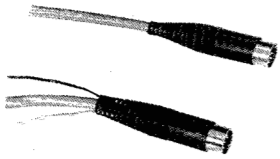
TCAT unit. Mounts in a panel cutout.

Interconnect cable (detachable).

Keys and keyring (with ID tag).



SLC 100 processor.



Optional remote communication kit.

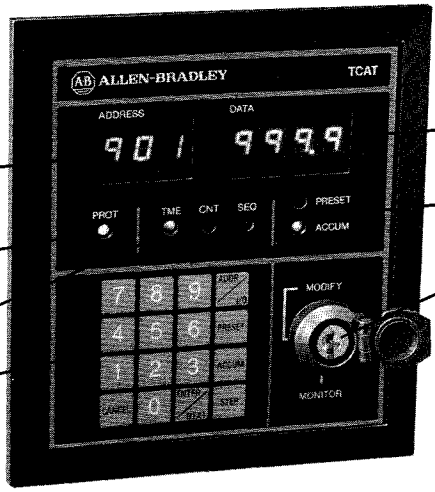
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TCAT – Features

The following features are pointed out in the illustration on Page 2-3. Programming terminology is explained in more detail in Chapter 3. Installation information appears in Chapter 8.

1. Keyswitch. You must use the key to operate the TCAT in the modify mode. This helps prevent unauthorized program modifications. Keyswitch cover protects keyslot.
2. Keys and keyring. For your convenience, error codes are printed on the keyring ID tag.
3. Keyboard. Used to access and enter data and addresses. Keys have positive, tactile feedback.
4. Address display. 3-character digital display shows addresses and other information.
5. Data display. 4-character digital display shows PR and AC values, and other information.
6. Instruction indicators. TME (green) indicates an RTO or RTF timer. CNT (green) indicates a CTU or CTD counter. SEQ (green) indicates an SQO or SQI sequencer.
7. Protect (PROT) status indicator. This red indicator is lit if the monitored instruction or sequencer step is protected in the program.
8. Data indicators. PRESET (green) indicates a preset (PR) value. ACCUM (green) indicates an accumulator (AC) value.
9. Error code table. Listed in English and French.
10. Mounting screws (4). The TCAT mounts in an enclosure or panel cutout. See Chapter 8.
11. Cable socket (labeled CC1). Plug the interconnect cable (supplied) into this socket and the programmer socket on the SLC processor. Refer to Chapter 8, Installation.

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(4) Address display.

(7) Protect status indicator.

(6) Instruction indicators.

(3) Keyboard.

(5) Data display.

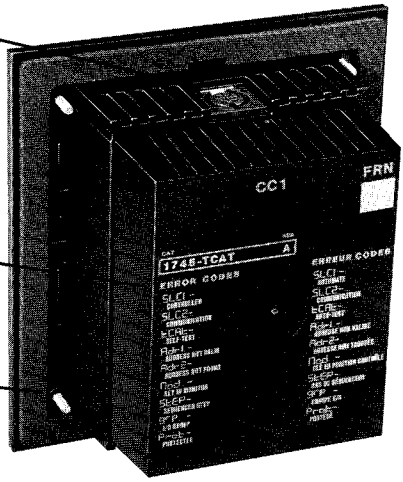
(8) Data indicators.

(1) Keyswitch.



(2) Keys and keyring.

(11) Cable socket.



(9) Error code table.

(10) Mounting screws.

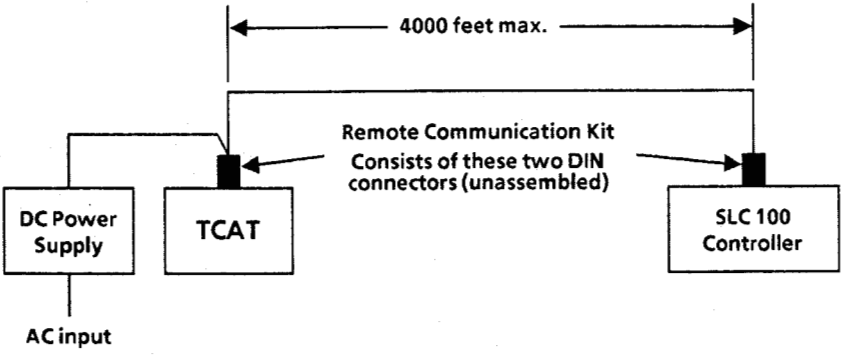
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Optional Remote Communication Kit

The remote communication kit (Catalog No. 1745-N2) allows you to locate the TCAT at a remote distance (up to 4000 feet) from the SLC 100 controller. For distances beyond 100 feet, a Series B SLC 100 controller is required.

The kit consists of two unassembled DIN connectors, pointed out in the figure below. You must provide the cable and a suitable power supply.

The kit is supplied with an instruction sheet to guide you thru the assembly and installation procedures. Cable and power supply specifications are included.



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Chapter 3 Basics of Operation

Chapter Objectives

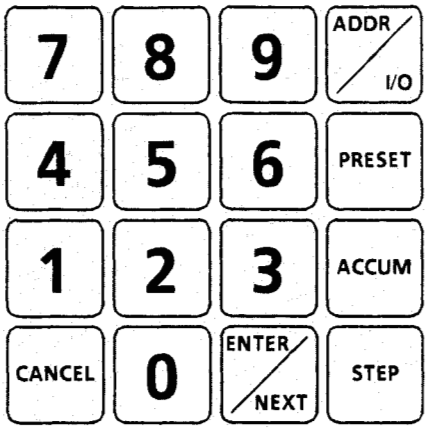
This chapter will show you the operating features of the TCAT in detail. Programming terminology will be explained. Error codes will be defined.

This will prepare you to perform the keystroke examples in Chapters 4, 5, and 6.

Keyboard

The TCAT keyboard is shown in Figure 3.1. Key functions are briefly explained. You will learn more about these functions in later chapters.

Figure 3.1



Key	Explanation
ADDR	Address
I/O	Input/Output groups
PRESET	Preset (PR) value
ACCUM	Accumulator (AC) value
CANCEL	Cancel previous keystroke
ENTER	Enter an address or data
NEXT	Move to next instruction
STEP	Step number (sequencers)

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Mode Selection

The keyswitch is shown at the right. The keyslot is vertical, indicating the MONITOR mode is in effect. The TCAT will operate in this mode with the key inserted or removed.

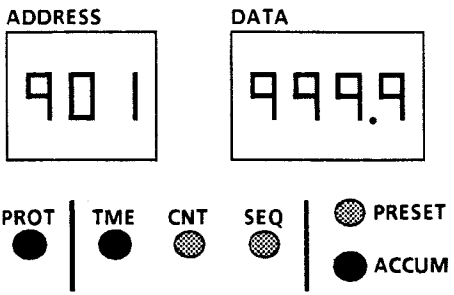


With the key inserted and turned clockwise 90° (keyslot horizontal), the MODIFY mode is in effect. The key cannot be removed in this mode.

Displays

Figure 3.2 shows the digital displays and LED indicators of the TCAT. The figure depicts a typical display.

Figure 3.2



Abbreviations

PROT – Protected	SEQ – Sequencer
TME – Timer	PRESET – Preset (PR) Value
CNT – Counter	ACCUM – Accumulator (AC) Value

Explanation of displays and LED indicators

In this typical display, the TME indicator is lit, telling us that a timer is being monitored. The ADDRESS display indicates the timer address to be 901.

The ACCUM indicator is lit, telling us that the AC value is being monitored. The DATA display shows the AC value to be 999.9.

The PROT indicator is lit, telling us that the timer is protected (the AC or PR value cannot be modified).

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Display Instruction Symbols

Various abbreviations and symbols appear in the DATA display to indicate the timer, counter, and sequencer instructions. These are summarized in Figure 3.3.

Figure 3.3

rto	Retentive On-Delay timer instruction.
rtF	Retentive Off-Delay timer instruction.
Ctu	Up-counter instruction.
Ctd	Down-counter instruction.
oE00	Sequencer output instruction. Event-driven. The last two digits (zeros in this case) indicate the sequencer group number.
oC00	Sequencer output instruction. Time-driven. The last two digits (zeros in this case) indicate the sequencer group number.
iE07	Sequencer input instruction. Event-driven. The last two digits (07 in this case) indicate the sequencer group number.
iC07	Sequencer input instruction. Time-driven. The last two digits (07 in this case) indicate the sequencer group number.
grP	This is a prompt message applying to address group numbers. It asks you to enter a group number.
StEP	This is a prompt message applying to sequencer instructions. It asks you to enter the sequencer step number.

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Error Codes

Procedural errors and other errors can occur when using the TCAT. Figure 3.4 explains the error codes which appear in the display when an error occurs.

Error code explanations also appear on the key ID tag and on the back of the TCAT.

Figure 3.4

Err	SLC 1	Processor is not in the Run mode, or the Auto/Manual switch is in Manual, or a processor error has occurred. ①
Err	SLC2	Communication error with the processor. ①
Err	tCAt	Error discovered during the TCAT power-up self test. ①
Err	Adr 1	Invalid address (not within the 901 to 932 range). ②
Err	Adr2	Address not found in the user program. ②
Err	Mod	This code appears when you attempt to modify data while in the monitor mode . ②
Err	StEP	Invalid sequencer step. ②
Err	grp	Invalid I/O group number. ②
Err	Prot	This error code appears when you attempt to modify data which is protected. ②

① Remedies for these errors appear in Chapter 8.

② These errors indicate that you entered wrong data or you attempted an invalid procedure. Remedy: Press the Cancel key, which returns the display to the point it was at before the error was made.

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Operating the TCAT

The following TCAT operating details will be easier to learn if you have access to a TCAT demonstrator unit connected to an SLC 100 demonstrator unit.

We recommend that you read the following paragraphs, then verify the procedures by practicing them on the demonstrator.

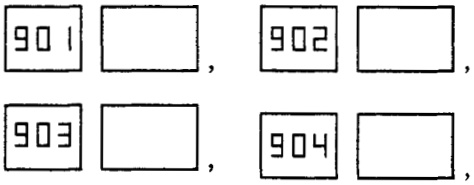
Power-Up

Case 1. The SLC 100 processor is in the Run mode, with the MAN/AUTO switch set at AUTO. This is the normal situation.

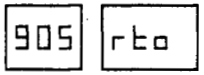
When you power-up the system, the TCAT will go thru a series of diagnostic tests and then show the lowest timer/counter/sequencer address number in your program, usually 901. The example below describes power-up. In this case, we are using 905 as the lowest address in the program.

Example: Suppose the lowest timer/counter/ sequencer address in your program is 905, an unprotected RTO timer, and the only other address you are using is 930, an SQO sequencer. The following will occur when you power-up:

The TCAT will go thru its diagnostic tests, then automatically scan addresses 901-932. The display will show 901, 902, 903, 904:



then stop at 905, showing you the instruction symbol:



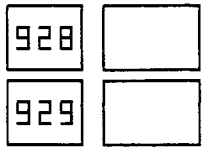
(TME indicator lit)

The TCAT keyboard is inoperative while the TCAT is scanning to an address number you are using in your program.

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Power-Up (continued)

To move to your next instruction, you simply press the NEXT key. If you do this while the TCAT is still scanning, the display will again show the addresses. You might happen to pick up the scan at address 928; the display would show 928, 929:



then stop at 930:



(SEQ indicator lit)

Any time you press the NEXT key after the scan is completed, the TCAT shows (in sequence) only those instructions you are using in the program. In this case, pressing NEXT repeatedly would alternately show instructions 905 and 930.

Case 2. The SLC 100 processor is in the Run mode, with the MAN/AUTO switch set at MAN.

When you power-up the system, the TCAT will go thru its diagnostic tests and then display:



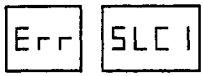
To clear this error, turn the processor AUTO/MAN switch to AUTO. The TCAT will then scan addresses as described in Case 1.

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Power-Up (continued)

Case 3. The SLC 100 processor is in some other mode than Run.

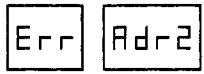
When you power-up the system, the TCAT will go thru its diagnostic tests and then display:



To clear this error, you must use the Pocket Programmer to place the processor in the Run mode. The TCAT will then operate as described in Case 1.

Case 4. Processor does not have a user program in memory, or the program does not contain any timer, counter, or sequencer instructions.

When you power-up the system, the TCAT will go thru its diagnostic tests, then scan addresses 901-932. The following error code will then appear:



To correct this error, enter a program having a timer/counter/sequencer instruction,then place the processor in the Run mode.

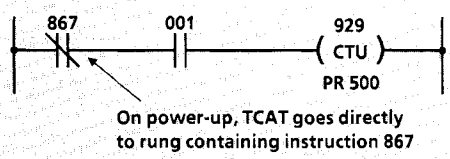
Special Note – Fine Time Base Instructions: If your program contains fine time base instructions (addresses 869-875), see Appendix A.

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Power-Up Using Address 867

An Examine OFF instruction at address 867 can be used in the SLC 100 program to automatically display any timer/counter/sequencer address you choose on power-up. An example is shown below.

Figure 3.5



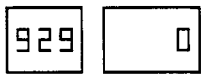
On power-up, TCAT goes directly to rung containing instruction 867

PROGRAMMING NOTE – Instruction 867 should be an Examine OFF, programmed in series with the rung containing the instruction you want displayed on power-up. (You can program instruction 867 anywhere in the rung, to the left of the timer/counter/sequencer instruction.)

We recommend that you use address 867 exclusively for this purpose. This avoids the possibility of causing the rung to be FALSE unintentionally.

On power-up, the TCAT will go thru its diagnostics checks, then *immediately* display the instruction in the rung containing address 867 (instead of scanning addresses until it reaches the lowest instruction used in the program).

Using the example above, the display shows:



(CNT, ACCUM indicators lit)

Note that the TCAT has gone directly to the AC value of the instruction. We've designed address 867 to work this way for your convenience.

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Using the Address Key



This is a dual function key. Press it once when you want to enter an address. Press it *twice* when you want to enter an I/O group number. We'll explain the I/O function in Chapter 6.

Moving to another address: Suppose the TCAT display shows



(CNT indicator lit)

and you want to move to address 915, an RTO timer. Press ADDR (*once*). The display will show:



The display will be flashing, prompting you for the last two numbers of the address. Press numeric keys 1, 5, then press ENTER. The display will show:



(TME, ACCUM indicators lit)

The display is showing you the AC value of the instruction. This occurs automatically, whenever you use the ADDR key to move to another address.

Mistakes: If you happen to press the ADDR key twice, or press the wrong numeric key, or enter an invalid address (error code appears), press the CANCEL key and try again.

Special Note – Fine Time Base Instructions: If your program contains fine time base instructions (addresses 869-875), see Appendix A.

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Using the NEXT Key



This key performs both the NEXT and ENTER functions. In the paragraph on Power-Up (Page 3-6), we used it to move to the *next* address in the user program. In the paragraph on Using the Address Key (Page 3-9), we used it to *enter* an address.

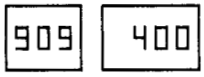
In the following paragraph, we'll show you how to use the NEXT key to move thru the timer/ counter/sequencer addresses in the user program. In Chapter 6 on monitoring inputs and outputs, we will show you how to use the NEXT key to move thru address group numbers.

Moving to the next address: Suppose your program has a timer, a counter, and a sequencer. The addresses are 905, 909, and 924 respectively. The display is showing the preset value of the timer:



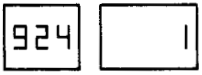
(TME, PRESET indicators lit)

Press the NEXT key. The display will show



(CNT, PRESET indicators lit)

The display has moved to the counter instruction, and it is showing the preset value. Press NEXT again. The display will show the sequencer:



(SEQ, PRESET indicators lit)

The display is showing the preset value of the current step.

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Using the NEXT Key (continued)

Two things are important to remember here:

- Pressing the NEXT key shows you the addresses in numerical order, even if they do not appear in numerical order in the user program.
- When moving to the next address, the display shows the same type of information that was shown previously. In this case it is the preset value. It could also be the AC value or the instruction symbol.

Special Note – Fine Time Base Instructions: If your program contains fine time base instructions (addresses 869-875), see Appendix A.

Using the CANCEL Key



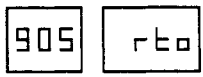
The CANCEL key can be used for three things:

- **Clear an error code.** Press CANCEL to clear an error code. Discussed on Page 3-4.
- **Show instruction symbol.** When the display is showing the PR or AC value, press CANCEL to display the instruction symbol. For example, if the PR value of timer 905 is shown,

  (TME, PRESET indicators lit)

  pressing CANCEL will bring up the instruction symbol:

  (TME indicator lit)

- **Cancel data.** When the display is prompting for numeric data (address, I/O group, step number, new AC or PR value) press CANCEL to cancel the data entry. Discussed on Page 3-9.

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Using the PRESET, ACCUM, and STEP Keys



The function of these keys is described in the keystroke examples of Chapters 4 and 5.

Protected PR and AC Values

The PR and AC values of a timer or counter can be protected in the user program. This is also true of the PR value of a sequencer step. These protected values cannot be changed with the TCAT.

When an instruction with protected PR and AC values is displayed by the TCAT, the PROT LED will be lit to indicate the protected status.

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Chapter 4 Timer and Counter Instructions

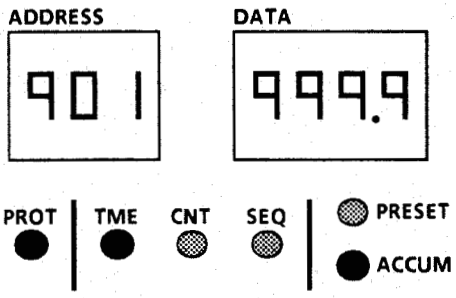
Chapter Objectives

In this chapter, you will be using the PRESET and ACCUM keys to monitor and modify the PR and AC values of a timer and counter instruction.

Displays Review

Figure 4.1 depicts a typical display. This is a repeat of Figure 3.2, included here to refresh your memory.

Figure 4.1



Abbreviations

PROT – Protected	SEQ – Sequencer
TME – Timer	PRESET – Preset (PR) Value
CNT – Counter	ACCUM – Accumulator (AC) Value

Explanation of displays and LED indicators

In this typical display, the TME indicator is lit, telling us that a timer is being monitored. The ADDRESS display indicates the timer address to be 901.

The ACCUM indicator is lit, telling us that the AC value is being monitored. The DATA display shows the AC value to be 999.9.

The PROT indicator is lit, telling us that the timer is protected (the AC or PR value cannot be modified).

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RTO and RTF Timer Characteristics

Timer instructions include the Retentive Timer On-Delay –(RTO)– and the Retentive Timer Off-Delay –(RTF)–. Both require the use of the Reset instruction –(RST)–. The TCAT will access RTO and RTF timers, but not the Reset instruction.

Timer instructions are represented by the following symbols:



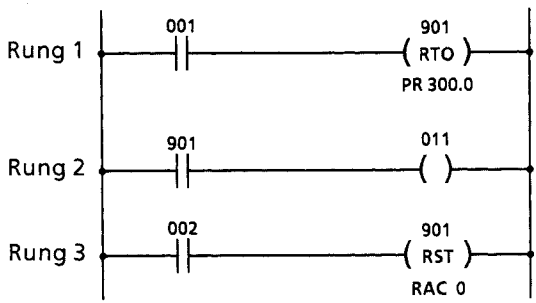
Address: 901-932
Range: 0.1-999.9 sec.

Figure 4.2 shows a typical timer ladder diagram. Rung 1 contains the RTO timer instruction. Its address is 901 and its PR value is 300.0. When this rung is TRUE, the AC value is incrementing. When the AC value equals the PR value, rung 2 goes TRUE, producing an output.

Rung 3 is the reset rung. When it goes TRUE, the AC value of the timer is reset to zero.

With the TCAT, you will be able to locate address 901, identify the instruction as an RTO timer, and monitor and modify its PR and AC values. You will also be able to observe the AC value incrementing when the timer rung is TRUE.

Figure 4.2



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CTU and CTD Counter Characteristics

Counter instructions include the Up Counter –(CTU)– and the Down Counter –(CTD)–. Both require the use of the Reset instruction –(RST)–. The TCAT will access CTU and CTD counters, but not the Reset instruction.

Counter instructions are represented by the following symbols:



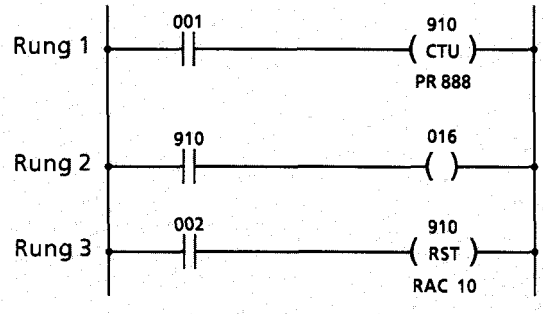
Address: 901-932
Range: 1-9999 counts

Figure 4.3 shows a typical counter ladder diagram. Rung 1 contains the CTU counter instruction. Its address is 910 and its PR value is 888. When this rung makes a FALSE to TRUE transition, the AC value increases by one. When the AC value equals the PR value, rung 2 goes TRUE, producing an output.

Rung 3 is the reset rung. When it goes TRUE, the AC value of the counter is reset to ten.

With the TCAT, you will be able to locate address 910, identify the instruction as a CTU counter, and monitor and modify its PR and AC values. You will also be able to observe the AC value increment with each FALSE to TRUE transition of rung 1.

Figure 4.3



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Monitoring a Timer Instruction

In the following keystroke example, we are assuming that the program contains an RTO instruction at address 914. Its PR value is 300.0, and it is protected (meaning that we can not modify the PR or AC value).

We will enter the address, which will be identified as a timer (TME). We will monitor the AC and PR values, and then identify the instruction specifically as an RTO (Retentive On-Delay) timer.

Figure 4.4

Keystroke Example – Monitor RTO timer at address 914. Preset is 300.0, protected.

Mode: MONITOR or MODIFY				Explanation
Press these keys	These LEDs will be lit	Display will show		
		Address	Data	
Apply power	CNT	901	Ctd	When you apply power, the lowest timer/counter/sequencer address is displayed, unless –]/[– 867 is used. Address 901 applies to a down counter (Ctd).
ADDR	None	9__		The display is flashing, prompting you for an address.
1, 4, ENTER	TME, ACCUM, PROT	914	.0	Address 914 is entered. The TCAT automatically monitors the AC value. The value will be incrementing if the timer rung is TRUE (timer operating). The TME LED indicates that address 914 applies to a timer; the ACCUM LED indicates the AC value is being monitored; the PROT LED indicates the AC and PR values are protected.
PRESET	TME, PRESET, PROT	914	300.0	The TCAT is monitoring the PR value. You can go back to the AC value if you choose. Just press the ACCUM key.
ACCUM	TME, ACCUM, PROT	914	.0	The TCAT is monitoring the AC value. The value will be incrementing if the timer rung is TRUE (timer operating).
CANCEL	TME, PROT	914	rto	The TCAT is identifying the instruction as a retentive timer On-Delay (RTO).

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Monitoring a Counter Instruction

The following keystroke example is quite similar to the timer keystroke example (Figure 4.4). In this case, we are assuming that address 914 applies to a CTU instruction. Its PR value is 3000, and it is protected (meaning that we can not modify the PR or AC value).

We will enter the address, which will be identified as a counter (CNT). We will monitor the AC and PR values, and then identify the instruction specifically as a CTU counter.

Figure 4.5

Keystroke Example – Monitor CTU counter at address 914. Preset is 3000, protected.

Mode: MONITOR or MODIFY				Explanation
Press these keys	These LEDs will be lit	Display will show		
		Address	Data	
Apply power	CNT	901	Ctd	When you apply power, the lowest timer/counter/sequencer address is displayed, unless –]/[– 867 is used. Address 901 applies to a down counter (Ctd).
ADDR	None	9__		The display is flashing, prompting you for an address.
1, 4, ENTER	CNT, ACCUM, PROT	914	0	Address 914 is entered. The TCAT automatically monitors the AC value. Value increases for each FALSE to TRUE transition of the counter rung. The CNT LED indicates that address 914 applies to a counter; the ACCUM LED indicates the AC value is being monitored; the PROT LED indicates the AC and PR values are protected.
PRESET	CNT, PRESET, PROT	914	3000	The TCAT is monitoring the PR value. You can go back to the AC value if you choose. Just press the ACCUM key.
ACCUM	CNT, ACCUM, PROT	914	0	The TCAT is monitoring the AC value. Value increases for each FALSE to TRUE transition of the counter rung.
CANCEL	CNT, PROT	914	Ctu	The TCAT is identifying the instruction as an up counter (CTU).

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Modifying a Timer Instruction

Figure 4.6 will show you how to modify timer data. We assume that the program contains an RTO instruction at address 901. Its PR value is 100.0, and its AC value is 999.9. The PR and AC values are not protected.

The keystroke example begins with the TCAT display showing the PR value (you learned how to do this in the previous keystroke examples). We will change the PR value to 300.0, then go on to the AC value and change that to 222.2. Note that the TCAT must be in the MODIFY mode to make these changes.

Figure 4.6

Keystroke Example – Modify RTO timer at address 901. PR value is 100.0. AC value is 999.9. PR and AC values are not protected.

Mode: MODIFY				
Press these keys	These LEDs will be lit	Display will show		Explanation
		Address	Data	
PRESET	TME, PRESET	901	100.0	**You have just pressed the PRESET key to display the PR value of timer 901. To change the PR value, press PRESET again.**
PRESET	TME, PRESET	901	____	**The four underlines are flashing, prompting you to enter a new PR value.**
3, 0, 0, 0 ENTER	TME, PRESET	901	300.0	**The PR value is changed to 300.0.**
ACCUM	TME, ACCUM	901	999.9	**The current AC value of 999.9 is displayed. We assume that the AC value is not incrementing (timer rung FALSE) To enter a new AC value, press ACCUM a second time.**
ACCUM	TME, ACCUM	901	____	**The four underlines are flashing, prompting you to enter a new AC value.**
2, 2, 2, 2 ENTER	TME, ACCUM	901	222.2	**The AC value is changed to 222.2.**

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Modifying a Counter Instruction

Figure 4.7 will show you how to modify counter data (similar to modifying timer data). We assume that the program contains a CTU instruction at address 901. Its PR value is 1000, and its AC value is 9999. The PR and AC values are not protected.

The keystroke example begins with the TCAT display showing the PR value (you learned how to do this in previous keystroke examples). We will change the PR value to 3000, then go on to the AC value and change that to 2222. Note that the TCAT must be in the MODIFY mode.

Figure 4.7

Keystroke Example – Modify CTU counter at address 901. PR value is 1000. AC value is 9999. PR and AC values are not protected.

Mode: MODIFY				Explanation
Press these keys	These LEDs will be lit	Display will show		
		Address	Data	
PRESET	CNT, PRESET	901	1000	**You have just pressed the PRESET key to display the PR value of counter 901. To change the PR value, press PRESET again.**
PRESET	CNT, PRESET	901	____	**The four underlines are flashing, prompting you to enter a new PR value.**
3, 0, 0, 0 ENTER	CNT, PRESET	901	3000	**The PR value is changed to 3000.**
ACCUM	CNT, ACCUM	901	9999	**The current AC value of 9999 is displayed. To enter a new AC value, press ACCUM a second time.**
ACCUM	CNT, ACCUM	901	____	**The four underlines are flashing, prompting you to enter a new AC value.**
2, 2, 2, 2 ENTER	CNT, ACCUM	901	2222	**The AC value is changed to 2222.**

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Using the ENTER Key to Reset the AC value to Zero

Figure 4.8 will show you how to use the ENTER key to change the AC value of a timer to zero (the PR value can be changed to zero in the same way). Arbitrarily, we have specified that the AC value is incrementing.

The AC value (and PR value) of a counter can be changed to zero in this same way.

Figure 4.8

Keystroke Example – Change the AC value of timer 901 to zero. The AC value is incrementing. PR and AC values are not protected.

Mode: MODIFY			
Press these keys	These LEDs will be lit	Display will show (Address ¦ Data)	Explanation
ACCUM	TME, ACCUM	901 ¦ 222.2	You have just pressed the ACCUM key. The AC value happens to be at 222.2, and incrementing. Suppose you want to "reset" the AC value to zero.
ACCUM	TME, ACCUM	901 ¦ ____.	The underlines are flashing, prompting you to enter a new AC value. To enter a value of zero, just press ENTER.
ENTER	TME, ACCUM	901 ¦ .0	The AC value is incrementing from zero.

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Chapter 5 Sequencer Instructions

Chapter Objectives

In this chapter, you will be monitoring the PR and AC values of the individual steps of a Sequencer instruction. You will also modify the PR value of a sequencer step.

SQO and SQI Sequencer Characteristics

Sequencer instructions include the Sequencer Output –(SQO)– and the Sequencer Input –(SQI)–. Both require the use of the Reset instruction –(RST)–. The TCAT will access SQO and SQI sequencers, but not the Reset instruction.

Sequencer instructions are represented by the following symbols:



Address: 901-932
100 steps maximum.

The Sequencer Output (SQO) instruction can control the ON/OFF status of up to 8 outputs for up to 100 steps. The ON/OFF status of outputs for each step is programmed.

The Sequencer Input (SQI) instruction differs from the SQO instruction in that the status of up to 8 *inputs* is programmed, producing an output only when the status of external inputs matches the programmed data for the particular step.

Sequencers can be time-driven or event-driven. With time-driven sequencers, each step functions similar to timer instructions, involving an AC value and a programmed PR value. In the same way, the event-driven sequencer functions similar to counter instructions.

You will find further operating details and programming information in the User's Manual for the SLC 100 Programmable Controller. Here we are concerned only with monitoring the PR and AC values of the individual steps, and modifying PR values.

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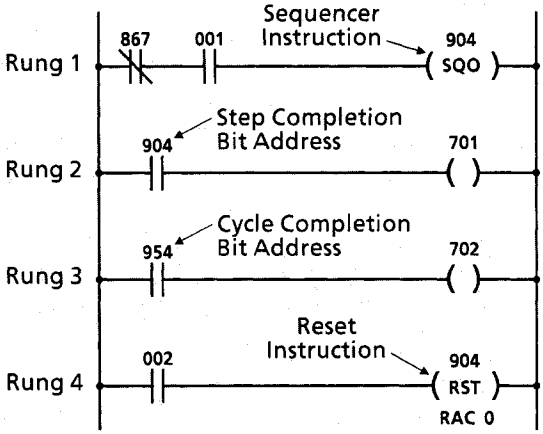
SQO and SQI Sequencer Characteristics (continued)

Figure 5.1 shows a typical ladder diagram of a sequencer output. This is a 5-step, time-driven sequencer controlling 6 external output addresses. For our purposes, we needn't consider the step completion bit (rung 2), the cycle completion bit (rung 3), or the Reset instruction.

A brief table of programmed data is included in the figure. Programmed data is coded. In this case, I/O group number 0 indicates that addresses 011 thru 018 are involved. The mask data indicates that addresses 017 and 018 are excluded from control. The step data indicates which outputs are ON and which are OFF during each step. PR values are programmed directly. Note that the PR values of steps 0, 2, and 4 are protected (meaning that we cannot modify them).

See Figure 5.3 on Page 5-4 for a typical sequencer input ladder diagram.

Figure 5.1



PROGRAMMED DATA

Instruction: SQO
Address: 904
Time-Driven
I/O Group: 0
Mask Data: 2, F

Step	Step Data	PR Value
0	1, 0	5.0*
1	2, 1	2.5
2	1, 2	30.0*
3	2, 4	2.5
4	2, 8	1.5*

*** Protected**

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Monitoring a Sequencer Instruction

The following keystroke example assumes that the program contains the sequencer shown in Figure 5.1. We will monitor the AC value, identify the step number, monitor the PR value, then identify the instruction.

Figure 5.2

Keystroke Example – Monitor the 5-step Sequencer Output instruction at address 904. See Figure 5.1 for programmed data.

Mode: MONITOR or MODIFY				Explanation
Press these keys	These LEDs will be lit	Display will show		
		Address	Data	
Apply power	SEQ, ACCUM, PROT	904	.0	Address 867 is programmed, so that the TCAT goes directly to timer/counter/ sequencer address 904 (and automatically displays the AC value). The SEQ LED indicates address 904 is a sequencer. The ACCUM LED indicates the AC value of the current step is being monitored. The PROT LED indicates the PR value of this step is protected.
STEP	SEQ, ACCUM, PROT	0	.0	The address display tells us we are monitoring the AC value of step 0. Note: You can call up the step number only when the display is showing the AC value or the PR value.
PRESET	SEQ, PRESET, PROT	0	5.0	Display indicates the PR value of step 0 is 5.0 (seconds). The PR value is protected.
CANCEL	SEQ	904	▫C00	As you recall, the CANCEL key calls up the instruction symbol. The 1st digit of the instruction symbol indicates SQO; the 2nd indicates time-driven; the 3rd and 4th indicate I/O group 0.

Display During Sequencer Operation – If the sequencer is operating during this keystroke example, the displayed AC value will be incrementing, and the displayed step number and PR value will advance as the sequencer moves from step to step.

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Modifying a Sequencer Step

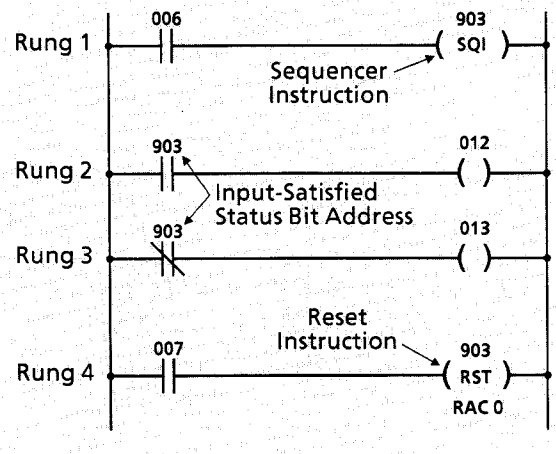
Figure 5.3 shows a typical ladder diagram of a sequencer input. This is a 4-step, event-driven sequencer involving 8 external input addresses. For our purposes, we needn't consider the Reset instruction (rung 4).

A brief table of programmed data is included in the figure. Programmed data is coded. In this case, I/O group number 7 indicates that I/O addresses 001 thru 008 are involved.

The mask data indicates that addresses 003 thru 008 are excluded from control. The step data indicates which inputs must be ON and which must be OFF during each step in order to produce an output (rungs 2 and 3).

PR values are programmed directly. Note that the PR values of steps 0 and 1 are protected (meaning that we cannot modify them).

Figure 5.3



PROGRAMMED DATA
Instruction: SQI
Address: 903
Event-Driven
I/O Group: 7
Mask Data: 0, 3

Step	Step Data	PR Value
0	0, 1	5*
1	0, 2	5*
2	0, 1	3
3	0, 2	10

* Protected

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Modifying a Sequencer Step *(continued)*

The following keystroke example assumes that the program contains the sequencer shown in Figure 5.3. We will modify the PR value of step 2, then attempt to modify the PR value of step 0.

Figure 5.4

Keystroke Example – Modify the Sequencer Input instruction at address 903. See Figure 5.3 for programmed data. Address 903 is the lowest timer/counter/sequencer address programmed.

Mode: MODIFY				Explanation
Press these keys	These LEDs will be lit	Display will show		
		Address	Data	
Apply power	SEQ	903	E07	Address 903 is displayed. The 1st digit of the instruction symbol indicates SQI; the 2nd indicates event-driven; The 3rd and 4th indicate I/O group number 7.
PRESET	SEQ, PRESET, PROT	903	5	To access a step number, the AC or PR value must first be displayed. We've called up the PR value (protected).
STEP	SEQ, PRESET, PROT	0	5	Current step number, 0, is displayed. Press STEP again to select another step.
STEP	SEQ	__	StEP	The display is flashing, prompting you for a step number.
2	SEQ, PRESET	2	3	Number 2 is displayed, flashing. Step 2 PR value is 3. If CANCEL key is pressed, monitoring of step 0 PR value resumes. This allows you to check the PR value of any step without making a change.
ENTER	SEQ, PRESET	2	____	Underlines are flashing, prompting for new PR value. If you press ENTER a 2nd time, the PR value will default to zero.
2, 5, ENTER	SEQ, PRESET, PROT	0	5	The PR value is changed to 25. The TCAT resumes monitoring the PR value of the current step. Now we'll attempt to modify the PR value of step 0.
STEP, 0, ENTER	SEQ, PRESET, PROT	Err	Prot	The PROT LED indicates the PR value is protected. The error code tells us we cannot change a protected PR value. Press CANCEL to remove the error code.

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Chapter 6 Monitoring Inputs and Outputs

Chapter Objectives

In this chapter, you will be monitoring the status of external inputs and external outputs. You will also monitor the status of instructions having internal addresses.

I/O Monitoring Capabilities of the TCAT

In most applications you will find it useful to monitor the on/off status of input devices (limit switches, selector switches etc.) and output devices (motor starters, relays, solenoids).

You may also want to monitor the status of instructions having internal addresses. For example, the status and overflow bits of counters could be used with output energize instructions having internal addresses. You will be able to observe how the counter affects this internal logic of your program.

Address Group Numbers

An address group number represents eight addresses. In most cases, these addresses are consecutive (001 thru 008, 011 thru 018).

There are 70 group numbers. They correspond to the group numbers established for programming sequencer instructions. For our purposes,we are using them to access user program addresses.

Figure 6.1 on the following page lists the 70 address group numbers.

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Address Group Numbers *(continued)*

Group number 0 gives you access to addresses of external outputs of the SLC 100 processor; group numbers 1 thru 6 give you access to addresses of external outputs of expansion units. Similarly, group numbers 7 thru 15 give you access to addresses of external inputs of the processor and expansion units. These 16 groups are the most widely used in TCAT applications.

Figure 6.1

Group No.	External Output Addresses	Internal Addresses
0	011-016	017-018
1	111-116	117-118
2	211-216	217-218
3	311-316	317-318
4	411-416	417-418
5	511-516	517-518
6	611-616	617-618

Group No.	External Input Addresses
7	001-008
8	101-108
9	201-208
10	301-308
11	401-408
12	501-508
13	601-608
14	009-010 109-110 209-210
15	309-310 409-410 509-510 609-610

Group No.	Internal Addresses
16	701-708
17	709-716
18	717-724
19	725-732
20	733-740
21	741-748
22	749-756
23	757-764
24	765-772
25	773-780
26	781-788
27	789-796
28	797-804
29	805-812
30	813-820
31	821-828
32	829-836
33	837-844
34	845-852
35	853-860
36	861-868
37 ①	869-876

① These are fine time base addresses and the auto/manual switch bit.

Group Numbers 38 thru 69

Groups 38 thru 69 correspond to addresses 901 thru 932 respectively. They are used in special sequencer instruction techniques.

The TCAT display for these group numbers does not represent external input/output addresses or internal addresses. Instead, it shows you the individual bits of a BCD counter (the count corresponds to the sequencer step number).

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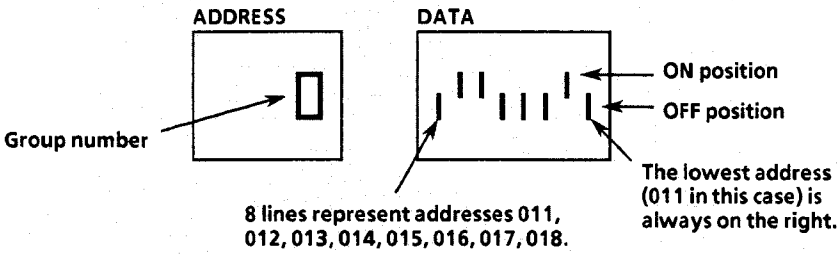
The TCAT Group Number Display

Pressing the dual function ADDR – I/O key *twice* will prompt you for a group number. After you enter a number and press ENTER, the TCAT will show you the group number and a representation of the status of the corresponding 8 addresses.

A typical TCAT display is shown in Figure 6.2. This display shows address group number 0. Figure 6.1 indicates that the symbols in the data window apply to addresses 011 thru 018. In this group, addresses 011, 012, 013, 014, 015, and 016 apply to external outputs of the SLC 100 processor unit. Addresses 017 and 018 are internal addresses.

Addresses appear in numerical order, with the lowest address on the right. If a symbol is "low" (011, 013, 014, 015, and 018), the instruction at that address is OFF (or the address is not used). If a symbol is "high", the instruction is ON.

Figure 6.2



This display tells you that devices connected to output terminals 12 and 16 of the processor unit are energized or ON. If you have output devices connected to terminals 11, 13, 14, and 15, they are OFF.

The display also tells you that an output energize instruction at internal address 017 is ON. If you are using an output energize instruction at address 018, it is OFF.

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Monitoring Group Number Addresses

In the following keystroke example, we will access a group number, use the ADDR – I/O key to access another group number, use the NEXT key to access successive group numbers, then return to the original instruction.

Figure 6.3

Keystroke Example – Monitor address group numbers 0, 15, and 17. *Return to the original display.*

Mode: MONITOR or MODIFY				Explanation
Press these keys	These LEDs will be lit	Display will show		
		Address	Data	
Apply power	CNT	901	CTD	We are assuming that the program contains a down counter at address 901, and address 867 is not used.
I/O, I/O,	None	--	GRP	The display is promting you for a group number.
0, ENTER	None	0		Group 0 is selected. Addresses 011, 012, 013, 014, 015, 016, 017, 018 are displayed. 012 and 014 are ON.
I/O, I/O, 1, 5, ENTER	None	15		Group 15 is selected. Addresses 309, 310, 409, 410, 509, 510, 609, 610 are displayed. 409 and 610 are ON.
NEXT, NEXT	None	17		Pressing the NEXT key twice has moved the display to group 17, applying to internal addresses 709, 710, 711, 712, 713, 714, 715, 716. Instructions at addresses 710, 713, 714, 715 are ON.
I/O, CANCEL	CNT	901	CTD	Pressing these keys has taken us back to the initial display, the down counter at address 901. We could also have accessed some other timer/counter/ sequencer address by pressing I/O, the new address, then ENTER.

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Display:
Discrete Light Emitting Diodes (LEDs) and 7-segment LED readouts.

Keyboard:
Sealed and embossed. 16 momentary push keys with tactile feedback.

Keyswitch:
Two-position keylock switch with cover.

Modes of Operation: Monitor and modify.

Error Code Identification Table:
On back of unit and on keyring ID tag.

Interconnect Cable:
6-foot (1.8m) cable. Plug-in connectors mate TCAT and processor unit.

Operating Power:
The TCAT receives power from the SLC 100 programmable controller via the 6-foot interconnect cable.

Remote connection (up to 4000 feet) is possible using a separate power supply rated at 10.8 to 26.4 VDC, 4 watts (for UL listing, power supply must be NEC Class 2). Use Belden cable style 9503 or equivalent. The Remote Communication Kit (Catalog No. 1745-N2) includes the necessary DIN connectors and the installation instructions.

Ambient Temperature Range:
0° to 60° C (operating). –40° to 85° C (storage).

Humidity Rating: 5 to 95% (without condensation).

Noise Immunity: NEMA Standard ICS 2-230.

Vibration (mounted in enclosure door or panel):
0.006 inch peak to peak displacement, 1.0g peak (max) acceleration, 1 Hr/axis.

Certification:
Meets NEMA Type 12 and 13 and IEC 529 IP65 enclosure applications. UL listed. CSA certified.

Overall Dimensions:
5.50" (139.7 mm) wide x 6.0" (152.4 mm) high x 2.89" (73.41mm) deep. Weight: 13 ounces.

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Chapter 8 Installation

Chapter Objectives

This chapter explains how to check out the TCAT prior to installation and how to install the TCAT in an enclosure cutout.

Equipment Checkout

Before you install the TCAT, check it for external damage which might have occurred during shipment. Then power it up to check for possible internal damage.

To power up the TCAT, you will need access to an energized SLC 100 processor. Power-up procedure:

1. Energize the processor. Place the processor in the Run mode with the pocket programmer. Turn the Auto/Manual switch to Auto.
2. Disconnect power to the processor. Unplug the pocket programmer.
3. Plug one end of the interconnect cable (supplied with the TCAT) in the socket on the top of the TCAT and the other end in the programmer socket on the processor. Cable connectors are keyed to guard against improper insertion. Make sure the spring latch is engaged to secure the connector in the socket.
4. Energize the processor. The TCAT should operate as described in Chapter 3 on Pages 3-5 to 3-8.
5. If the TCAT display shows any of the following 3 error messages, follow the procedures listed in Figure 8.1.

Err SLC 1

Err SLC2

Err tCAt

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Equipment Checkout (continued)

Figure 8.1 lists error codes which indicate possible internal malfunctions. Explanations of other error codes appear on Page 3-4.

Figure 8.1

ADDRESS Display	DATA Display	Meaning	Remedy
Err	SLC 1	SLC 100 processor hardware problem. As noted on Page 3-4, this error code also appears if the processor is not in the Run mode, or the Auto/Manual switch is in Manual.	1. Refer to the remedial advice in the SLC 100 User's Manual, Chapter 21, Fig. 21.2. 2. If the error code still exists, contact your local A-B representative for repair or replacement.
Err	SLC2	Communication problem between TCAT and processor.	1. Check the interconnect cable to make certain it is securely connected at both ends. 2. Recycle power to the processor. If error code still appears, contact your local A-B representative for repair or replacement.
Err	tCAt	TCAT hardware problem. Detected during self-test.	Contact your local A-B representative for repair or replacement.

Enclosure Considerations

The TCAT is designed for mounting in a cutout in the door of the controller enclosure. Follow all recommendations on enclosures listed in the SLC 100 User's Manual.

The enclosure protects the equipment from atmospheric contamination. Standards established by the National Electrical Manufacturer's Association (NEMA) define enclosure types, based on the degree of protection an enclosure will provide. Select a NEMA type 12 or 13 enclosure that is suitable for your application.

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Mounting

Figure 8.2 shows the cutout dimensions for the TCAT. We have also included a mounting template (Appendix E) for your convenience. In locating the cutout on the enclosure door,

- Provide adequate clearance behind the enclosure door and above and below the TCAT housing. See note on ventilation below.
- Position the TCAT so that stress will not be exerted on the interconnect cable when the door is opened.
- Before drilling, make certain that all power to the equipment within the enclosure is disconnected. Make certain the drill bit will not make contact with equipment and cause damage.

Important note on ventilation: To keep the TCAT temperature within the specified range, cooling air in the enclosure must be between 0-60°C.

Allow 3 inches clearance above and 6 inches clearance below the TCAT housing.

All four studs on the TCAT should be grounded to the enclosure door. To accomplish this, scrape away the paint at the points where each KEPS nut (supplied) makes contact with the inside of the enclosure. Important: The torque requirement on the KEPS nut is 6 in-lbs. Tighten so that the TCAT bezel just contacts the surface of the enclosure.

Caution: The TCAT has ventilation holes for cooling on the upper and lower portions of the back cover. If you make additional holes in the enclosure, make sure the ventilation holes are covered to protect against metal chips entering the TCAT housing.

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Connecting the Cable

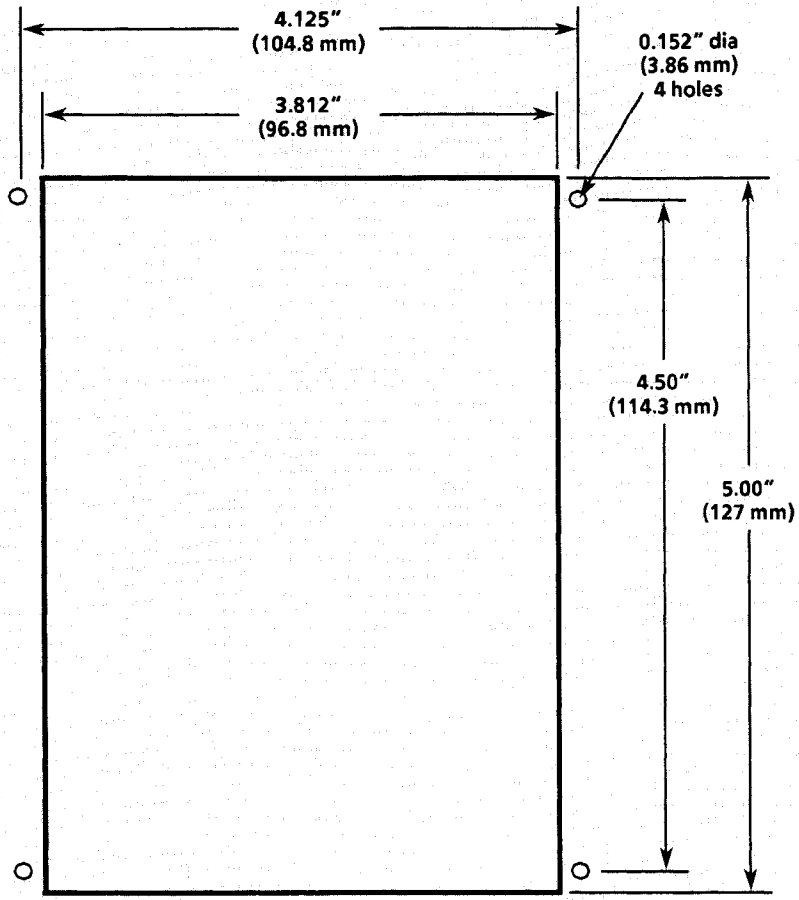
A cable instruction sheet is provided with the TCAT unit. It is important that you read and follow these instructions when connecting the TCAT to an SLC-100 processor.

One end of the interconnect cable plugs into the socket on the top of the TCAT and the other end plugs into the programmer socket on the processor. Cable connectors are keyed to guard against improper insertion. Make sure the spring latch is engaged to secure the connector.

> **Caution:** The TCAT end of the interconnect cable is live. We recommend that you disconnect the cable at the SLC end, to guard against connector short circuits and possible damage to the processor.

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Figure 8.2



Approximate cutout and drilling dimensions for the TCAT

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Cleaning Recommendations

The following materials are recommended for cleaning the front panel of the TCAT.

1. Detergent Solution. (Typical household type cleaner.)
2. Isopropyl Alcohol.
3. Methanol.

> **WARNING:** Solutions containing the following substances must not be used:
> Chlorinated Hydrocarbons
> Toluene
> Acetone
> Ammonia

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Appendix A Using Fine Time Base Instructions

Description

Fine Time Base instructions allow you to program timers with greater resolution than the 0.1 second resolution provided by standard timers. I/O group 37 includes fine time base bit addresses 869 through 875 (address 869 is a scan counter; addresses 874 and 875 count in intervals of 0.5 and 1.0 seconds respectively). The measured scan time of your SLC 100 program determines which of the remaining Fine Time Base instructions you can use.

Caution: The scan time of the SLC 100 is slightly increased during the following operations of the TCAT.

1. During the TCAT power up scan discussed on Page 3-5.
2. Each time the NEXT key is pushed to select a new address to monitor.
3. When the Address key and the numeric keys are used to select a new address to monitor.

An increase in scan time may cause Fine Time Base instructions to operate inaccurately.

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Appendix B Special Sequencer Instruction Techniques

Description

The SLC 100 sequencer instructions can be used to create:

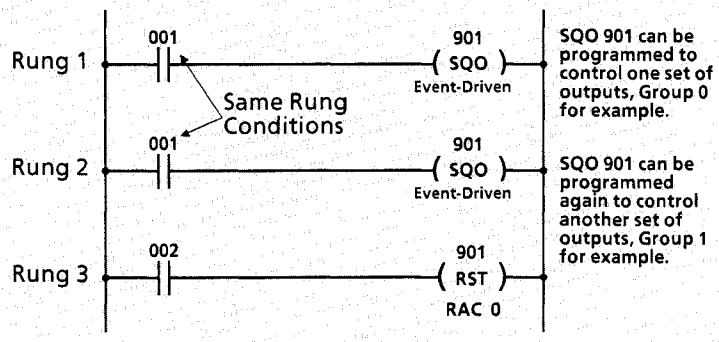
- Cascaded sequencers
- Reversing sequencers
- SQI driving an SQO
- SQI monitoring an SQO step
- Sequencer jump operation

Procedures for monitoring/modifying sequencer instructions are explained in Chapter 5. A cascaded sequencer example is shown below.

Cascaded Sequencer Instruction

Sequencer instructions can be cascaded to control more than 8 bit addresses. These instructions will sometimes share the same addresses, operate according to the same rung conditions, have the same preset value and have the same number of steps. An example of this type of cascaded sequencer is illustrated in the figure below.

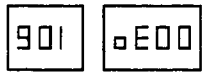
Figure B.1



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Cascaded Sequencer Instruction (continued)

You can differentiate between the sequencer instructions at address 901 by monitoring the instruction symbol. The TCAT will display



when monitoring the sequencer controlling group 0. The TCAT will display



when monitoring the sequencer controlling group 1. (Instruction symbols are explained on Page 3-3.) Accessing the instruction symbol on power up or by using the Cancel key is described in previous chapters. The instruction symbol should be checked before modifying the preset value of a cascaded sequencer step.

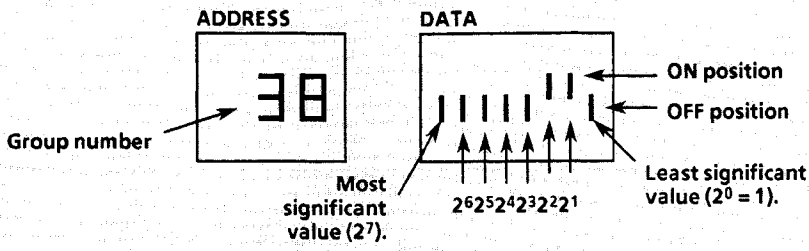
Important: When modifying PR values of cascaded sequencers, make certain that sequencer rung conditions are false, then change the PR values of both sequencers.

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Step Number Display

When programming reversing sequencers, SQI monitoring an SQO step, and the sequencer jump operation, special sequencer groups 38 through 69 must be used. Special sequencer groups 38 through 69 are binary counters that display the current step number for the operation. The step number is displayed in binary notation with the least significant digit in the far right position. Figure B.2 below illustrates this.

Figure B.2



The display tells you that there is a ONE in the second and third positions. In binary notation, this represents a six ($2^1 + 2^2 = 2 + 4 = 6$). The current step number for the sequencer operation is step number 6.

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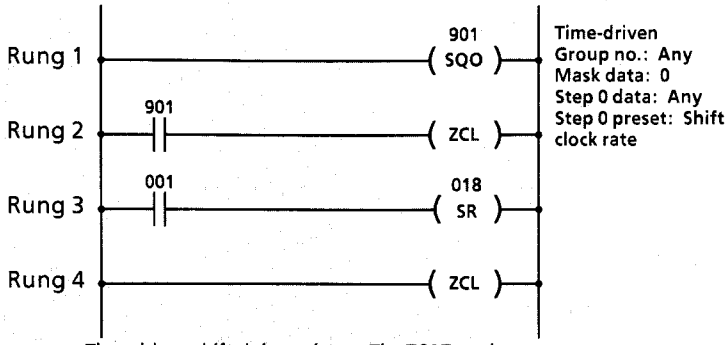
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Appendix C Shift Register Instructions

Description

With a shift register instruction, status data enters an 8-bit register and is automatically shifted through the register from one bit address to the next on a time or event-driven basis. A sequencer instruction is used to control the shift rate as shown in the figure below.

Figure C.1



Time-driven shift right register – The TCAT can be used to modify the preset value of step 0 which is the shift clock rate.

The shift clock rate of a shift register instruction can be modified with the TCAT. In the figure above, the shift register at address 18 is controlled by sequencer 901. The preset value of sequencer 901 controls the shift clock rate. This preset can be modified with the TCAT. See the keystroke example for modifying sequencer steps in Chapter 5 on Page 5-4 for details.

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Appendix D EEPROM Memory Module

Description

The EEPROM module is an optional device which plugs into the SLC 100 processor unit. With the EEPROM you can:

- Save the contents of the RAM memory, for storage purposes.
- Load the contents of the EEPROM memory into the processor RAM.

If you have an EEPROM module installed and you have modified data with the TCAT and you wish the modified data to be saved in the EEPROM, you must do so *before disconnecting power*. Do this by exchanging the TCAT with the pocket programmer and following the procedure described in Chapter 16 of the SLC 100 User's Manual (Publication 1745-800).

After saving the contents of the RAM, you can remove power.

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Appendix E Mounting Template

Description

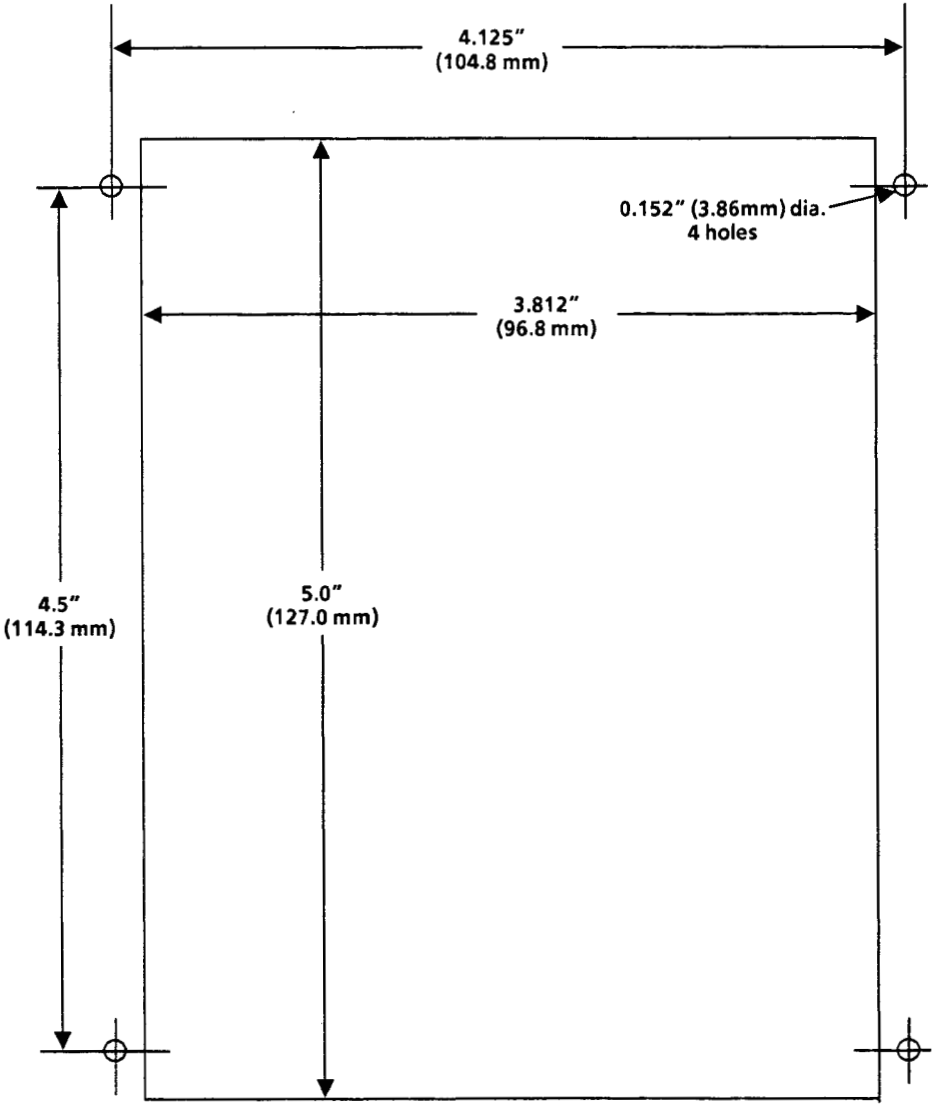
We've provided the following template (3 copies) for your convenience in mounting the TCAT. **Before you use the template, read the installation recommendations on Page 8-3.**

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Appendix E Mounting Template

Before you use this template, read the installation recommendations on Page 8-3.

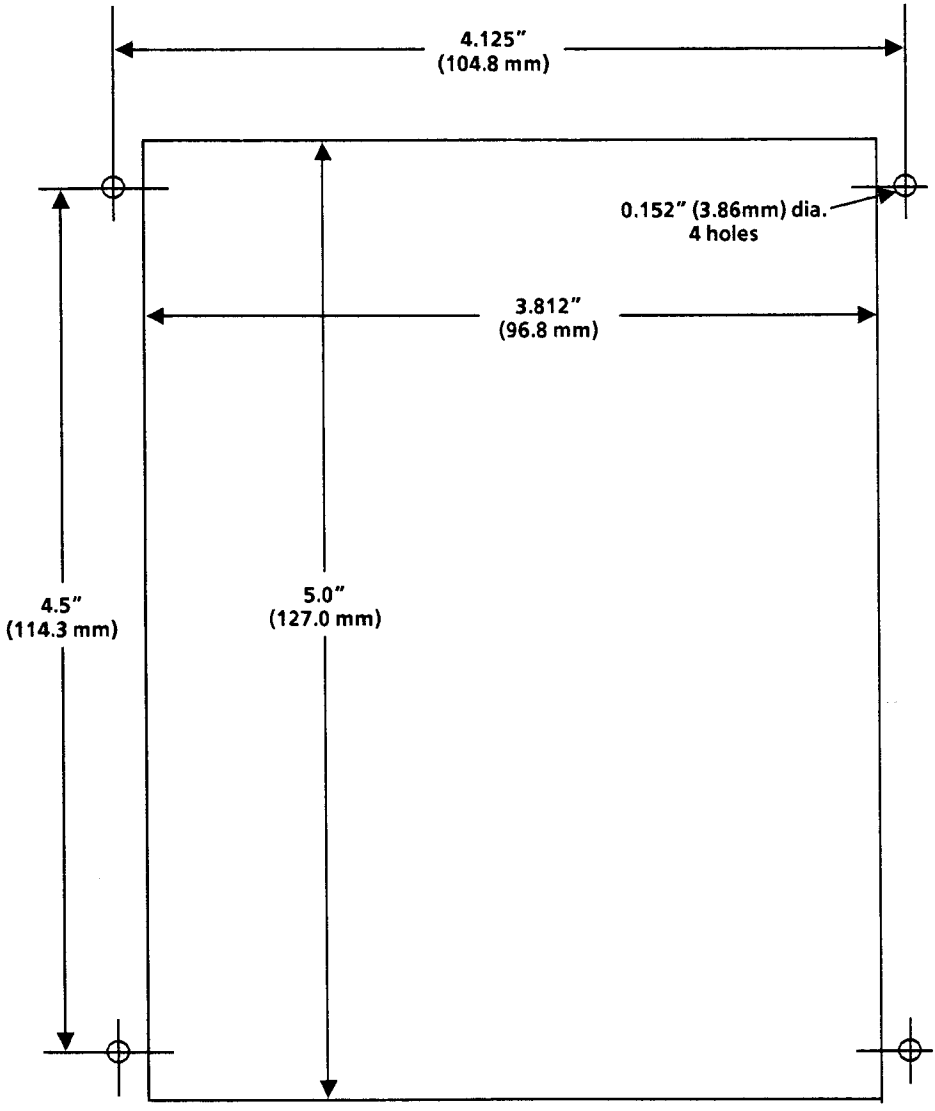


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Appendix E Mounting Template

Before you use this template, read the installation recommendations on Page 8-3.

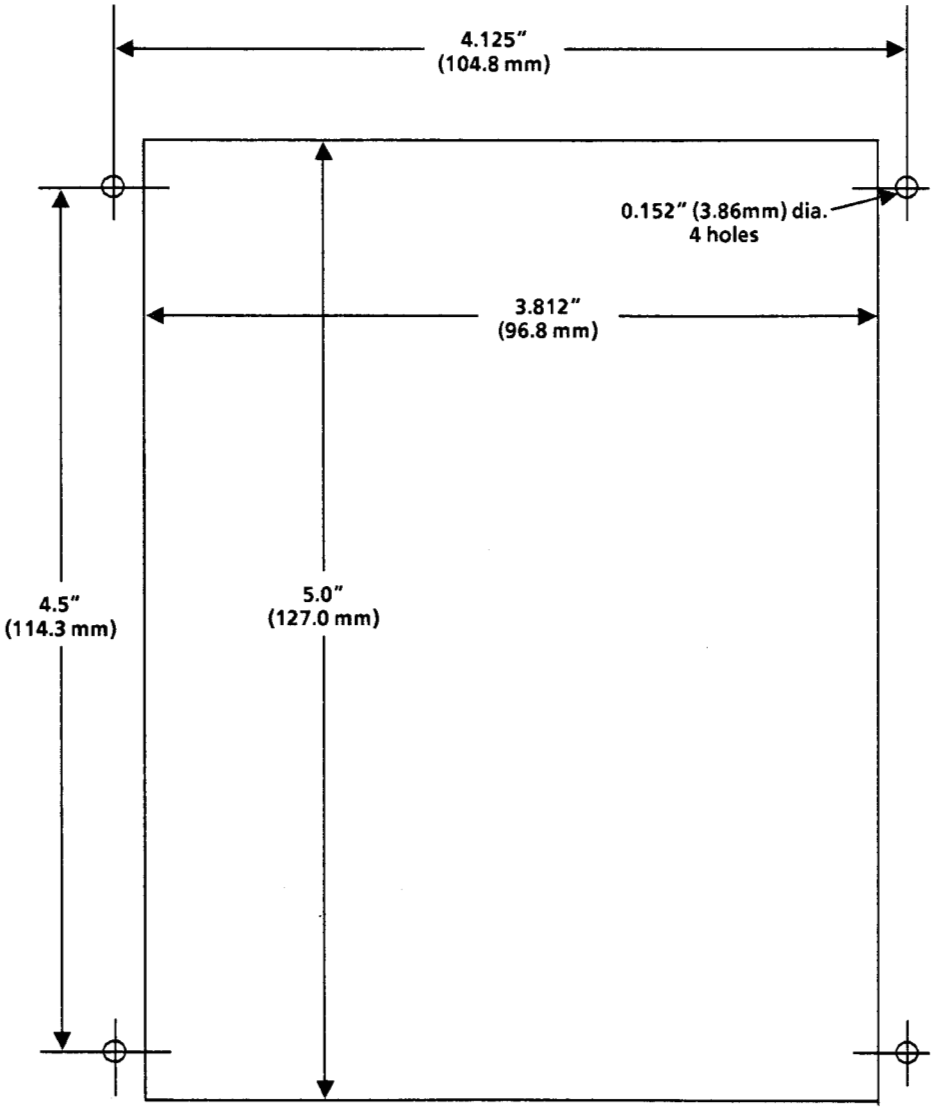


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Appendix E Mounting Template

Before you use this template, read the installation recommendations on Page 8-3.



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