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DDR SDRAM DIMM/SODIMM

Old Part number

M	X	X	X	X	X	X	X	X	X	X	X	-	X	X	X
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16

1. Memory Module (M)

2. DIMM Configuration

3 : 4 & 8 Byte DIMM 4 : 8 Byte SODIMM

3~4. Data bits

12 : x72 184pin 1U Register DIMM
46 : x72 294pin Register DIMM with PLL
47 : x72 294pin Register DIMM with PLL(512MB DIR2)
63 : x64 172pin U-DIMM
66 : x64 168pin U-DIMM 68 : x64 184pin U-DIMM
70 : x64 200pin U-DIMM
72 : x64 184pin Register DIMM
73 : x64 184pin Register DIMM with FET switch
74 : x72 168pin U-DIMM
81 : x72 184pin U-DIMM
83 : x72 184pin Register DIMM
85 : x72 200pin U-DIMM
89 : x64 200pin Register DIMM

5. Feature

H: DDR SDRAM(3.3V Vdd) L: DDR SDRAM(2.5V Vdd)
T : DDR-II

6~7. Depth

01 : 1M 02 : 2M
04 : 4M 08 : 8M
09 : 8M(for 128Mb/512Mb) 16 : 16M
17 : 16M(for 128Mb/512Mb) 28 : 128M
29 : 128M(for 128Mb/512Mb) 32 : 32M
33 : 32M(for 128Mb/512Mb) 56 : 256M
64 : 64M 65 : 64M(for 128Mb/512Mb)

8. Number of Bank in Comp. & Interface & Refresh

0 : 4bank,Mixed interface,64m/4K Refresh(15.6us)
1 : 4bank,SSTL_2,64m/4K Refresh(15.6us)
2 : 4bank,SSTL_2,64m/8K Refresh(7.8us)
3 : 8bank,SSTL_2,128m/16K Refresh(7.8us)
5 : 4bank,SSTL(1.8V,1.8V),64ms/8K(7.8us)

9. Composition Component

0 : x4 3 : x8
4 : x16 5 : x32
6 : x16+x32 7 : x4 Stack(Uniframe)
8 : x4 Stack(Flexframe) 9 : x8 Stack(Flexframe)

10. Component Generation

M : 1st Generation A : 2nd Generation
B : 3rd Generation C : 4th Generation
D : 5th Generation

11. Package

G : UBGA K : TSOP2-400 for DDP
T : TSOP2-400 U : TSOP2-400F-LF

12. PCB revision & type

0 : None 1: 1st Rev.
2 : 2nd Rev. 3: 3rd Rev.
L : Low Cost

13. "-"

14. Power

C: Normal Power, Self Ref. L: Low Power, Self Ref.

15~16. Speed

A0 : 10 ns @CL2 A2 : 7.5 ns @CL2
A3 : 6 ns @CL2 A4 : 5 ns @CL2
B0 : 7.5ns @CL2.5 B3 : 6 ns @CL2.5
C4 : 5 ns @CL3 C5 : 3.75 ns @CL3
D4 : 5 ns @CL4 D5 : 3.75 ns @CL4
D6 : 3.3 ns @CL4 E4 : 5 ns @CL5
E5 : 3.75 ns @CL5 E6 : 3.3 ns @CL5

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