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CPCI-100A

Two Slot IndustryPack Carrier for CompactPCI

Application Information

The CPCI-100A IndustryPack carrier board provides two IndustryPack slots on 3U CompactPCI card. Two versions are available: the CPCI-100A-FP provides shielded 0.8mm Champ50 front panel connectors for IndustryPack I/O; the CPCI-100A-BP provides IndustryPack I/O via the CompactPCI J2 connector. A rear-panel transition module, the BIO-1 is available.

The CPCI-100A provides an IndustryPack clock that is software-selectable between 8 and 32 MHz on a per-slot basis The standard 8 MB memory space is available for each IndustryPack site. High speed 32-bit PCI host address space permits desirable linear mapping and fixed address partitions of memory, I/O and ID spaces for each IndustryPack module.

The CPCI-100A uses a PLX PCI9080 controller as the PCI bus interface chip and Altera FPGA logic, which provides fully configurable PCI interrupt vector space. The PCI bus interface automatically handles 3.3 V or 5.0 V signalling levels.

The CPCI-100A meets the PCI specification and conforms to the VITA-4 IndustryPack Logic Interface Specification. This guarantees compatibility with a wide range of IndustryPacks.

Features

- Two IndustryPack slots on a 3U CompactPCI board
- I/O via two 50-pin Champ50 shielded connectors (-FP version)
- I/O via CompactPCI backplane J2 connector (-BP version)
- Switchable 8/32 MHz IndustryPack bus for each slot
- Two LEDs for IndustryPack access and one for power monitor
- Filtered power rails and resettable fuses
- Interrupt acknowledge space for retrieving IndustryPack vectors
- Reduced memory-map version available by special order

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Specifications

Form Factor	3U CompactPCI
PCI	PCI Specification, Revision 2.1
PCI Controller	PLX PCI9080
Number of IndustryPack Slots	Two Supports up to one double-wide IndustryPack
IndustryPack Bus Clock	Switchable 8/32 MHz per IndustryPack slot
IndustryPack Features Supported	I/O space, ID space, memory space, interrupt acknowldege cycles
IndustryPack Memory Size	8 Mbytes per IndustryPack*
Carrier Board Memory Space	64 Mbytes*
IndustryPack Interrupts Supported	Int0* and Int1* for each slot
PCI Bus Interrupts Generated	INTA# only
I/O Access	-FP: One Champ50 0.8mm sheilded connector per IndustryPack on front panel -BP: Via CompactPCI J2
Indicators	One green LED per slot for CPU access on PCB One amber LED for power monitor on PCB
Fuses	Resettable 2.5A @ 20º C for 5 VDC Resettable 2.0A @ 20º C for +/-12 VDC
Dimensions	160 mm x 100 mm
Weight	0.17 kg (0.35 lb)
Power Requirements	+5V VDC, 200 mA typ +12 V DC, 10 mA typ –12 V DC, 10 mA typ Extra power required for IndustryPack modules
Environmental	Operating temperature: 0 to 70°C Humidity: 5 to 95% non-condensing Storage: –40 to +85°C

*A reduced memory map version of the CPCI-100A is available by special order. Contact sales for more information.

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Order Information

CPCI-100A-FP	Two slot 3U CompactPCI IndustryPack with front panel I/O
CPCI-100A-BP	Two slot 3U CompactPCI IndustryPack with rear panel I/O
EK-CPCI-100A-FP	Engineering Kit for CPCI-100A-FP. Contains: Printed hardware user manual Floppy disk containing: SDpacK SDK for Windows NT Driver software license agreement Floppy disk conatining: Example PCI programming code Bill of materials Assembly drawing Circuit schematic PLX PCI9080 data sheet
EK-CPCI-100A-BP	Engineering Kit for CPCI-100A-BP. Contains: Printed hardware user manual Floppy disk containing: SDpacK SDK for Windows NT Driver software license agreement Floppy disk conatining: Example PCI programming code Bill of materials Assembly drawing Circuit schematic PLX PCI9080 data sheet

Associated Products

CPCI-200A-FP	Four slot 6U CompactPCI IndustryPack with front panel I/O
CPCI-200A-BP	Four slot 6U CompactPCI IndustryPack with rear panel I/O
BIO-1	CompactPCI rear panel transition module for CPCI-100A-BP
C-HD50M-CM50M	Six foot cable HD50 male to Champ50 male
IP-TERM-HD50	50-pin screw terminal block with HD50 female

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