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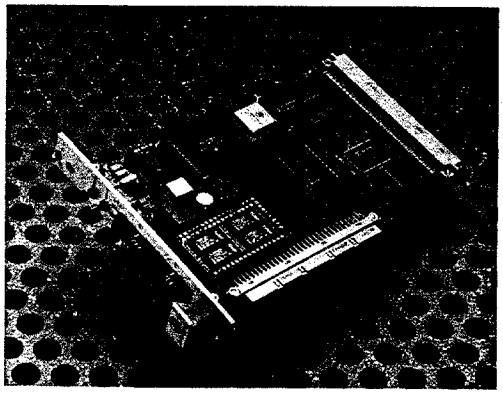
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881-532-01

SBC5



Application Information

The SBC5 VMEbus single board computer hosts a Motorola MC68030 processor at 32 MHz. One or four MBytes of dual access DRAM provides high performance via a burst mode cache fill interface. Full system controller functions are provided, including a four-level arbiter and a digital BBSY filter. Two serial ports and a clock-calendar provide software compatibility with GreenSpring's complete line of SBCs. The SBC5 is available in either 3U or 6U form factor.

Up to one MByte software is resident on two EPROMS or an optional Flash EPROM module. The Flash module supports resident erase/write/ verify update of code or data up to 100,000 update cycles. Dramatically reduced software development, debugging and updating time is possible by the use of Flash EPROM over traditional EPROM. The costly and error-prone process of erasing EPROMs is gone. Software updates now take only 30 seconds, without powering down the system. The Flash memory is 32-bits wide, permitting efficient execution of system and application software directly from Flash. The Flash module replaces one of the EPROMS; single EPROM is supported.

Expansion capability is provided to OEMs through the use of compatible S1 connector, which supports 32-bit data width, interrupts and DMA. This connector provides a true upgrade path from the SBC3, through the SBC4 to the SBC5.

System configuration information may be stored in the local four kbit EEPROM. Access to the configuration EEPROM is via general purpose I/O lines on the DUART, so no system address space is consumed. Simple read-write drivers are provided in GreenSpring's pROBE jr. monitor and also under OS-9.

VMEbus interface logic is provided in our own proven GreenSpring Logic-cell Array (GLA™). This 100-pin surface mount application specific device implements full VMEbus functionality more economically than older ceramic PGA packages.

Specifications

Processor	MC68EC030-40 (no MMU) MC68030-33 (with MMU) opt.
Co-Processor	MC68882 optional
Clock Speed	32 Mhz
RAM	One or four megabytes page-mode dual-access DRAM
Wait States	Three for first RAM access, one for subsequent burst cache fill
EPROM	Up to one Mbyte (Two MB supported on special order), 32-pin and 28-pin devices are supported
Flash EPROM	One MB module 32-bit wide 100,000 update cycles.
EEPROM	Serial 4 kbit EEPROM accessed via DUART digital I/O lines
Serial I/O	Two ports, RS-232D
Counter/Timer	One, 16-bits. Interrupts.
Clock/Calendar	DS-1216F standard
VMEbus	VMEbus C.1 Master and slave D16, D08(OE), D32 with S1 expansion board, A24, A16(master only) interrupt Handler (all levels), mailbox interrupt System controller: TIMEOUT RESET Four level arbiter SYSCLK Digital BBSY filter
VMEbus Interface	GreenSpring Logic-cell array
Compatibility	Address Map, Serial I/O, GLA and S1 compatible with SBC3 and SBC4
Expansion Connector	96-pin DIN provides expansion to support VMEbus D32 capability. Direct access to the 68030 bus permits memory access, I/O, interrupts, DMA, and VMEbusmaster/slave.
Front Panel Switches	Reset, Abort
Front Panel LEDs	Run, External
Power Requirements	+ 5 VDC, 1.5 A, typical, 1.8A max –12 VDC, 6 mA typical +12 VDC, 15 mA typical, 125mA max during Flash Programming

SBC5-32-FLASH

Robert Jr.

Order Information

SBC5-32
SBC5 processor with 1 MB DRAM, Clock/ Calendar, two EPROM sockets, 3U front panel

Option-4MB
Upgrade to 4 MB RAM

Option-Flash
Adds 1 MB Flash EPROM module

Option-MMU
MC68030-33 upgrade includes internal MMU

Option-I
In-plane connector (RA) installed in S1 for single 6U combination

Option-XC
Delete Clock/Calendar

The above items can be ordered in any combination except, -I and -P which are mutually exclusive

Eng-Kit-SBC5
Includes serial cable, null modem, and documentation. Order Engineering Kit with first purchase.

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