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MP Series



AC/DC

5 to 7 Watts

Triple Outputs

- **High performance design**
- **Ultra-low noise**
- **Continuous short circuit protection**

Specifications

INPUT

Voltage and Frequency

Standard	105 to 125 Vac - 50 to 440 Hz
Suffix I	200 to 252 Vac - 50 to 60 Hz
Suffix N	90 to 110 Vac - 50 to 60 Hz
Suffix K	200 to 252 Vac - 50 to 60 Hz
Suffix K2	105 to 125/210 to 250 Vac

OUTPUT

Voltage Tolerance	± 1%
Ripple and Noise (PARD)	1mV RMS
Regulation	
Line	0.01%
Load	0.2%
Short Circuit Protection	Current Limiting
Temperature Coefficient	0.02% / °C

GENERAL

I/O Isolation	1500 Vac
Suffix I	2500 Vac

ENVIRONMENTAL

Operating Temperature	-25°C to +71°C No Derating
Storage Temperature	-25°C to +85°C
Cooling	Free-air Convection

All specifications are typical at nominal line and full load at 25°C unless otherwise noted and are subject to change without notice.

The MP Series boast over a decade of reliable, field proven service and are the recognized industry standard for high performance AC/DC power supplies.

These economy priced triple output supplies are ideal for those applications where space (PC Board real estate) is limited. Three outputs are available in the area normally occupied by most single or dual output encapsulated modules.

Features include MTBF's of greater than 150,000 hours, lower case temperature rise (to 18°C cooler) and the high in-circuit performance. This higher efficiency results in lower ambient temperatures and greater system reliability.

MP Series Ordering Information

Output Voltage	Output Current	Case	Model
5 Vdc / ±12Vdc	300 mA / ±150mA	B	MP34-060
5 Vdc / ±12Vdc	500 mA / ±100mA	B	MP34-070
5 Vdc / ±15Vdc	300 mA / ±150mA	B	MP35-060
5 Vdc / ±15Vdc	500 mA / ±100mA	B	MP35-070
5 Vdc / ±15Vdc	750 mA / ±100mA	C	MP35-095

Dimensions and Connections

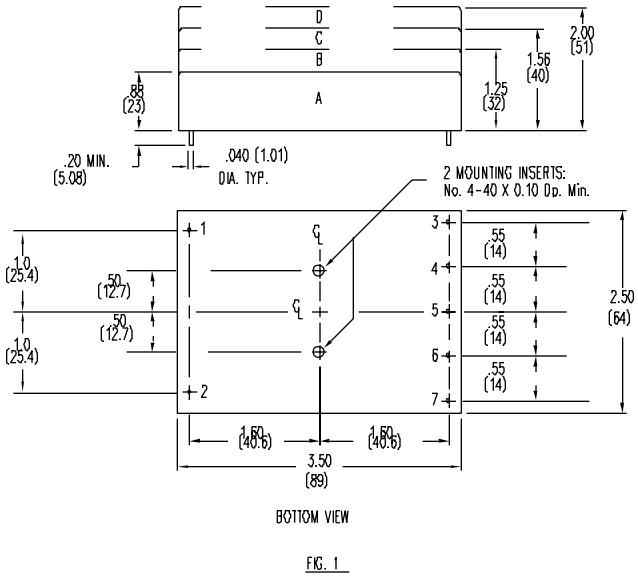


FIG. 1

PIN CONNECTIONS

1. VAC in high
2. VAC in neutral
3. Common*
4. + 5 Output
5. -12 or 15 Output
6. Common*
7. +12 or 15 Outut

* Pins 3 and 6 are Internally Connected

NOTES:
1. Ripple measured with a 3.3 mf tantalum capacitor across each output.

11/01/2001

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