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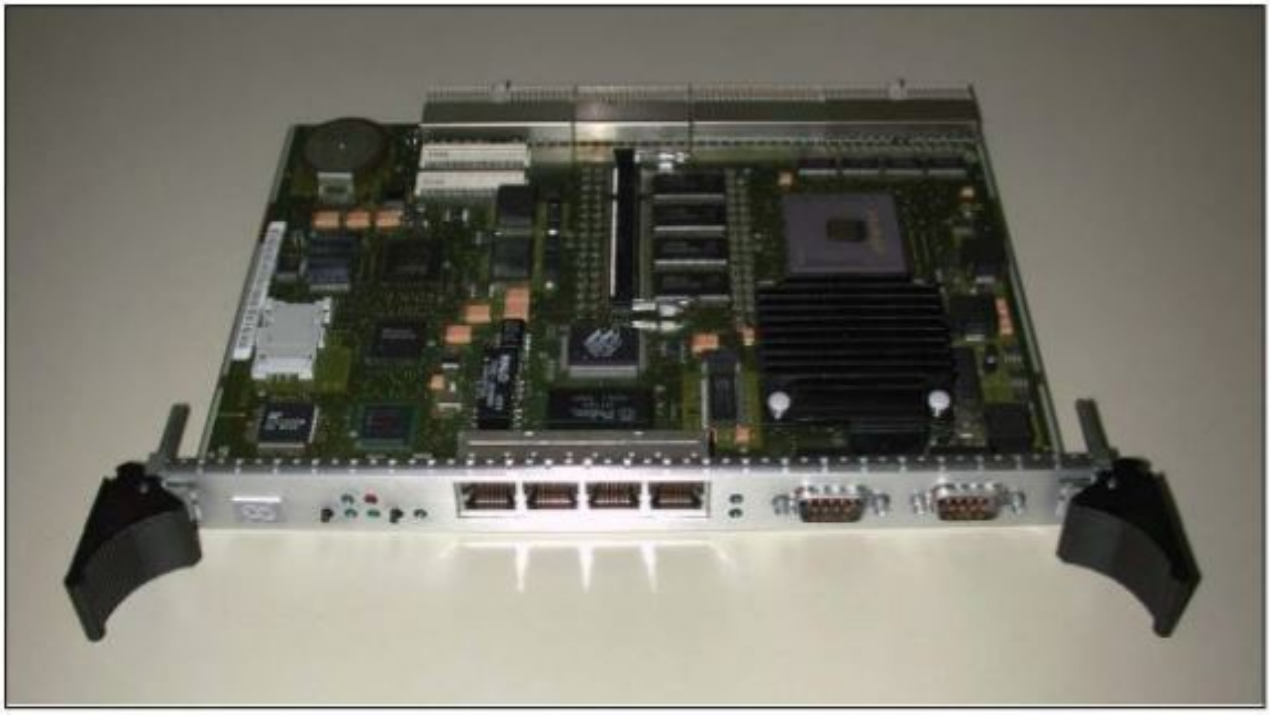
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1.2.3 Control board DSCXL

The DSCXL (**D**ata and **S**witch Processor for **C**ompactPCI/**LAN**) is the central processor board. The board controls ADP processing (including controlling the CompactPCI buses in the back-plane) and is responsible for fundamental system control functions.

The board is hot swapable !!!
The Harddisk must be deactivated by AMO: DEACTIVATE-DSSM:UNIT=A1, CNO=1 (6); , if the ADP is to be unplugged.



Pentium III Processor

- 800 MHz Frequency.
  133 MHz System bus Frequency.
  512-Kbyte cache (on-die, full speed level two (L2) cache)
  Micro-FCBGA technology.
  Quad Quadword Wide (256-bit) cache data bus.
  8-way cache associativity
  Max. 9.84 A power dissipation @ 1.15 V core voltage.
  Max. Junction Temperature Tj=100°C.

Hardware Version

- DSCXL:S30810-Q2311-X-*

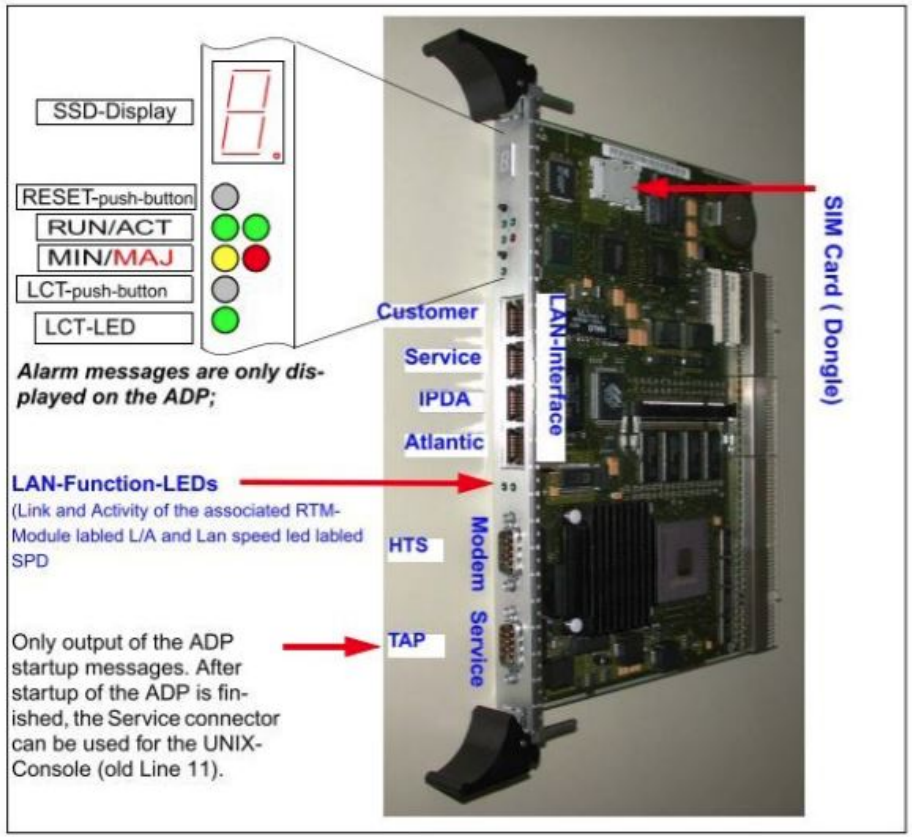
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LED and SSD Displays/Keys

The front side of the board features one SSD display, two keys, five LEDs for DSCXL status and two LEDs for the LAN interface to the RTM board with the following properties:

DSCXL displays	Properties
SSD display	Indicates the current status of the DSCXL
RUN LED green	CPU active
ACTIVE LED green	System status
LCT LED green	Loading from MO active
Minor Alarm LED yellow	Alarm LED
Major Alarm LED red	Alarm LED
Keys	
Reset	Manual reset for DSCXL
LCT	Starts loading from the MO instead of the HD

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DSCXL board front panel

Reset/LCT key combinations:

You can start loading from the MO disk with the Reset and LCT keys.

- **Loading from MO disk:**
  Press the LCT key briefly followed by the Reset key.

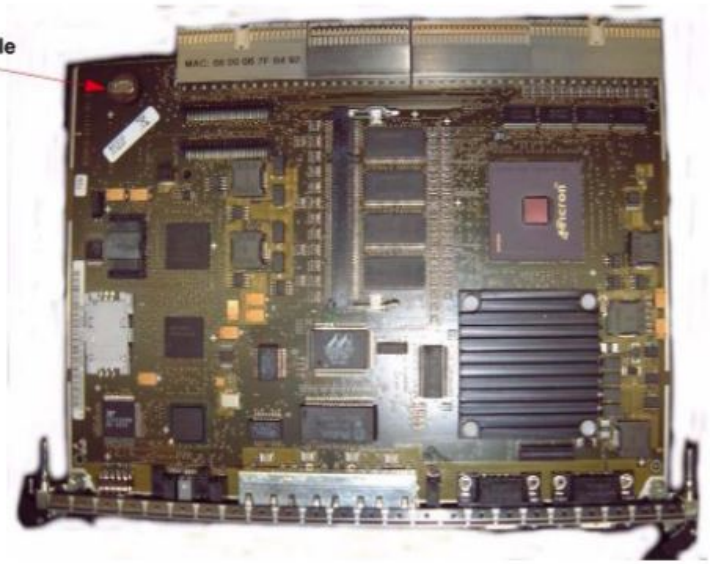
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LAN displays	LED on	LED off	LED flashing
LINK/ACT green	Connection	No connec-tion	Transferring or receiving sig-nal
SPEED green	100 Mbps	10 Mbps	

LED-displays for Lan interface to the RTM board

The LEDs for the LAN interfaces on the front panel are integrated in the RJ45 sockets.

Jumper settings and rechargeable battery



No jumper settings are necessary !

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Interfaces

- Standard CompactPCI interface (C39334-Z7086-*) for the backplane
- 4 x RJ45 connectors for LAN interfaces
- 2 x V.24 9-pin SUB-D connector (service terminal/modem)

Standard CompactPCI interface

The DSCXL board can be implemented in all compatible CompactPCI 2.16 backplanes. Once implemented, the DSCXL takes over the control of the CompactPCI buses in the system slot. If the DSCXL is inserted in a peripheral slot, it cannot be connected to the CompactPCI bus. The special HiPath features can only be provided in connection with the HiPath backplane.

LAN access ports

- ***"Customer":***
  - Access port via UW7 to the ADP
  - Standard-IP-address: **192.1.2.5**
- ***"Service":***
  - Access port to the ADP (**192.0.2.3**) and UW7(Assistant) (**192.0.2.5**) via the Atlantic-LAN
- ***"Atlantic":***
  - Access port to the ADP, CC-A and CC-B via the Atlantic-LAN (**192.0.2.x**)
- ***"IPDA":***
  - Access Point (AP) - Signaling connection
  - IP adresses are set by the AMO SIPCO

Attention:

The LAN access ports "SERVICE" and "ATLANTIC" are blocked on the DSCXL board, if the board is used as a CC (Common Control) !!!

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V.24 interfaces

These interfaces are provided for connecting terminals and modems (service/HTS).

PIN No.	Signal	I/O	Remark
1	DCD	I	
2	RXD	I	
3	TXD	O	
4	DTR	O	
5	0 V		Ground
6	DSR	I	
7	RTS	O	
8	CTS	I	
9	RI	I	

Power Supply

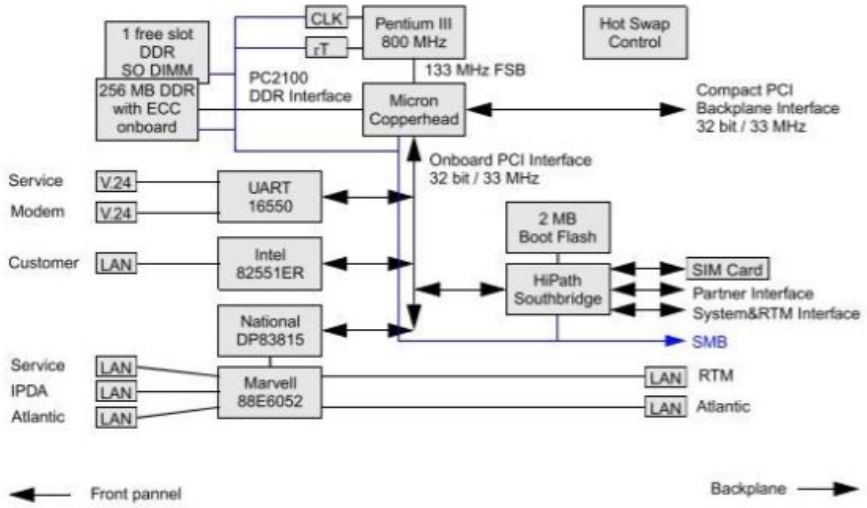
The board can plugged in and unplugged during live operation (hot swapable). The power supply voltages (+12 V, +5 V and +3.3 V) are supplied through the backplane. DC/DC converter generates the individual power supply voltages needed for the various circuits on the DSCXL board.

Temperature Monitoring

The processor board and the processor itself are monitored using temperature sensors. If one of the two operating temperatures is exceeded, the speed of the fans in the CompactPCI shelf is increased to regulate the operating temperature.

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Block diagram



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