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Find the *Siemens MM3* at our website: *Click HERE*

MM3 communications buffer module

Overview

The MM3 module makes 2 x 64 Kbyte buffer memories available to exchange data between CPU modules

The module contains a clock unit which can be optionally synchronized by keys and by receiving the DCF77 time signal (radio-based clock)

This clock can be used as source for all clock function blocks of the CPU modules in the subrack ("system time", max. jitter between the individual modules: 3 ms).

Application

Applications can be found in:

- SIMADYN systems with more that one CPU
- SIMADYN systems which require clock time

Function

- With radio-based clock
- L and C bus connection
- 64 Kbyte RAM for L and C bus respectively
- Real-time clock; the time is buffered using the battery in the subrack

  - the time can be set via keys

  - the time can be received (DCF77)
- 10 digit, 7-segment display to display the time: Month, day, hour, minute, seconds (years can be alternatively displayed as a 4-digit number)
- 4 LEDs to display the status of the time receiver
- Digital output (relay) to output a system error (program interrupted/stop status of a CPU module)

Technical specifications

Technical specifications	
Current consumption	+5 V/600 mA (max.) +15 V/50 mA (max.) +3.4 V/0.02 mA (max.)
Space requirement	1 slot (20.32 mm)
Weight	0.5 kg

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