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TMDSEMUPROTRACE

XDS560v2 PRO TRACE Receiver & Debug Probe

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Overview

Description & features | Supported products







Important: The XDS560v2 PRO TRACE Receiver is a legacy product that supports pin trace for TI processors. For new devices it is recommended to use a third party trace receiver.

The XDS560v2 PRO TRACE Receiver supports the same features as the XDS560v2 System Trace family (USB version (https://www.ti.com/tool/TMDSEMU560V2STM-U), USB & Ethernet version (https://www.ti.com/tool/TMDSEMU560V2STM-UE)) and adds support for core pin trace (instruction and data) in its large external memory buffer. Available for selected TI devices, this external memory buffer captures all instructions that are executed by a processor or core to allow analyzing hard-to-find issues in an embedded system. Also, all XDS debug probes support Core and System Trace in all Arm® and DSP processors that feature an Embedded Trace Buffer (ETB).

The XDS560v2 PRO TRACE Receiver connects to the target board via either a MIPI HSPT 60-pin or a TI 60-pin connector (with a supplied adapter) and to the host PC via either USB2.0 High speed (480Mbps) or Ethernet 10/100Mbps. It also requires a license of Code Composer Studio™ IDE running on the host PC.

The XDS560v2 PRO TRACE Receiver comes in a package consisting of:

- Trace capture box, which contains the processing/memory/interface unit.
- Target cable and Trace JTAG header, which contains a signal buffer unit.
- Setup guide.
- MIPI HSPT 60-pin to TI 60-pin converter adapter.
- 12V/5.0A power supply.
- USB 2.0 cable.
- Ethernet CAT5 cable.
- IEC 60320 C13 Power cables for United States (NEMA 5/15), most of Europe (CEE7/7) and United Kingdom (BS1363).

Trace Features:

- DSP core pin trace is available for 66AK2x, C66x, C645x and C647x devices.
- Arm core pin trace (ETM) is available for AM437x, AM64x, 66AK2x, RM46x, RM57x, TMDS570LS31x/21x and TMS570LC43x devices.
- System pin trace is available for devices that are system trace enabled such as AM335x, AM437x, 66AK2x, C66x, DM81x, AM38x.

You will need:

- An installation of Code Composer Studio™ v4 through v12, newer versions are not supported.
- A target board that sports one of the compatible JTAG headers (MIPI HSPT 60-pin or TI 60-pin).

You may need:

- An adapter to allow connecting to target boards that features different JTAG headers.

Shipping information:

- Product box dimensions: 510mm x 310mm x 160mm (20in x 12in x 6.0in).
- Packaged product weight: 2.5kg (5.5lb).

Features

XDS560v2 is the latest variant of the XDS560 family of high-performance debug probes (emulators) for TI processors. With the fastest speeds and most features of the entire XDS family, XDS560v2 is the most comprehensive solution to debug TI microcontrollers, processors and wireless connectivity microcontrollers.

The XDS560v2 is the first of the XDS560 family of debug probes to provide System Trace (STM) capability, a type of Trace that monitors the entire device by capturing system events such as the state of processing cores, internal buses and peripherals. Most XDS560v2 models feature also System Pin Trace, a mode where the System Trace data is sent to an external memory buffer (128MB) inside the XDS560v2 to allow capturing larger number of system events. The System Pin Trace data connection requires additional wiring to the JTAG connector.

The XDS560v2 PRO TRACE is the second generation of the XDS560 family of debug probes to feature Core Pin Trace (Instruction and Data), a type of Trace that captures all instructions executed by a core and sends them to an external memory buffer (1GB) inside the XDS560v2 PRO TRACE. The Core Pin Trace does not intrude in the real-time behaviour of the system and allows capturing a larger number of instructions. The Core Pin Trace data connection requires additional wiring to the JTAG connector.

To accommodate for all types of Pin Trace (Instruction and System), all XDS560v2 variants feature a standard 60-pin MIPI HSPT connector as the primary JTAG connectivity to the target. In addition to that, all variants also feature modular target adapters for TI and ARM standard JTAG connectors (the offer of adapters varies per model).

The XDS560v2 supports the traditional IEEE1149.1 (JTAG) emulation as well as IEEE1149.7 (cJTAG) and operates with JTAG interface levels from +1.2V to +4.1V.

Compact JTAG (cJTAG) is a major improvement over the traditional JTAG, as it supports all its features while using only two pins, and is available in selected TI wireless connectivity microcontrollers.

All XDS560v2 models support either USB2.0 High speed (480Mbps) or Ethernet 10/100Mbps, with some models supporting both. Also, some models support PoE (Power over Ethernet) for added flexibility.

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