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unEVErsity Connections Program

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*THE **FASTEST** VERIFICATION*

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ZeBu-UF unEVErsity Bundle

ZeBu-UF-UB	Virtex4 LX200	Memory (DDR2 DRAM+SRAM)	Capacity in ASIC Gates
ZeBu-UF1-UB	1	128MB+16MB	1,500,000
ZeBu-UF2-UB	2	128MB+32MB	3,000,000
ZeBu-UF4-UB	4	512MB+64MB	6,000,000
ZeBu-MMB-UB	ZeBu MultiMedia Board for MultiMedia ICE applications		
ZeBu-UF4-ICE-UB	ZeBu-UF4 DirectICE module for ICE applications		

- The ZeBu-UF University Bundle includes:
  - One ZeBu-UF board to be chosen among UF1, UF2, UF4, including one ZeBu compiler license and one ZeBu run-time license with hardware debug capabilities
  - One zFAST fast synthesis 5-year TBL license, supporting VHDL/Verilog, including ZW-FPGA Foundation Library
  - One zTIDE simulation environment 5-year TBL license for developing synthesizable transactor
  - One ZEMI-3 custom transactor compiler 5-year TBL license and run time 5-year TBL license
  - Five Validation IP (to be chose among Memory Models and Transactors)
  - Software support as described in Slide 7
- Additional Validation IP (either Memory Models or Transactors) can be purchased
- Any ZeBu-UF can be equipped with one MultiMedia Board
- The ZeBu-UF4 can be equipped with the DirectICE module for in-circuit-emulation mode applications
  - ZeBu-UF1 and UF2 include the DirectICE connectors

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ZeBu-Personal unEVErsity Bundle

ZeBu-Personal	Virtex5 LX330	Memory (DDR2 RAM + SSRAM)	Design Capacity [ASIC Gates]
ZeBu-Personal-UF	2	2Gbits + 512Mbits	5,000,000
ZeBu-MMB-UB	ZeBu MultiMedia Board for MultiMedia ICE applications		

- The ZeBu-Personal University Bundle includes:
  - One ZeBu-Personal board, including one ZeBu compiler license and one ZeBu run-time license with hardware debug capabilities
  - One zFAST fast synthesis 5-year TBL license, supporting VHDL/Verilog, including ZW-FPGA Foundation Library
  - One zTIDE simulation environment 5-year TBL license for developing synthesizable transactor
  - One ZEMI-3 custom transactor compiler 5-year TBL license and run time 5-year TBL license
  - Five Validation IP (to be chosen among Memory Models and Transactors)
  - Software support as described in Slide 7
- Additional Validation IP (either Memory Models or Transactors) can be purchased
- ZeBu-Personal can be equipped with one MultiMedia Board
- ZeBu-Personal includes the DirectICE connectors

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ZeBu-XL unEVErsity Bundle

ZeBu-XL-UB	Virtex2-8000	Memory (DRAM)	Capacity in ASIC Gates
ZeBu-XL8-UB	8	1.5GB	6,000,000
ZeBu-XL16-UB	16	1.5GB	12,000,000

- The ZeBu-XL University Bundle includes:
  - One ZeBu-XL system to be chosen between XL8 and XL16, including one ZeBu compiler license and one ZeBu run-time license with hardware debug capabilities
  - One zFAST fast synthesis 5-year TBL license, supporting VHDL/Verilog, including ZW-FPGA Foundation Library
  - One zTIDE simulation environment 5-year TBL license for developing synthesizable transactor
  - One ZEMI-3 custom transactor compiler 5-year TBL license and run time 5-year TBL license
  - Five Validation IP 5 (to be chosen among Memory Models and Transactors)
  - Software support as described in Slide 7
- Additional Validation IP (either Memory Models or Transactors) can be purchased

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ZeBu-XXL unEVErsity Bundle

ZeBu-XXL Systems	Virtex4 LX200	Capacity in ASIC Gates	DirectICE
ZeBu-XXL8-UB System	8	12,000,000	No
ZeBu-XXL16-UB System	16	24,000,000	No
ZeBu-XXL24-UB System	24	36,000,000	No
ZeBu-XXL32-UB System	32	48,000,000	No
ZeBu-XXL40-UB System	40	60,000,000	No
ZeBu-XXL48-UB System	48	72,000,000	No
ZeBu-XXL56-UB System	56	84,000,000	No
ZeBu-XXL64-UB System	64	96,000,000	No

- A DirectICE Module trades off one 8xFPGA Module
  - A DirectICE Module reduces the overall design capacity of the system by 12,000,000 ASIC gates

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ZeBu-XXL unEVErsity Bundle

- The ZeBu-XXL University Bundle includes:
  - One ZeBu-XXL system (see next page), including one ZeBu compiler license and one ZeBu run-time license with hardware debug capabilities
  - One zFAST fast synthesis 5-year TBL license, supporting VHDL/Verilog, including ZW-FPGA Foundation Library
  - One zTIDE simulation environment 5-year TBL license for developing synthesizable transactor
  - One ZEMI-3 custom transactor compiler 5-year TBL license and run time 5-year TBL license
  - Five Validation IP (to be chosen among Memory Models and Transactors)
  - Software support as described in Slide 7
- Additional Validation IP (either Memory Models or Transactors) can be purchased

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Support for unEVErsity Offerings

Software Support

Software support includes bug fixes, software updates when and if available and technical support directly from the factory support@eve-team.com.

Support does not include hardware replacement or repair.

University may elect to have hardware repaired (EVE will provide a specific quote) or elect to purchase new hardware (EVE will provide new quote).

For support issues, EVE will only accept communication from only one professor or one professor appointed technical liaison from the university.

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unEVErsity Connections Program - EUCP

- Rules for Engagement
  - Can only be used for:
    - Course work or teaching in connection with a university-sponsored class at an accredited college or university
    - Academic research either sponsored by or conducted by an accredited college or university.
  - Can not be used for:
    - Profit research or commercial projects
    - Benchmarking
    - Software development
  - One university appointed individual (professor or technical liaison) is allowed to interact with Eve for maintenance issues including bug fixes, technical support direct from support@eve-team.com, and software upgrades
    - Technical Liaison responsibilities are:
      - To serve as the point of contact between University and Eve
      - To coordinate installation and maintenance
      - Administer license obligations

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unEVErsity Connections Program - EUCP

- **Rules of Engagement Continued**
  - **University will**
    - **Consent to Eve's use of the University name and logo to identify University as a partner of Eve**
    - **Reference Eve in all publications (thesis's, conference papers, technical papers, contributed articles, research projects, talks, etc...) and will allow Eve to review (if requested) before publication.**
    - **Provide Eve with (if requested) and allow Eve to post referenced publications (point above) on Eve's University Connections web page.**
    - **Agree to take reasonable precautions to prevent public use of Eve's software**

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