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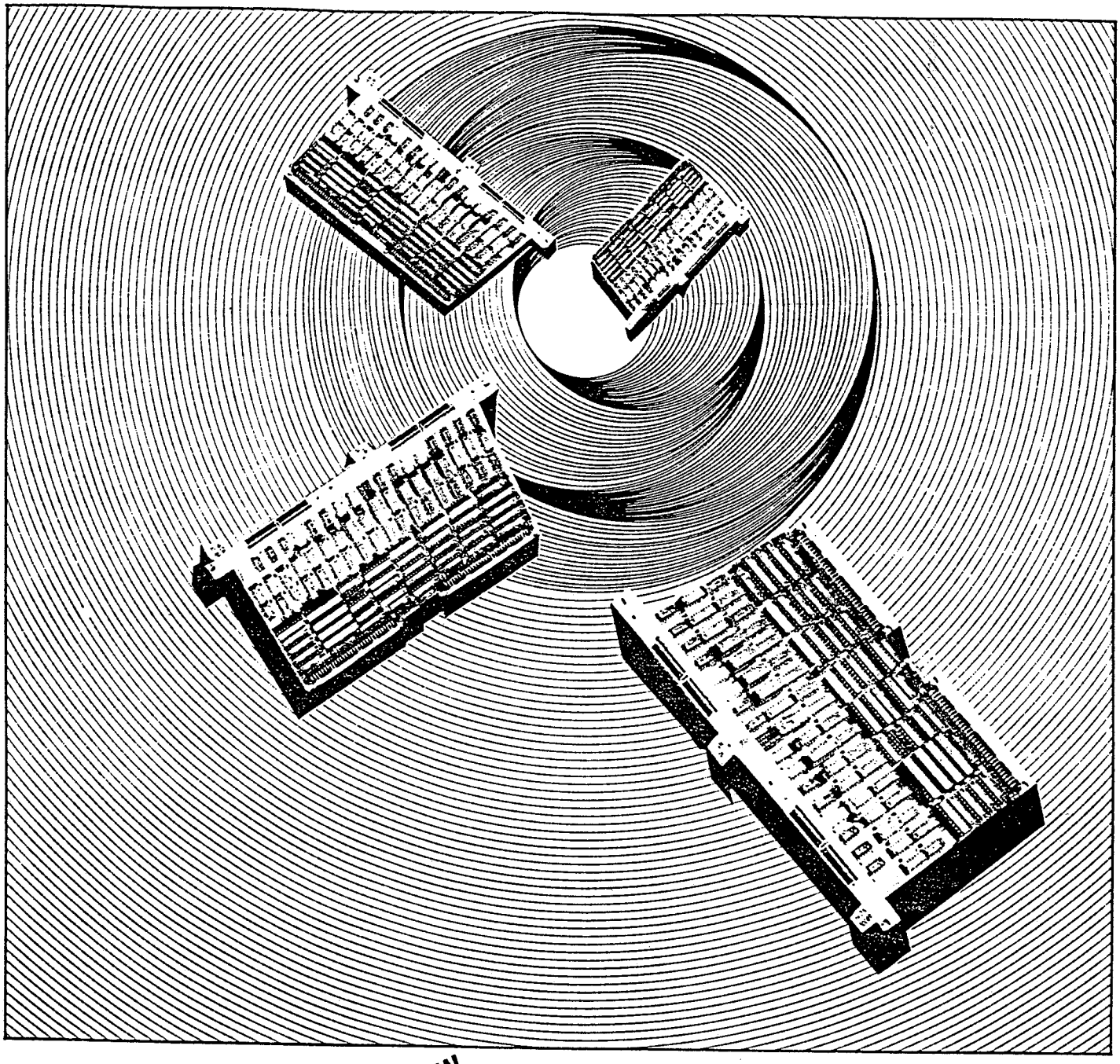
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Find the *Tahoma / IKON 10089* at our website: *Click HERE*



THE NEW VMEbus CONNECTION

The next generation of high performance, parallel, DMA interface for the VMEbus is here today at IKON. FIFO buffering increases throughput for the new Model 10089 by a factor of two over previous designs while preserving compatibility with them.

The IKON Model 10089 is an emulation of the widely accepted DEC* standard DR11-W interface transposed to the VMEbus form factor with high speed FIFO buffering and A32/D32 on the VMEbus.

The Model 10089 is being used both as a general purpose peripheral interface and as an interprocessor link to DEC* or other CPUs via DR11-W or other emulations such as IKON's units for the Multibus** I and VERSAbus*** and increases throughput by a factor of two over previous designs.

Want to discuss possible applications or need additional data? Write or call today.

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**Multibus is a registered trademark of Intel Corporation.
***VERSAbus is a registered trademark of Motorola, Inc.

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IKON MODEL 10089
High-Performance VMEbus/DR11-W Emulator Specifications

Conforms to VMEbus Specification Revision C

Slave Mode Access
- 16-bit I/O map or 24-bit standard map with switch-selectable address.
- PAL-selectable address modifier decode. Standard factory PAL responds to hex modifier codes of 29 and 2D in the I/O map and 39, 3A, 3D and 3E in the standard map.

Interrupts
- Jumper-selected interrupt level with software-selected vector.

DMA
- 32-bit linear address generation with 16- or 24-bit range (word) counter for up to 32M byte DMA blocks anywhere in the full VME address space.
- DMA logic is all bipolar, with 20 MHz clock for maximum speed and bus efficiency.
- Jumper-selectable bus priority.
- Multiple word or longword per arbitration in output mode, single word per arbitration in input mode, with early BBSY* release in either mode — allowing concurrent bus arbitration and maximum bus access by other devices.
- Transfer rates of up to 3.3M bytes/second for input and 6.6M bytes/second for output.

External Interface
- All DR11-W I/O modes supported, except control of bus address and range counter incrementation, byte DMA, and change of data direction within DMA block.
- Input de-skew and output settling times jumper-selectable.
- I/O connectors and pin-out identical to DEC* format.
- All input and output signals terminated and driven/received with high-hysteresis unified-bus-type open collector transceivers and receivers.
- Optional byte swapping to and from the external device with separate switches for Programmed I/O and DMA.
- Cycle request (CRQA & CRQB) and BUSY polarities jumper-selectable.

Power and Environmental
Power consumption — 3.7A of 5VDC
Commercial temperature range — 0°C - 70°C
Humidity tolerance to 90%, non-condensing

Mechanical
Double-high (6U) VME bus form factor.
Multi-layer construction

Delivery
Generally from stock — maximum of 30 days for individual orders of up to 25 units.

Possible Applications

Interprocessor links to:
MicroVAX*, PDP*-11, VAX*
Data General
Perkin-Elmer
Harris
Gould/SEL
PRIME
Mainframes via Mitek or FASTPATH**

Networking with:
Bridge Communications
GENet
Ungermann-Bass NetOne

Other Uses:
Zycad Logic Evaluators
Numerix Array Processors
Optivision/Canoga Fiber Optics Modems
Phoenix Data A-D/D-A
Aydin Workstation

Graphics/Image Processing:
Raster Technologies
Metheus
Chromatics
Megatek
Ramtek
International Imaging Systems
GE Graphicon
and others

OTHER IKON PRODUCTS

DR11-W Emulations
10077 Multibus**
10087 Hi-Perf. Multibus
10083 VERSAbus****
10084 VMEbus
10094 PC/AT

Hardcopy Interfaces
10071-5 Multibus Versatec
10085 Multibus
10088 VME bus
10092 PC/AT
} Versatec & Centronics

HYPERchannel*Interfaces**
10077-NSC Multibus
10091 Hi-Perf. Multibus
10090 VMEbus
10096 PC/AT

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