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The BE-64 is a C size VXI card which houses the digital resource required in digital test and troubleshooting applications.

BE-64 FEATURES

- SINGLE C SIZE MESSAGE BASED VXI MODULE.
- SCPI COMMAND SET
- 64 I/O CHANNELS X 32 K BITS / CHANNEL
  - 32K I/O memory subdivided into multiple data tables
  - Data rates from DC to 25 Mhz
- 16 TIMING SETS
  - Each timing set is defined by a 256 bit x 24 bit memory
  - Timing sets are docked by a 50, 20 or 10 Mhz (20, 50 or 100 ns resolution) or external dock (50 MHz max.)
  - Each timing set includes the following:
    - 8 user defined output signals - TSOUT1 thru TSOUT8
    - 3 test inputs (handshake) signals (2 level and 1 edge sensitive input)
    - Field 1 and Field 2 control signals
    - Programmable cell delay (from 1 to 64K clocks)
    - Programmable timeout (from 1 to 32K clocks)
    - 3 Programmable sync signals
- SEQUENCE DEFINITION
  - Programmable idle sequence
  - Program from 1 to 16 sequences where each sequence . . .
  - Selects one of the sixteen timing sets
  - Selects a data table
  - Selects a loop count from 1 to 64K (or continuous)
  - Zero time delay between timing sets, data tables and sequences
  - Single cycle (all sequences) or continuous
- 24 PROGRAMMABLE SET / SENSE SIGNALS
- MACRO COMMANDS
  - UUT RAM and ROM Test
  - Learn CRC's from input data tables & serial data channels
  - Compare response tables to expect tables
  - Algorithmic fill and copy (data tables and timing sets)

WORD GENERATOR

The word generator mode of the Model BE-64 incorporates the following features.64 I/O channels x 32K bits/channel, 25 Mhz data rates, Tri state output control 32 I/O channels x 64K bits/channel, 50 Mhz data rates. The 32K memories are software allocated into tables. Up to 32K tables may be defined. Table length is programmed from 1 word to 32K words. Each table may be looped from 1 to 64K times, or run in continuous mode. Up to 16 tables can be hardware linked together, generating zero dead time between tables. Continuous operation of an "Idle" table which can run while downloading new data. Each table can be input or output.

LOGIC RECORDER

The BE-64 can be programmed to record up to 64 channels by 32K/channel.

STIMULUS/RESPONSE TESTING

For test applications where test vectors are available, the Model BE-64 can be programmed to transmit or receive 64 channels by 32K bits per channel at frequencies to 25 Mhz.

BUS STRUCTURED INTERFACE SIMULATION

Bus structured interfaces are simulated exactly like the bus structured interface of a microprocessor. The architecture of the BE-64 has enabled Talon customers to simulate well over 1000 different digital interfaces. These interfaces range from a serial T2 communication interface to a high speed VME bus structured interface. Talon offers "set up" configurations for the following bus structured interfaces.

VME bus, VXI bus, Multibus, AT bus, NuBus, SCSI bus, Microchannel, EISA bus, S bus

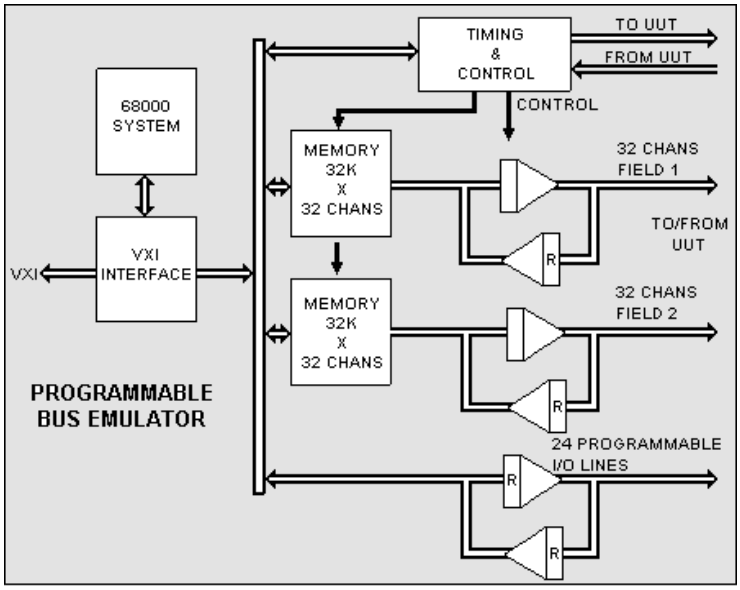
NON BUS STRUCTURED INTERFACE SIMULATION

The Model BE-64 word generator model allows the simulation of almost any user defined digital interface.

MICROPROCESSOR BUS EMULATION

The Model BE-64 can be programmed to simulate the bus structured interface of any microprocessor incorporating an address and data bus, each bus not exceeding 32 bits in width. All timing and control signals are simulated, generating bus cycles to 50 Mhz.

BLOCK DIAGRAM/SPECIFICATIONS



BE-64 Block Diagram

- 64 I/O Chans x 32K bits/chan (maximum data rate - 25 Mhz)
- 12 Output Programmable Timing and Control Signals; maximum output rate = 50 Mhz
- 3 Input Programmable Timing and Control Signals; maximum input rate - 50 Mhz
- 6 Input External Field Control Signals; maximum input rate - 25 Mhz
- 24 Programmed I/O Signals; data rate defined by transfer rate of controller.
- 30 miscellaneous timing and control signals required for exact simulation of various microprocessors and compatible signals for parallel to serial word generator converter card.
- 1 Clock Out; frequency = 50 Mhz, 20 Mhz, or 10 Mhz
- 1 Clock Input; maximum frequency = 50 Mhz
- All signals TTL compatible.

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