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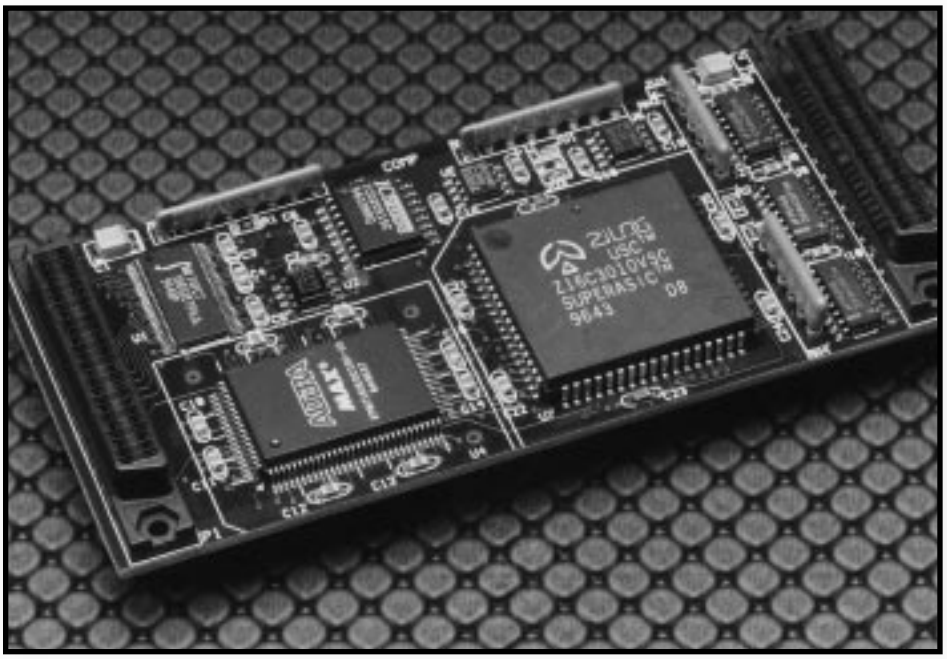
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Find the *Tek Microsystems FP-Serial2-422* at our website: ***Click HERE***

FastPacks FP-Serial2



When you need the highest performance serial I/O on the market, TEK's FP-Serial2 goes far beyond the basic communication offered by other IP modules—giving you the added performance and flexibility required by even the most demanding applications.

With flexible interrupt and DMA support, the FP-Serial2 easily handles message-based protocols at high data rates without overloading the host processor. Direct access to all Z16C30 registers and write posting ensures efficient IP bus performance, and the FastPack SmartSerial controller automatically adapts to 8 or 32 MHz operation for optimum performance in all configurations.

A dedicated clock extraction circuit on each serial channel extracts maximum performance from the Z16C30, extending the asynchronous data rate to 2.5 Mbps. And with a PLL-based clock synthesizer for each channel, it's easy to generate exact baud rates even at the highest operating frequencies.

Like all TEK FastPacks, the FP-Serial2 comes with unlimited technical support, ANSI C software drivers and demonstration code, a three-year limited warranty, and a 30-day satisfaction guarantee.

Feature	Benefit
Built around Zilog Z16C30	Supports most common protocols, including asynchronous, SDLC/HDLC, Bisync, and others. 32-byte RX and TX FIFOs minimize latency requirements and improve performance.
Fully compliant with ANSI/VITA 4-1995 and IndustryPack specifications	Compatible with all IP carrier boards
PLL-based clock synthesizer for each channel	Supports exact generation of arbitrary baud rates, even at high frequencies
EPLD-based clock extraction circuit	Extends maximum asynchronous data rate from 625 Kbps to 2.5 Mbps
Turnkey software drivers for Motorola MVME162 and MVME172 carrier boards	Drop-in solution

TEK Microsystems

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Specifications

Interface
- ANSI/VITA 4-1995 and IndustryPack compatible

Clock Speed
- 8 or 32 MHz, auto-sensing

Size
- Single-size, Type I (1.8" x 3.9" x 0.314" max)

Address Spaces
- ID (8-bit data)
- I/O (16-bit data)

Number of Channels
- Two

Serial Data Rate
- 10 Mbps with external clock
- 2.5 Mbps without external clock

Interrupt Sources
- Autovectored, one source per channel

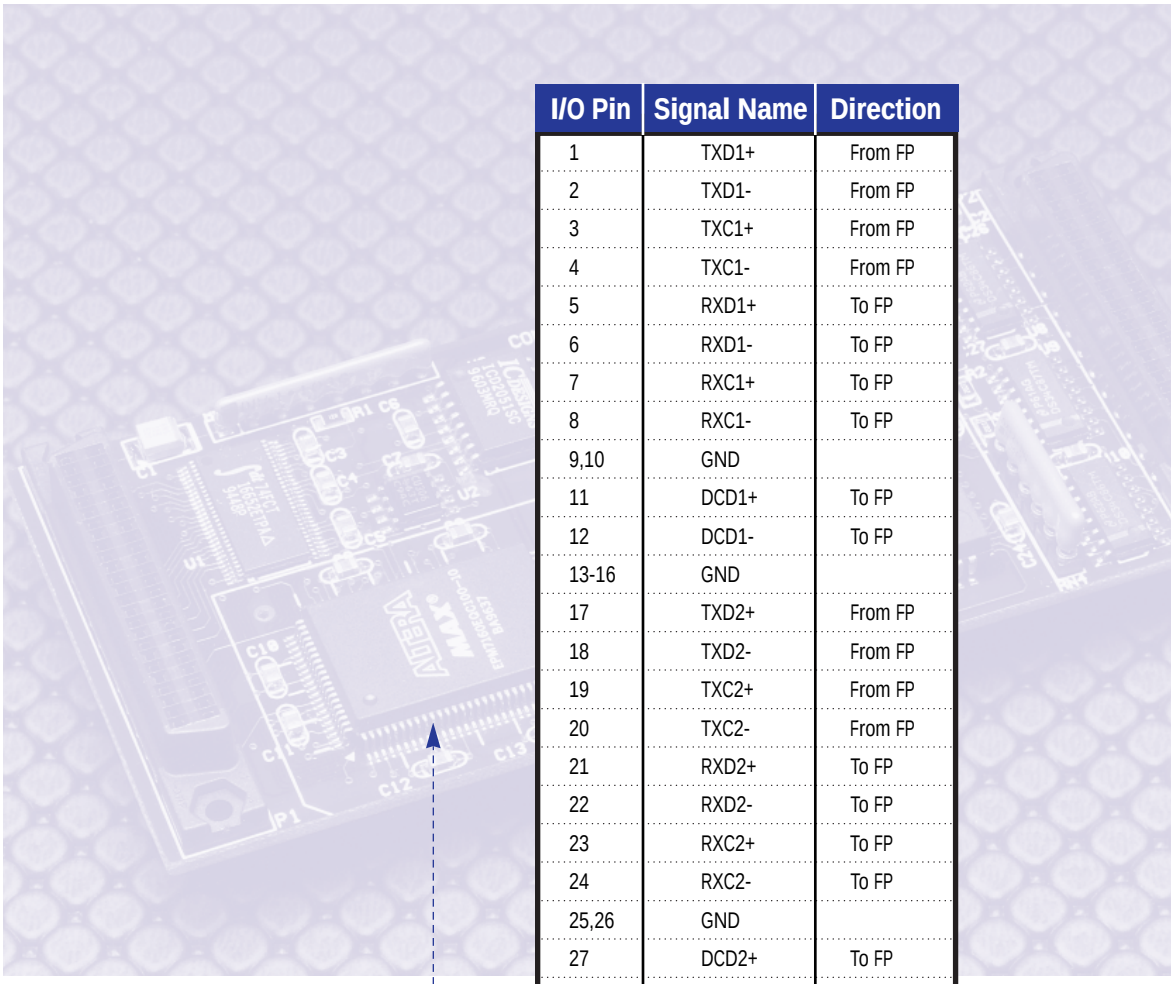
DMA Sources
- Channel A RX/TX
- Channel B RX/TX
- Channel A and B RX
- Channel A and B TX

Power Requirements
- +5 Volts, 375 mA

Operating Temperature
- 0 to +70 degrees C

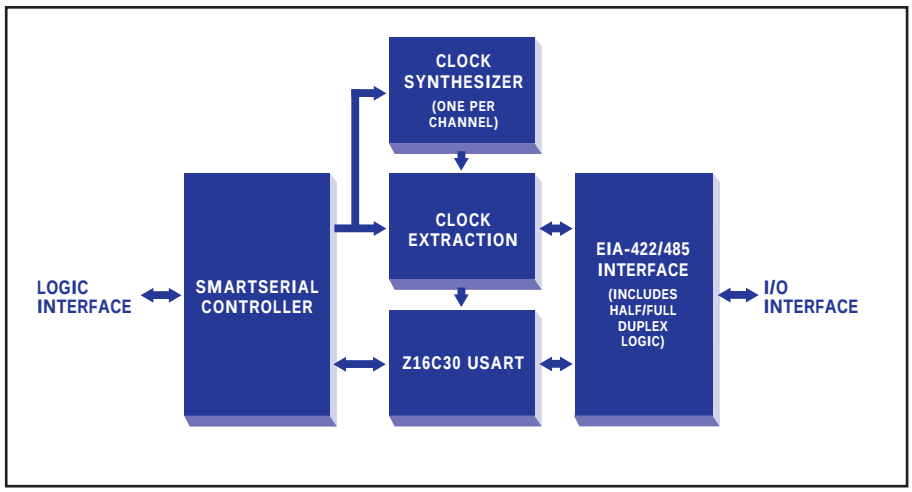
Storage Temperature
- -10 to +85 degrees C

Humidity
- 5 to 95 percent, non-condensing



I/O Pin	Signal Name	Direction
1	TXD1+	From FP
2	TXD1-	From FP
3	TXC1+	From FP
4	TXC1-	From FP
5	RXD1+	To FP
6	RXD1-	To FP
7	RXC1+	To FP
8	RXC1-	To FP
9,10	GND	
11	DCD1+	To FP
12	DCD1-	To FP
13-16	GND	
17	TXD2+	From FP
18	TXD2-	From FP
19	TXC2+	From FP
20	TXC2-	From FP
21	RXD2+	To FP
22	RXD2-	To FP
23	RXC2+	To FP
24	RXC2-	To FP
25,26	GND	
27	DCD2+	To FP
28	DCD2-	To FP
29-32	GND	
33-40	N/C	
41-50	Reserved	

FP-Serial2 Block Diagram



Ordering Information

FP-Serial2-422
Two channel EIA-422/485 Serial I/O interface

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