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Find the *Tektronix / Microwave Logic GB1400* at our website: *Click HERE*

| GB Comparison Feature | GB660/CSA907A Tx and Rx | GB700 Tx and Rx | GB1400 Tx and Rx |
|---|---|---|---|
| *Transmitter* | | | |
| Frequency range<br>Internal Clock Source<br>External Clock<br>External Clock w/Burst Mode[1] | <br>150 kHz to 700 MHz<br>150 kHz to 700 MHz<br>150 kHz to 700 MHz | <br>150 kHz to 705 MHz<br>150 kHz to 705 MHz<br>150 kHz to 705 MHz | <br>1 MHz to 1405 MHz<br>1 MHz to 1405 MHz<br>150 kHz to 1405 MHz |
| Freq Resolution CR/LF (Internal Clock) | 1 kHz | 1 kHz | 1 kHz (was 10 kHz) |
| Clock /Data Output Amplitude | 500mV to 2.0 V | 500mV to 2.0 V | 500mV to 2.0 V |
| Clock/Data Output Offset | -2.0V to +1.8 V | -2.0V to +1.8 V | -2.0V to +1.0 V<br>-2.0V to +1.8 V with PECL opt. |
| Clock/Data Threshold Resolution | 50 mV steps | 50 mV steps | 50 mV steps |
| Std. Programmable Memory | 16 bits | 128 Kbits | 16 bits |
| Optional Memory | 128 Kbits | none | 1 Mbit |
| PRBS Patterns ($2^n$-1) | 7,15,17,20,23 | 7,15,17,20,23 | 7,15,17,20,23 |
| Burst Mode New Line (External Clk Only) | Standard Feature | Standard Feature | **Optional Feature** |
| *Receiver* | | | |
| Frequency range<br>w/Burst Mode[1] | 150 kHz to 705 MHz<br>150 kHz to 705 MHz | 150 kHz to 705 MHz<br>150 kHz to 705 MHz | 1 MHz to 1405 MHz<br>150 kHz to 1405 MHz |
| Burst Mode (Ext Clk Only) | Standard Feature | Standard Feature | **Optional Feature** |
| Clock/Data Input levels (max) | 500 mV to 6.0 V p-p | 500 mV to 6.0 V p-p | **500 mV to 2.0 V p-p** |
| Clock Input Threshold | -3.00 to +4.5 V | -3.00 to +4.5 V | **Fixed threshold levels** |
| Data Input Threshold | -3.00 to +4.5 V | -3.00 to +4.5 V | **-1.5V to 1.0 V** |
| Clock/Data Threshold Resolution | 50 mV steps | 50 mV steps | 50 mV steps |
| Clock/Data Input Terminations | GND, AC, -2V, +3V | GND, AC, -2V, +3V | GND, AC, -2V |
| Single-Ended operation | Automatic selection | Automatic selection | **Requires external cable[2]** |
| Clk/Data Delay Range / Resolution | ±4 ns in 100 pS steps | ±4 ns in 20 pS steps | ±4 ns in 5 pS steps |
| | | | |
| *Note 1 Burst mode operation requires ECL levels and is DC coupled* | | | |
| *Note 2 Single-ended operation requires ext. cable connection from rear panel DATA THRESHOLD SMA* | | | |



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