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Find the *Teledyne Dalsa / Coreco Imaging Viper-Quad* at our website: ***Click HERE***

Viper Quad



The Viper-Quad is a single slot, high speed video acquisition device for the PCI-bus. It features four independent acquisition channels each comprised of an analog-to-digital converter, synchronization circuitry, anti-aliasing filter (optional) and input look-up tables. These four channels can be used to acquire images from four non-genlocked monochrome cameras. For synchronization purposes, each channel also features separate horizontal and vertical sync signals (I/O), trigger inputs (optically isolated for protection from potentially hazardous voltages), trigger outputs, exposure control output and a strobe output. These permit each channel to acquire images from four completely independent cameras.

Targeted at machine vision applications using multiple cameras, the Viper-Quad can be used to acquire multiple views of the same object (360 degree inspection) or can be used to acquire images of multiple objects on a production line.

The Viper-Quad can transfer images to host memory via the host PCI-bus, or across the 200MB/s Coreco Auxiliary Bus (CAB) to the Mamba for embedded processing. Available in two versions, the Viper-Quad comes mounted with four acquisition channels, or for more cost sensitive applications, with two acquisition channels.

Acquisition Features

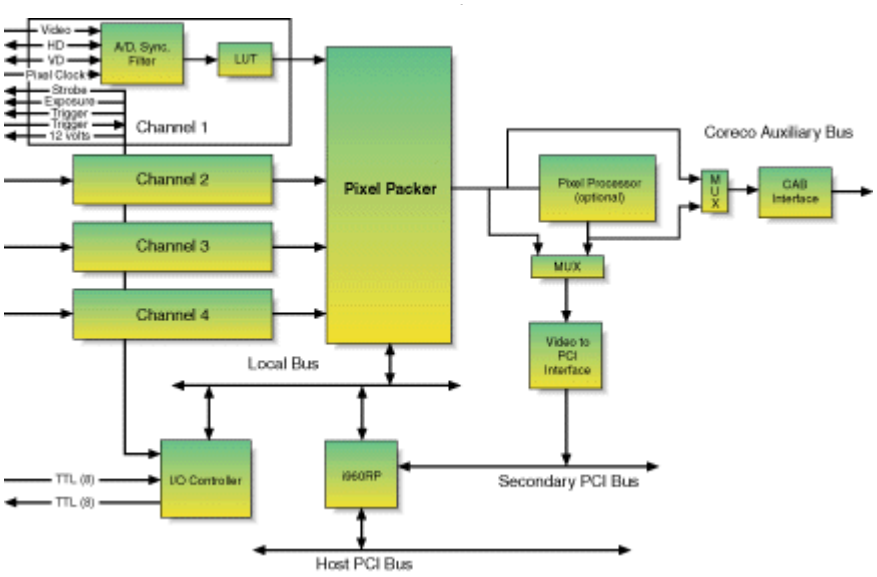
- Simultaneous acquisition from four on-synchronized cameras
- Acquires from interfaced or progressive scan cameras
- Asynchronous reset acquisition on four independent channels
- Hirose connectors for industrially secure camera connections
- 40 MHz digitization per channel
- 46 dB signal-to-noise ratio

Control Features

- Four independent trigger input and output controls
- Four independent exposure control outputs
- Four independent strobe outputs
- 12 volt fused camera power supply
- 16 bits of generalized I/O
- Localized I/O controller circumvents operating system latencies
- CAB interface for transferring images to the Mamba for embedded processing

Host Interface Features

- i960 System Controller
- PCI-bus master supports scatter/gather transfers
- 100 MB/s sustained PCI-bus bandwidth
- Local memory stores buffer image data during heavy PCI-bus traffic


Video
HD
VD
Pixel Clock
Strobe
Exposure
Trigger
Trigger
12 volts
A/D, Sync, Filter
LUT
Channel 1
Channel 2
Channel 3
Channel 4
Pixel Packer
Pixel Processor (optional)
MUX
GAB Interface
Coreco Auxiliary Bus
MUX
Video to PCI Interface
Local Bus
I/O Controller
i960RP
Secondary PCI Bus
TTL (8)
TTL (8)
Host PCI Bus

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