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Find the *Teradyne / LitePoint / ZTEC Instruments ZT4211-01PXI* at our website: *Click HERE*

ZT421x | Digital Storage Oscilloscope

High Voltage Oscilloscope

The ZT421x Digital Storage Oscilloscope (DSO) Family provides high voltage performance and supports industry standards for easy integration into automated test systems. The powerful hardware of the ZT421x Series is complemented by a flexible Graphical User Interface (GUI) and software, which enhance the instruments' capabilities for new and legacy test requirements. In addition, on-board signal processing and measurement suite speed up data acquisition and analysis. The instruments are available in PXI, LXI and VXI form factors.

FEATURES

- Available in PXI, LXI & VXI platforms
- 2 channels and 4 channels
- High input voltage range up to 400 Vpp
- Real time interleaved sample rates up to 1 GS/s
- On-board signal processing
- Compatible with third-party tools such as LabVIEW and LabWindows/CVI

BENEFITS

- High density form factor lowers overall system size and cost
- Platform flexibility promotes integration with various designs and chassis
- On board processing speeds up data acquisition and analysis
- Backed by Teradyne's best-in-class customer support and calibration/repair services

End Markets Served

- Defense & Aerospace
- Medical
- Automotive
- Semiconductor Test
- Telecommunication Test



Example Applications

- High Energy Physics Applications
- Multi-Channel Capture
- Automated Testing of Electronics and Radars
- High Speed Test Applications
- Replacement alternatives for legacy instruments used on Military ATEs

Ordering Information

Part Number	Description
ZT4211-01PXI	2 Channels in PXI
ZT4211-01LXI	2 Channels in LXI
ZT4211-01VXI	2 Channels in VXI
ZT4212-01LXI	4 Channels in LXI
ZT4212-01VXI	4 Channels in VXI

(For contact information, please refer to page 18.)

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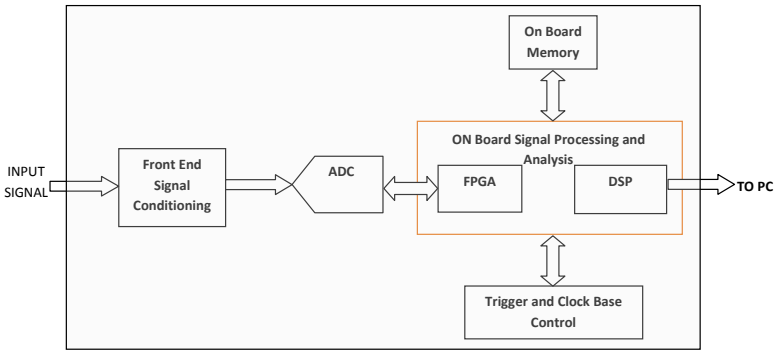
Hardware

On Board Signal Processing and Analysis

ZT-Series DSOs include an on-board DSP for signal processing and analysis capability that shortens test cycles and reduces the need for user developed signal processing algorithms. The on board capability is much faster than processing on a separate PC or controller. It also removes the need to transfer large amount of waveform data to an external PC or controller and thereby speeding up post capture analysis. The on board DSP and analysis capability ranges from performing basic mathematical functions to the more complex computations like Fast Fourier Transform (FFT), derivative, integration, histogram, limit testing, mask testing, and waveform parameter trending.

On Board Memory

ZT421x DSO provides a significant amount of flexible segmented on-board memory. Segmented memory allows waveforms to be saved on the DSO memory for later analysis and viewing. The memory segments can be viewed either overlaid or individually.



ZT421x Block Diagram

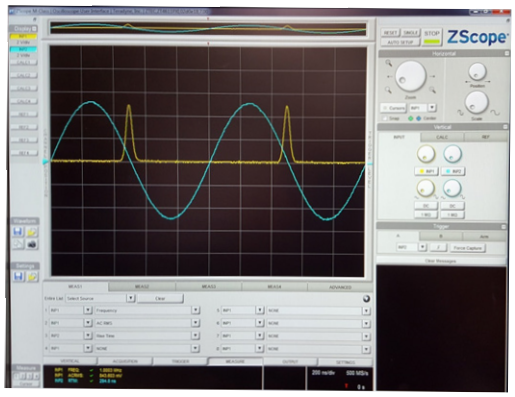
Software

Driver

ZT421x DSO instruments are operated with ZT-Series Driver software that includes API function calls or SCPI commands. The driver can work with the most commonly used application development environments like Microsoft Visual Studio® and LabWindows™/CVI for TPS development or be integrated with third-party tools like LabVIEW™.

Easy-to-Use Graphical User Interface

The ZT421x driver software suite includes a Graphical User Interface (GUI) called ZScope™. ZScope is an intuitive software interface that provides manual instrument control and a user experience similar to that of a benchtop oscilloscope. Using ZScope, no more than two clicks are required to access all oscilloscope functions. In addition to the standard oscilloscope measurement functions, ZScope includes built-in advanced measurements such as: limit testing, histogram, FFT, filtering, SNR, spurious free dynamic range, ENOB, total harmonic distortion, measurement trending, and SINAD to name a few.



ZScope Graphical User Interface

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ZT421x Series Specifications

ZT421x Series specifications are subject to change without any notice. Unless otherwise noted, the specifications of ZT421x Series are warranted at ambient temperature range of 0°C to 50°C.

ZT421x specifications are warranted under the following conditions:

- Instrument is warmed up for 20 minutes at ambient temperature
- Instrument is in the calibration window

Acquisition

Sample Rate:

- 10 kS/s to 500 MS/s, non-interleaved and real-time
- 1 GS/s, interleaved real-time or equivalent-time
- 1 GS/s to 50 GS/s, equivalent-time
- 100 GS/s, interleaved equivalent-time

Table 1 – Acquisition Model Sample Rates

Mode(s)	Sample Rate
Non-Interleaved Real-Time	10 kS/s 20 kS/s 50 kS/s 100 kS/s 200 kS/s 500 kS/s 1 MS/s 2 MS/s 5 MS/s 10 MS/s 20 MS/s 50 MS/s 100 MS/s 200 MS/s 500 MS/s
Interleaved	1 GS/s
Equivalent Time	2 GS/s 5 GS/s 10 GS/s 20 GS/s 50 GS/s
Interleaved Equivalent Time	100 GS/s

Table 2 – Acquisition Specifications

Specification	Value
Non-Interleaved Real-Time	
ZT4211	2 channels @ up to 500 MS/s real-time
ZT4212	4 channels @ up to 500 MS/s real-time
Interleaved Acquisition	
ZT4211	1 channel @ up to 1 GS/s real-time
ZT4212	2 channels @ up to 1 GS/s real-time
Sampling Modes	Normal: single-shot acquisition Average: multiple-capture acquisition Envelope: multiple-capture minimum & maximum detection Equivalent Time: multiple-capture, high-rate reconstruction Peak Detect: single-shot 10x over-sampling envelope detection High Resolution: single-shot 10x over-sampling averaging Fast: Multiple acquisitions
Multiple-Capture Count	2 to 65,536 waveforms in powers of 2
Acquisition Re-Arm Time	≤ 5 µs
Total Memory	
ZT42x1	256 MiSamples
ZT42x2	512 MiSamples
Maximum Record Length	
Non-Interleaved	128 MiSamples
Interleaved	256 MiSamples
Minimum Waveform Size	10 Samples
Maximum Waveform Size	
Normal or Fast Acquisition Mode	Maximum Record Length (above)
All Other Modes	512 KiSamples
Segmented Memory	View & compare history of previous waveforms in memory View component waveforms when averaging
Maximum Segments	32 Ki waveforms @ ≤ 4 KiSamples

Vertical Input Channels

Table 3 – Input Channel Specifications

Specification	Value
Number of Channels	
ZT4211 (interleaved acquisition)	Quantity 1
ZT4211 (non-interleaved acquisition)	Quantity 2
ZT4212 (interleaved acquisition)	Quantity 2

ZT4212 (non-interleaved acquisition)	Quantity 4
Connectors	BNC
Maximum Input (50 Ω)	±5 V (DC + peak AC) CAT I Input load protection at ±6 VDC
Maximum Input (1 MΩ)	±300 V [DC + peak AC (<100 kHz)], CAT II Peak AC, de-rated 20 dB/decade above 100 kHz

Full Scale Input Range & Offset Adjust

Table 4 – Full Scale Input Range & Offset Adjust Value

Impedance	Range	Full Scale	Offset	Maximum Range & Offset
1 MΩ	40 V/div	400 Vpp	0 V	± 200 V
	20 V/div	200 Vpp	± 100 V	± 200 V
	8 V/div	80 Vpp	± 100 V	± 140 V
	4 V/div	40 Vpp	± 100 V	± 120 V
	2 V/div	20 Vpp	± 100 V	± 110 V
	1 V/div	10 Vpp	± 100 V	± 105 V
	500 mV/div	5 Vpp	± 2.5 V	± 5 V
	200 mV/div	2 Vpp	± 2.5 V	± 3.5 V
	100 mV/div	1 Vpp	± 2.5 V	± 3 V
	50 mV/div	500 mVpp	± 2.5 V	± 2.75 V
	25 mV/div	250 mVpp	± 2.5 V	± 2.625 V
	10 mV/div	100 mVpp	± 2.5 V	± 2.55 V
	5 mV/div	50 mVpp	± 2.5 V	± 2.525 V
	2.5 mV/div	25 mVpp	± 2.5 V	± 2.5125 V
	1.25 mV/div	12.5 mVpp	± 2.5 V	± 2.50625 V
50 Ω	1 V/div	10 Vpp	0 V	± 5 V
	500 mV/div	5 Vpp	± 2.5 V	± 5 V
	200 mV/div	2 Vpp	± 2.5 V	± 3.5 V
	100 mV/div	1 Vpp	± 2.5 V	± 3 V
	50 mV/div	500 mVpp	± 2.5 V	± 2.75 V
	25 mV/div	250 mVpp	± 2.5 V	± 2.625 V
	10 mV/div	100 mVpp	± 2.5 V	± 2.55 V
	5 mV/div	50 mVpp	± 2.5 V	± 2.525 V
	2.5 mV/div	25 mVpp	± 2.5 V	± 2.5125 V
	1.25 mV/div	12.5 mVpp	± 2.5 V	± 2.50625 V

Technical Notes:

- For the range, magnification is used below 5 mV/div range. Full-scale range for accuracy specifications is defined as 50 mV.
- Full-scale range assumes 10 divisions.

Inputs

Table 5 – Input Specifications

Specification	Value
Analog Bandwidth (1 MΩ)	DC to 300 MHz typical, 250 MHz minimum
Rise Time	1.15 ns
Slew Time	2 kV/µs
Impedance	1 MΩ | | 12 pF or 50 MΩ ± 1% accuracy
Input Bias	
50 Ω	≤ ± 10 µA
1 MΩ	≤ ± 1 nA
DC Gain Accuracy	≤ ± 1% full scale range
DC Offset Accuracy (+25° C)	
50 Ω, ≤ 100 mVpp	< ± (1% full scale range + 1% offset + 2 mV)
50 Ω, ≥ 250 mVpp	< ± (1% full scale range + 1% offset + 1 mV)
1 MΩ, ≤ 100 mVpp	< ± (1% full scale range + 1% offset + 2 mV)
1 MΩ, ≥ 250 mVpp, ≤ 5 Vpp	< ± (1% full scale range + 1% offset + 1 mV)
1 MΩ, ≥ 10 Vpp	< ± (1% full scale range + 1% offset + 50 mV)
DC Offset Drift (per °C)	
50 Ω, ≤ 50 mVpp	< ± (0.05% full scale range + 250 µV)
50 Ω, ≥ 1 Vpp	< ± (0.05% full scale range + 100 µV)
1 MΩ, ≤ 500 mVpp	< ± (0.05% full scale range + 275 µV)
1 MΩ, ≥ 1 Vpp, ≤ 5 Vpp	< ± (0.05% full scale range + 100 µV)
1 MΩ, ≥ 10 Vpp	< ± (0.05% full scale range + 10 mV)
Input VSWR (50 MΩ)	≤ 1.4:1, DC to 250 MHz
Analog Filter	20 MHz or Bypass Filter Stopband Rejection: approx. 3 dB @ 20 MHz
Probe Attenuation	0.9 to 1000:1
Coupling	DC or AC
AC Coupling	
50 Ω	200 kHz high-pass
1 MΩ	10 Hz high-pass

Channel-to-Channel Isolation	
DC to 100 MHz	≥ 50 dB
100 MHz to 250 MHz	≥ 40 dB
Typical RMS Noise	
Full Scale ≥ 100 mVpp	
50 Ω (full bandwidth)	0.3% of full scale range + 600 µV
50 Ω with 20 MHz Filter	0.3% of full scale range
1 MΩ (full bandwidth)	0.7% of full scale range + 700 µV
1 MΩ with 20 MHz Filter	0.5% of full scale range
Full Scale < 100 mVpp	
50 Ω (full bandwidth)	0.7% of full scale range + 600 µV
50 Ω with 20 MHz Filter	0.5% of full scale range
1 MΩ (full bandwidth)	0.7% of full scale range + 700 µV
1 MΩ with 20 MHz Filter	0.5% of full scale range + 100 µV
Digital Resolution	8 bits (0.390% of full scale) Up to 32 bits with averaging
Power Supply Noise Rejection	25 dB (PXI only)

Technical Notes:

- Rise time is calculated from t_r = 0.35/bandwidth.

Dynamic Range (500 MS/s, 50Ω)

Table 6 – Dynamic Range Specifications

Input Range	Frequency	Signal-to-Noise Ratio (SNR)	Total Harmonic Distortion (THD)	Signal-to-Noise & Distortion (SINAD)
10 Vpp	10.7 MHz	41.21 dBc	-53.15 dBc	40.94 dBc
1 Vpp	10.7 MHz	41.93 dBc	-59.53 dBc	41.86 dBc
100 mVpp	10.7 MHz	33.48 dBc	-57.59 dBc	33.47 dBc
10 Vpp	60.1 MHz	41.24 dBc	-48.68 dBc	40.51 dBc
1 Vpp	60.1 MHz	41.36 dBc	-49.93 dBc	40.79 dBc
100 mVpp	60.1 MHz	33.16 dBc	-48.12 dBc	33.02 dBc

Horizontal

Table 7 – Horizontal Specifications

Specification	Value
Sweep Time Range	10 ns to 100 s (1 ns/div to 10 s/div)

Sweep Time Resolution	10 ns to 10 ms dependent on sweep points and sample rate
Horizontal Position	
Pre-Trigger	0 to 100% of acquisition window
Post-Trigger	0 to 10,000*Gate Resolution
Channel-to-Channel Skew	Channels at same input settings
Channels 1-to-2, 3-to-4	≤ 100 ps
Channels 1-to-3, 1-to-4, 2-to-3, 2-to-4	≤ 200 ps
Skew Adjust	± 10 µs channel-to-channel skew adjustment 1 sample interval resolution ± 1 sample interval accuracy
Timebase Reference	10 MHz
Timebase Reference Source	Internal TCXO, External Input, Backplane (PXI/VXI)
Internal TCXO Timebase	± 2.5 ppm accuracy
Timebase Output	External Output

Technical Notes:

- Horizontal time range assumes 10 divisions for horizontal axis.

Trigger

Table 8 — Trigger Specifications

Specification	Value
Sweep Modes	Auto or Normal triggered
Trigger Source	Channels 1 to 4, External Input, Pattern, Software, TTL Trigger 0-7, Star Trigger (PXI), ECL Trigger 0-1 (VXI)
Trigger Slope/Polarity	Positive or Negative
Trigger B	Qualify trigger on second source for edge trigger event
Trigger A Holdoff	Programmable delay after trigger A before recognizing next trigger A event
Trigger B Holdoff	Programmable delay after trigger A before recognizing trigger B event
Trigger A/B Holdoff Range	0 to 100 seconds
Trigger A/B Event Counter	Qualify trigger on N_{th} Trigger event, N=1 to 65536
Trigger A Modes	Edge, Pattern, State, Pulse Width, Video
Trigger B Modes	Edge, Pattern
Pattern Trigger Mode	Pattern match true or false
Pattern Sources	Channels 1 to 4, External Input, TTL Trigger 0-7, Star Trigger (PXI), ECL Trigger 0-1 (VXI)

State Trigger Mode	Edge event when pattern match true or false. Pattern source used as Arm qualifier.
Pulse Width Trigger Mode	Trigger on pulse width greater than, less than, within, or outside limits < Limit 1, > Limit 1, Limit 1 < width < Limit 2, Limit 1 > width > Limit 2
Pulse Width Limits	10 ns to 500 ms 5 ns resolution ± 5 ns accuracy
Pulse Width Minimum	2 ns pulse width captured for < Limit1
Video Trigger Mode	PAL (50 Hz), NTSC (60 Hz), SECAM (50 Hz) Standard, Field, Line selectable
Trigger Timestamp	100 ns resolution, 1 second rollover

Trigger, Analog Input

Table 9 – Analog Input Trigger Specifications

Specification	Value
Analog Input Triggers	Channels 1 to 4
Trigger Level	(offset - full scale range/2) to (offset + full scale range/2)
Trigger Hysteresis	2.5% (overdrive required); 4628 PXI only: 3.5-5.5%
Trigger Level Resolution	0.025% of full scale range
Trigger Level Accuracy	± (2% full scale range + 5 mV + offset accuracy)
Trigger Sensitivity	
DC to 100 MHz	5% of full scale range
>100 MHz	10% of full scale range
Trigger Bandwidth	DC to 300 MHz typical, DC to 250 MHz minimum
Glitch Detection	≥ 500 ps glitch captures in edge trigger mode

Arm

Table 10 – Arm Specifications

Specification	Value
Functionality	Arm to qualify Trigger Event
Source	External Input, TTL Trigger 0-7, Star Trigger (PXI), ECL Trigger 0-1 (VXI), Software
Polarity	Positive or Negative

External Input (Front Panel)

Table 11 – Front Panel External Input Specifications

Specification	Value
Functionality	Trigger Input, Timebase Reference Input, External Arm
Absolute Maximum Input (no damage)	≤ ± 5 V (DC + peak AC), CAT I
Input Trigger Level Adjustment	-2 V to +2 V 0.5 mV resolution ≤ 20 mV accuracy 20 mV overdrive (input hysteresis)
Input Bandwidth (-3 dB) DC	≥ 250 MHz
Input Impedance	1 MΩ | | 30 pF or 50 Ω ≤ ± 2% accuracy
Connector	
PXI	SMB
VXI/LXI	BNC

External Output (Front Panel)

Table 12 – Front Panel External Output Specifications

Specification	Value
Functionality	Trigger Output, Timebase Reference Output, Event Output, Programmable Clock Output, Programmable Pulse Output, Constant Level, and Probe Compensation Output
Output Event Source	Arm Event, Trigger A Event, Trigger B Event, Trigger Complete Event, Capture Complete Event, Operation Complete Event, Master Summary Status Event, Limit Test Successful Event
Polarity	High or Low Truth
Programmable Event Pulse Width	50 ns to 163 ms
Programmable Clock	Period: 26.667 ns to 100 seconds 50% Duty Cycle
Programmable Pulse	
Pulse Repetition	26.667 ns to 100 seconds
Interval Pulse Width	26.667 ns
Probe Compensation	10 kHz Clock which can be used to compensate probes
Limit Test Successful	Event pulse after each capture upon limit or mask test success
Output Level	TTL Compatible into ≥ 200 Ω ≥ ± 24 mA Output Drive
Output Enable	Tri-State Output Capability
Connector	
PXI	SMB

VXI/LXI	BNC

Backplane Triggers

Table 13 – Backplane Trigger Specifications

Specification	Value
Functionality	Multi-Instrument Synchronization Trigger, Event Output Signals
Triggers	TTL Trigger 0-7 ECL Trigger 0-1 (VXI)
Direction	Input or Output
Source	Arm Event, Trigger A Event, Trigger B Event, Trigger Complete Event, Capture Complete Event, Operation Complete Event, Master Summary Status Event, Constant Level
Polarity	High or Low Truth
Programmable Event Pulse Width	50 ns to 163 ms

Measurements

Table 14 – Measurements Specifications

Specification	Value
Measurements	AC RMS, Amplitude, Average, Cycle Average, Cycle Frequency, Cycle Period, Cycle RMS, DC RMS, Duty Cycle High, Duty Cycle Low, ENOB, Number of Falling Edges, Fall Crossing Time, Fall Overshoot, Fall Preshoot, Fall Time, Frequency, High, Low, Maximum, Mid, Minimum, Peak-to-Peak, Period, Phase, Pulse Width Positive, Pulse Width Negative, Number of Rising Edges, Rise Crossing Time, Rise Overshoot, Rise Preshoot, Rise Time, SFDR, SINAD, SNR, Standard Deviation, THD, Time of Maximum, Time of Minimum
Edge Measurements	N_{th} edge selectable, N = 1 to 65,535
Maximum Measurements	N_{th} maximum selectable, N = 1 to 100 Applies to Maximum and Time of Maximum
Measurement Methods	Entire waveform, Gated by Time, Gated by Points, Gated by Frequency, Gated by Cursors
Measurement Levels	Low, Mid, High reference levels for edge measurements set in absolute voltages or relative percentages
Cursors	Quantity 2 Horizontal & vertical axis location markers X, Y, ΔX, ΔY Measurements
Measurement Lists	Quantity 4 Up to 8 measurements per list that are performed upon acquisition Stored for rapid measurement setup

Measurement Trending	Historic buffer of measurement data stored in Calculate channel (see Calculations)
Measurement Accuracy	
Delta DC Voltage	± DC gain accuracy
Absolute DC Voltage	± (DC gain accuracy + offset accuracy)
Time	± time resolution
Frequency	± (1/(period of applied signal ± time resolution))

(Note: Tme resolution = one sample interval, one equivalent-time sample interval, or one interpolated-time sample interval (depending upon acquisition mode)

Reference Waveforms

Table 15 – Reference Waveforms Specifications

Specification	Value
Reference Channels	Quantity 4
Reference Storage	Non-volatile memory storage
Reference Data	32 KiSample maximum waveform size 32-bit resolution

Calculations

Table 16 – Calculations Specifications

Specification	Value
Calculate Channels	Quantity 4
Calculate Data	512 KiSample maximum waveform size
32-bit resolution	32 KiSample maximum waveform size 32-bit resolution
Calculate Functions	Add, Subtract, Multiply, Copy, Invert, Integral, Derivative, Absolute Value, Limit Test, Mask Test, Frequency Transform, Time Transform, Histogram, Measurement Trending
Limit Test	Measurement limit range testing or waveform mask testing
Limit Test Reporting	Measurement maximum, minimum, average, current value, pass count and fail count
Frequency Transform	FFT Magnitude
FFT Windowing	Rectangular, Hamming, Hann, Blackman, Flattop
FFT Data Format	Linear Magnitude, Logarithmic Magnitude, Phase, Real, Imaginary
Time Transform	Digital Infinite Impulse Response (IIR) filter
IIR Filter Type	Auto-generate: Low-pass, 2 to 40 data point smoothing

Histogram	65,536 bins for up to 16-bit histogram horizontal resolution
Measurement Trending	Historical waveform of measurement data. Provides trend data of 1 measurement point per capture.

Data Processing & Download

Table 17 – Data Processing & Download Specifications

Specification	Value
Self-Calibration	Automatic internal calibration: Input DC Offset Zero, Input DC Offset Adjust Scale Factor, ADC Balance, ADC Timing
Auto-Scale	Automatic adjust to input signals: Input Range, Offset, Sample Rate, Trigger Source and Trigger Level
Waveform Data Formats	8-bit, 16-bit or 32-bit signed integer 32-bit or 64-bit floating point Intel or Motorola Byte Order
Waveform Download Mode	
Normal	Every real-time data point
Decimated	Every N_{th} real-time data point (N = 2 to 100,000)
Interpolated	N points for every real-time point (N = 2 to 100)
Waveform Interpolation Modes	None, First Order (linear), or sin(x)/x (sinc)

Instrument Stored States

Table 18 – Instrument Stored States Specifications

Specification	Value
Functionality	Non-volatile storage of instrument setup configuration
Stored States	30 State 0 is Reset State Power-On State programmable

LED Indicators

Table 19 – LED Indicators Specifications

Specification	Value
RDY (Ready)	OFF: Hardware Failure ON: Unit has passed power-up self-diagnostics TOGGLE: Unit has an error pending in error queue
HST/LAN (Host)	OFF: Interface fault ON: Normal interface operation TOGGLE: Device identify enabled

TRG (Trigger)	OFF: Trigger event not detected ON/PULSE: Trigger complete event detected
ACT (Active)	OFF: Instrument Idle ON/PULSE: Data acquisition initiated
1588 (1588 Clock Status) (LXI only)	OFF: IEEE 1588 clock not synchronized or fault ON: Clock locked as IEEE 1588 slave TOGGLE @ 1 s: Clock synchronized as IEEE 1588 master TOGGLE @ 2 s: Clock synchronized as IEEE 1588 grand master
PWR (Power) (LXI only)	ON: Unit is powered on OFF: Unit is powered off

PXI Interface

Table 20 – PXI Interface Specifications

Specification	Value
PCI Bus Data Interface	33 MHz, 32 bit 132 MByte/s burst up to 120 MByte/s sustained
PCI Voltage	Universal, +3.3 V or +5 V
PCI Standard Compatibility	Version 2.2
PXI Slot Compatibility	PXI Standard Slot and PXIe Hybrid Slot Compatible
PXI Timing & Triggering Signals (XJ4 Connector)	PXI_TRIG[0:7] input/output PXI_STAR input PXI_CLK10 input
PXI Identification	
Primary ID	3712 (0E80)
Secondary ID	4611 (1203)

Technical Notes:

- Sustained PXI and transfer rates are dependent upon host system configuration.

VXI Interface

Table 21 – VXI Interface Specifications

Specification	Value
Command Interface	A16 message-based servant, SCPI compatible
Interrupt Operation	Programmable interrupter, Level 1-7
Manufacturer ID	
Primary ID	3712 (0E80)

Secondary ID	421 (1A5)

LXI Interface

Table 22 – LXI Interface Specifications

Specification	Value
Command Interface	LAN 10/100/1000, SCPI Compatible
Manufacturer ID	
Primary ID	3712 (0E80)
Secondary ID	421 (1A5)

Status Reporting

Table 23 – Status Reporting Specifications

Specification	Value
IEEE-488.2 Device Status	Reporting Structure including Status Byte, Standard Event Registers, Questionable Registers, Operation Registers and Self-Test Status Registers

AC Power (LXI)

Table 24 – LXI AC Power Specifications

Specification	Value
Line Voltage	90-264 VAC, 47-63 Hz, automatic selection
Input Protection	AC line fuse, 250 VAC, 2.0 A, fast-acting
Harmonic Distortion	Meets EN610100-3-2
Surge Withstand	Meets EN61000-4
EMI Filtering	Meets CISPR 11 and 22 and FCC Part 15 Class B (conducted)

Power & Cooling Specifications

Power Supplies

Table 25 – Power Supplies Specifications

Model	Platform	Voltage	Typical Current	Maximum Current

ZT4211	PXI	+3.3 VDC +5 VDC +12 VDC -12 VDC	3.99 A 0.76 A 0.00 A 0.00 A	5.55 A 1.08 A 0.00 A 0.00 A
	VXI	+5 VDC +12 VDC +24 VDC -2 VDC -5.2 VDC -12 VDC -24 VDC	3.85 A 0.00 A 0.00 A 0.07 A 0.42 A 0.00 A 0.00 A	5.41 A 0.00 A 0.00 A 0.08 A 0.53 A 0.00 A 0.00 A
	LXI	115 VAC	0.33 A	0.38 A
ZT4212	VXI	+5 VDC +12 VDC +24 VDC -2 VDC -5.2 VDC -12 VDC -24 VDC	6.26 A 0.00 A 0.00 A 0.07 A 0.69 A 0.00 A 0.00 A	8.67 A 0.00 A 0.00 A 0.08 A 0.90 A 0.00 A 0.00 A
	LXI	115 VAC	0.43 A	0.51 A

Cooling and Power Consumption

Table 26 – Cooling and Power Consumptions Specifications

Model	Platform	Typical Cooling & Power	Maximum Cooling & Power
ZT4211	PXI	17.0 W	24 W
	VXI	22 W	30 W
	LXI	38.4 W	43.5 W
ZT4212	VXI	35 W	48 W
	LXI	49.4 W	58.2 W

Physical & Environmental Specifications

Size & Weight

Table 27 – Size & Weight Specifications

Specification	Value
PXI Physical Size	Single-Wide 3U PXI Instrument 8.25" x 0.79" x 5.25" (L x W x H) 20.96 cm x 2.01 cm x 13.34 cm (L x W x H)

VXI Physical Size	Single-Wide C-size VXI bus Instrument 14.45" x 1.20" x 10.35" (L x W x H) 36.70 cm x 3.05 cm x 26.29 cm (L x W x H)
LXI Physical Size	Half-Width 1U LXI Instrument 13.35" x 7.25" x 1.75" (L x W x H) 33.91 cm x 18.42 cm x 4.345 cm (L x W x H)
PXI Weight	10.6 oz or 300 g
VXI Weight - ZT4211	2.97 lbs or 1.35 kg
LXI Weight - ZT4212	4.54 lbs or 2.06 kg

Temperature Range

Table 28 – Temperature Range Specifications

Specification	Value
Operating	0°C to +50°C ambient
Storage	-40°C to +75°C
Over-Temperature	Automatic shutdown if internal temperature exceeds +90°C *(For instruments with DSP V8.03 or less, the automatic shutdown temperature is +65°C)*
Calibration Range	+20°C to +30°C ambient, after a 20 minute warm-up period, to meet all calibration specification accuracies

Relative Humidity

Table 29 – Relative Humidity Specifications

Specification	Value
Operating or Storage	10 to 90% non-condensing, up to +50 °C

Altitude

Table 30 – Altitude Specifications

Specification	Value
Operating	Up to 3 km Up to 5 km with Maximum Input (1 MΩ) of ±150 V
Storage	Up to 15 km

Safety & Compliance Information

Safety

This product is designed to meet the requirements of the following standard of safety for electrical equipment for measurement, control, and laboratory use:

- EN 61010-1

Electromagnetic Compatibility

CE Marketing EN 61326-1:1997 with A1:1998 and A2:2001 Compliant.

FCC Part 15 (Class A) Compliant.

Emissions

- EN 55011 Radiated Emissions, ISM Group 1, Class A, distance 10 m, emissions < 1 GHz
- EN 55011 Conducted Emissions, Class A, emissions < 10 MHz Immunity
- EN 61000-4-2 Electrostatic Discharge (ESD), 4 kV by Contact, 8 kV by Air
- EN 61000-4-3 RF Radiated Susceptibility, 10 V/m
- EN 61000-4-4 Electrical Fast Transient Burst (EFTB), 2 kV AC Power Lines
- EN 61000-4-5 Surge
- EN 61000-4-6 Conducted Immunity
- EN 61000-4-8 Power Frequency Magnetic Field, 30 A/m
- EN 61000-4-11 Voltage Dips and Interrupts

CE Compliance

This product meets the necessary requirements of applicable European Directives for CE Marking as follows:

- 72/23/EEC Low Voltage Directive (Safety)
- 89/336/EEC Electromagnetic Compatibility Directive (EMC)

See Declaration of Conformity for this product for additional regulatory compliance information.

LXI Conformance

This product's LXI models are conformant to the LXI Consortium's Functional Class C, Revision 1.2.

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