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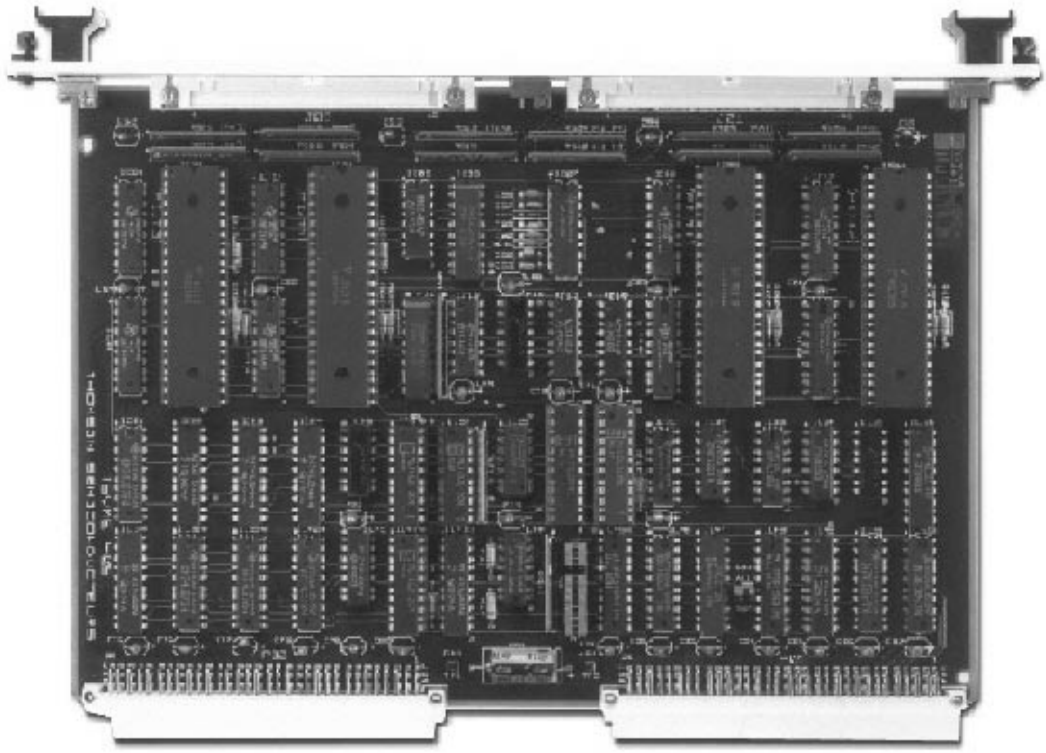
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Find the *Mercury Computer Systems / Themis TSVME-405-V1R3* at our website: *Click HERE*

TSVME 405

Parallel Input / Output Board



Main specifications

- 64 input/output lines splitted in 8 x 8 ports
- 16 handshake lines also usable as interrupt inputs or as additionnal input/output lines
- 8 timer lines also usable as input/output lines
- Outputs programmable in open-collector or totem-pole
- Removable resistor networks for flat cable adaptation
- 4x24-bit timers programmation with 4 Parallel Interface Timers (PI/T) TS68230
- Programmable interrupt level and vector
- Input/Output connection via two 3M 50-pin front pannel connectors or via VMEbus connector P2

INDUSTRIAL I/O BOARD

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The **TSVME405** is a TTL level input/output VMEbus board built around four 68230 or PI/T (Parallel Interface/Timers) circuits which provides three major types of applications :

- centralizing of information from various industrial captors via interface boards located outside the VMEbus system ;
- command of industrial outputs via power boards which are also remotely installed ;
- fast transfer of parallel data between two systems.

The TSVME405 board has 88 input/output lines distributed as follows :

- 8 x 8-bit I/O ports which may be grouped in twos of fours ;
- 4 x Timer outputs, each capable of generating an interrupt with its own vector on the VMEbus ;
- 4 x Clock or Timer enable inputs ;
- 8 x Port interrupt inputs of Handshake inputs ;
- 8 x Port interrupt inputs or Handshake outputs.

Output port/connector interfacing is achieved by programmable tri-state input or output buffers, or open collector output buffers. These buffers are also designed to leave the lines undisturbed when the board is off.

The electrical characteristics of the TTL I/Os are compatible with the specifications as defined by the IEEE 488 standard.

Each input line can be closed on a resistance network to the ±5V and to the ground. It is thus possible to adapt inputs to the impedance of the transmission line. The hysteresis of the input buffers guarantees good protection against transmisssion interference.

The configuration of the 88 lines is fully software programmable, namely :
- Line type : input or output ;
- Output type : Totem-Pole or open collector ;
- Transfer direction controlled by handshake inputs or by 68230 ;
- Ports and Timers interrupt generation ;
- Interrupt level on the VMEbus.

ORDERING INFORMATION :

TSVME405	64 TTL inputs/outputs

☞ **CUSTOMIZED BOARDS :** VME Boards are usually delivered with a standard, general-purpose configuration. Application-related configuration can be made either by the customer, or before shipping, in our factory. Given the jumper, solder beads and wrapping configuration, we deliver boards ready to be plugged into the destination VME system.

TEKELEC SYSTEMES – *VME*

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E-mail : vme@temex.fr / WebSite : www.temex-telecom.com/vme.htm

FTC-VME405en-a

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