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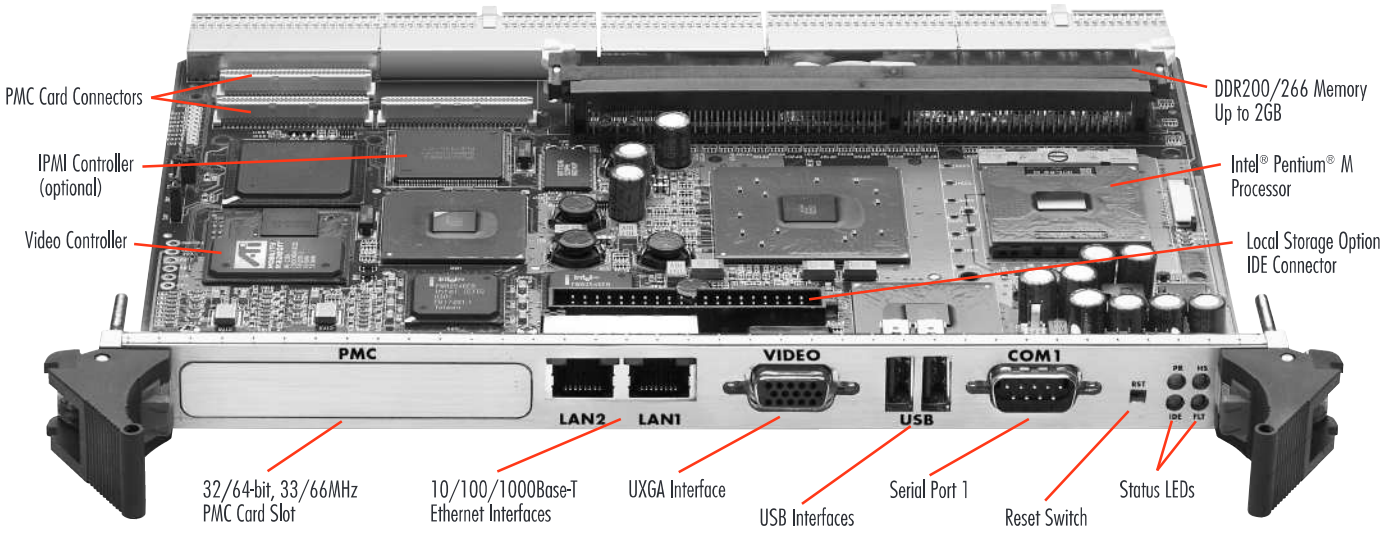
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Find the *Trenton CP10/2.26E2-NI* at our website: *Click HERE*

CP10

PRODUCT DATA SHEET

CP10 CompactPCI® SINGLE BOARD COMPUTER



The flexible design features of Trenton's CP10 enable the SBC to excel in a wide variety of applications. Optional plug-in local storage, PMC Card support, a 32/64-bit, 33/66MHz CompactPCI® bus interface, and optional IPMI support enable the CP10 for use in a variety of CompactPCI system board applications.

PROCESSOR:

Intel® Pentium® M processor at speeds of 1.3GHz to 1.8GHz*
Processor Package: Micro-FCPGA

**Higher speeds as available*

The Intel® Pentium® M processor combines micro-architecture enhancements with innovative power management techniques to produce a processor ideal for CompactPCI® applications. These enhancements and innovations include:

- A 2MB Level 2 (L2) cache memory is available on processor speeds 1.8GHz and above. Processors below 1.8GHz have an L2 cache of 1MB.
- Enhanced Intel SpeedStep® Technology (EIST)
- Processor speeds of 1.8GHz and above are produced using Intel's 90nm process technology. Processors below 1.8GHz use the .13 micron process.
- New micro-op engine (Micro-op fusion)
- Demand Based Switching (DBS)
- Advanced branch prediction unit

CHIPSET:

The CP10 uses Intel's E7501 chipset which provides support for DDR200/266 memory with ECC and a high-speed Hub Link 2.0 interface.

FORM FACTOR:

The CP10 is a single-slot module (4HP width) with a 6U height and is fully compliant with the CompactPCI® Core Specification: PICMG® 2.0, R3.0.

CompactPCI® BUS:

The CP10 provides direct support for up to seven expansion CompactPCI slots at 32-bit/33MHz and up to four expansion slots at 64-bit/66MHz. Each slot provides support for PCI Bus mastering.

LOCAL STORAGE OPTIONS:

A Compact Media Daughter Card (CMDC) plugs into connector P11A on the CP10 and provides support for either a CompactFlash™ or a Microdrive™ storage device. The CP10 also supports an IDE laptop hard drive via Trenton's Hard Drive Adapter Hit (HDAK).

DUAL ETHERNET INTERFACES - 10/100/1000BASE-T:

An internal 64-bit/100MHz PCI-X bus connects to the Intel® 82546EB Ethernet controller, ensuring full-speed Gigabit Ethernet to the CP10's front panel ports. The controller supports 10/100/1000Base-T Ethernet connectivity to the RJ-45 connectors located on the CP10's front panel. Optional CP10 Ethernet routing configurations provide additional Ethernet interfaces on the rear transition module. Consult Trenton for optional RTM Ethernet routing and ordering details.

BUS SPEEDS:

CompactPCI® Bus	- 32-bit/33MHz, 32-bit/66MHz, 64-bit/33MHz, 64-bit/66MHz
PCI-X Local Bus	- 64-bit/100MHz
PCI Local Bus	- 32-bit/33MHz & 64-bit/66MHz
Hub Link 2.0	- 1GB/s
System or FSB	- 400MHz or 533MHz

PMC OPTION CARD SLOT:

The CP10's PMC option card slot supports a PMC card with either a 32/64-bit, 33/66MHz PCI or PCI-X interface.

REAR TRANSITION MODULE (OPTIONAL):

The RTM20 Rear Transition Module is available from Trenton to provide the following I/O and data storage functionality to the rear of a CompactPCI chassis:

- Dual 10/100Base-T Ethernet Ports
- Ultra XGA Video Interface Port
- Serial Port
- Mouse/Keyboard Port
- Dual USB Ports (rev. 1.1)
- Dual IDE connectors
- Floppy drive connector
- CompactFlash™

Check with Trenton to discuss other available RTM versions for use with the CP10 or to discuss your unique rear chassis I/O requirements.

BIOS (FLASH):

The CP10's BIOS is AMIBIOS8® with IPMI extensions. The flash BIOS resides in the 82802 Firmware Hub (FWH). Some of the key features of the BIOS are:

- Boot from network, USB mass storage devices, IDE or ATAPI
- Serial port console redirection to support headless operation
- IPMI v1.5 Baseboard Management Controller (BMC) support
- BMC console (text) redirection
- Intel® SpeedStep support

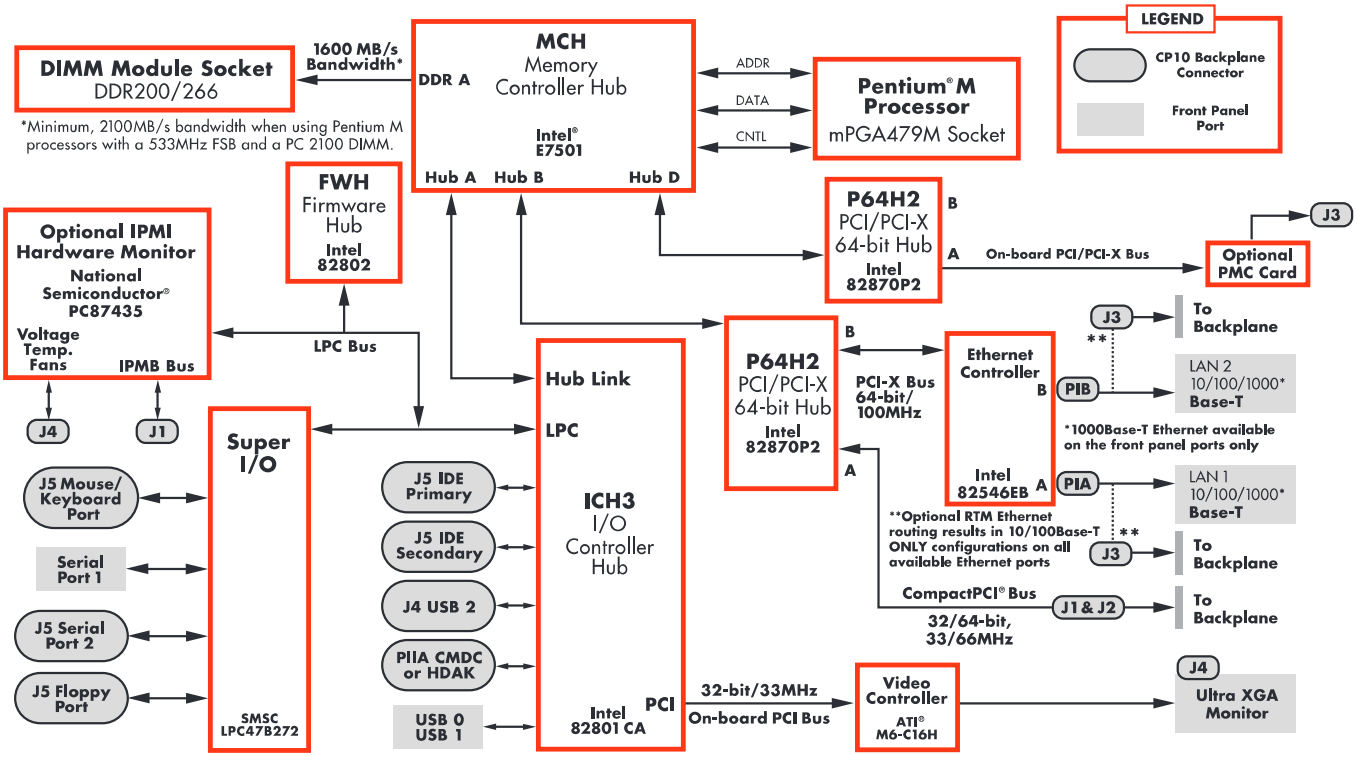
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CP10

PRODUCT DATA SHEET



ULTRA XGA INTERFACE:

The ATI® M6-C16H video controller enables 2D/3D video acceleration and provides 16MB of integrated video DDR memory. The video controller supports pixel resolutions up to 1600 x 1200 (UXGA). Software drivers are available for most popular operating systems.

DUAL ULTRA ATA/100 INTERFACES:

Dual high-performance PCI EIDE interfaces are capable of supporting up to two IDE disk drives each in a master/slave configuration. The interfaces support Ultra ATA/100 with synchronous ATA mode transfers up to 100MB per second. The interfaces are routed through backplane connector J5. Dual EIDE connectors are available on the optional RTM20 rear transition module.

DDR200/266 MEMORY:

The CP10 provides a single channel DDR memory interface terminating at a DIMM socket. The memory interface supports up to 2GB of memory and has a minimum interface bandwidth of 1600MB/s. The SBC accepts a single ECC, registered PC1600 or PC2100 DIMM.

UNIVERSAL SERIAL BUS (USB):

A total of three (3) USB ports are available on the CP10. USB ports 0 and 1 are located on the CP10's front panel and USB port 2 is available at connector J4. The CP10's Intel® E7501 chipset supports USB revision 1.1.

ADDITIONAL CP10 FEATURES:

- Two high-speed serial ports
- PS/2 mouse/keyboard interface via J5
- Floppy drive interface
- Programmable watchdog timer

CP10 APPLICATION CONSIDERATIONS:

Power Requirements:

Typical Values

CPU	+5V	+12V	+3.3V
1.8GHz*	6.25A	0.20A	4.10A
1.8GHz**	4.03A	0.20A	5.96A
1.6GHz*	6.20A	0.20A	3.99A
1.6GHz**	4.00A	0.20A	5.80A

-12V @ <100mA

Tolerance for all voltages is +/- 5%.

**5V(I/O) configured backplane*

***3.3V(I/O) configured backplane*

Temperature/Environment:

Operating Temperature:	00 to 550 C. with 350 LFM of airflow
Storage Temperature:	-200 to 700 C. with 350 LFM of airflow
Humidity:	5% to 90% non-condensing
Cooling Solution:	Passive Heat Sink

STANDARDS:

- PCI Local Bus Specification 2.1
- CompactPCI® Core Specification, PICMG® 2.0, R3.0
- CompactPCI® PCI Mezzanine Card (PMC) I/O Pin Assignment Specification, PICMG® 2.3, R1.0
- CompactPCI® System Management Specification, PICMG® 2.9, R1.0 (Intelligent Platform Management Interface (IPMI) support)

AGENCY APPROVALS:

Designed for UL60950, CAN/CSA C22.2 No. 60950-00, EN55022:1998 Class B, EN61000-4-2:1995, EN61000-4-3:1997, EN61000-4-4:1995, EN61000-4-5:1995, EN61000-4-6:1996, EN61000-4-11:1994

ORDERING INFORMATION:

Model Name: CP10

Model #	CPU Speed
With IPMI	
6420-206-xM	1.8GHz
6420-004-xM	1.6GHz
Without IPMI	
6420-706-xM	1.8GHz
6420-504-xM	1.6GHz

(xM = Memory)

Rear Transition Module

Model #	CPU Speed
5958-000	RTM20

Model Name: CMDC

Model #	Description
6245-000	With Type 2 media connector

Model Name: HDAK

Model #	Description
6303-000	With hard drive connector

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