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INTERFACE SPECIFICATIONS

6.1 INTRODUCTION

Table 6-1 lists the specifications for the V.35/306 DTE/DCE Data Interface.

Table 6-1
V.35/306 DTE/DCE Data Interface Specifications

Item	Specification
Maximum data rates	
306 mode	15 Mb/s
V.35 mode	Cable length dependent
Clock and data tolerances	
Delay, SCT to SD	approximately 70 ns typical
Skew, SCTE to SD	approximately 20 ns typical
Balanced Drivers	
Signal swing (bipolar)	±0.55V ±0.1V into 100 ohms
Short circuit current	less than 100 mA
Rise Time	less than 20 ns
Generator Impedance	100 ohms
Balanced Receivers	
Load resistance	100 ohms
Unbalanced Drivers (EIA RS-232-C)	
Rise time	greater than 20 microseconds
Generator Impedance	less than 100 ohms
Short circuit current	less than 100 mA
Output level	± 10 volts typical (7k ohm Load)
Unbalanced Receivers	
Load Impedance	3k to 7k ohms
Maximum input voltage	± 25 Vdc
Polarity	
Data Polarity	Mark (binary 1): "A" lead negative with respect to "B" lead. Space (binary 0): "A" lead positive with respect to "B" lead
Signaling Polarity	On: greater than +3 Vdc Off: open or less than -3 Vdc

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