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Find the *UTEX / Acquisition Logic AL81G* at our website: *Click HERE*

Acquisition Logic
14120 Parke Long Court, Suite 113
Chantilly, VA 20151
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Email: info@acqlog.com

AL81G 1 GHz A/D Board

**As of August 2013 this product is obsolete.
See AL81xGTe for modern replacement**

Analog Section

- Single channel analog input
- DC coupling
- 50 Ohm input impedance
- Input ranges: 125, 250, 500 mV and 1V peak-to-peak
- Bandwidth: DC to 350 MHz
- Fine offset and gain control

A/D Converter

- 1 Gsamples/sec sampling rate, 8 bits resolution, single channel

Sampling Rates

- Sampling rates: 1000, 500, 250 & 100 MSamples/sec

Connectors

- BNC connector for analog signal input
- BNC connector for trigger input/output

Trigger Sources

- Signal Threshold Trigger
- Encoder trigger
- Software trigger
- Internal trigger connector (3 pin header)
- External trigger input

Acquisition Control

- Pre-trigger and Post-trigger delay acquisition
- Auto re-arming

Memory

- 64 MSamples on-board acquisition memory, fast offload while acquiring
- Optional 256MB

DSP Functions

- Interpolation *2 and *4 supporting input bandwidth of 400 MHz
- Programmable peak detector with one front surface follower and multiple data gates
- Missing trigger count appended to data set
- Repetition rate 90 kHz with 2500 samples/shot

Bus Interface

- PCI interface, 32 bits, 33 MHz
- PCI burst transfer rates up to 133 Mbytes/sec
- PCI master mode operation
- DMA transfers with scatter/gather support
- Interrupt on completion on DMA transfers

General

- 9.35 in. x 4.2 in. PCI board

Power Requirements

- +5.0 V 2.0A
- +3.3V 4.5A
- +12V 0.1A
- -12V 0.1A

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