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VI Logo

PowerPC Logo

Power•4B-300

Ultra-High Performance PowerPC 760 Based VME Engine

Power 4B-166 Board

Key Features

- 32 to 288 Mbytes of DRAM
- 512 Kbytes of Level 2 cache
- 8 Mbytes of FLASH Memory
- Motorola MPC106 PowerPC to PCI Bridge
- VME64 Interface
- 100Base T and 10BaseT Ethernet
- Fast SCSI-2 Port
- Parallel Port
- 4 RS232 Serial Ports
- Real-Time Clock
- 8 Kbytes of Battery Backed NVRAM
- PMC Expansion Connector

The Power•4B-300 is a **300MHz** PowerPC 760 based 6U VME Engine designed for extremely high performance embedded and real-time compute applications.

PowerPC Logo **760 Microprocessor**

The IBM PowerPC 760 Microprocessor runs at **300MHz** internally and interfaces to the external memory subsystem at 66 MHz. The PowerPC 604e has a 32Kbyte Level 1 data cache and a 32 Kbyte instruction cache.

Fast Ethernet

A DEC 21140 Ethernet controller combined with Broadcom BCM5000 and AMD AM7992 transceivers provide both 100base T-4/10base T and AUI Ethernet ports. Connections are on the P2 connector.

Features List

Fast SCSI-2 Port

A Symbios/NCR 53C810 chip is used to provide

Features List

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16 to 256 Mbytes of DRAM

Organized as 4M x 64, the on board 32 Mbytes of 60 nsec EDO DRAM runs with 7-2-2-2 cycle time without ECC and 9-4-4-4 with ECC support. An additional 32 to 256 Mbytes can be added with the use of on (single slot) or two mezzanine boards. The memory incorporates ECC and parity functions. The mezzanine boards are available in 16, 32, 64 or 128 Mbytes.

Features List

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512 Kbytes of Level 2 Cache

Organized as 64K x 72, the 9 nsec Synchronous SRAM achieves 2-1-1-1 access time from the 604e.

Features List

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8 Mbytes of FLASH Memory

Organized as 1 Mbyte x 64, with 256 Kbyte sectors, the on-board FLASH provides 8 Mbytes of non-volatile memory. Support for ROM emulation is provided via a standard 32 pin PLCC socket

Features List

Motorola MPC106 PowerPC to PCI Bridge

The Motorola PowerPC to PCI Bridge chip has a fully integrated system memory and cache controller to provide maximum PCI to memory throughput.

Features List

VME Interface

The Tundra Semiconductor Universe chip provides a single stage interface between the on board PCI the SCSI interface. The PCI interface allows the SCSI port to achieve a 10 Mbyte/Sec transfer rate. The SCSI port is available on the P2 connector.

Features List

Parallel Port

A National Semiconductor PC87332 (Super I/O chip) provides an industry standard bi-directional parallel port.

Features List

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4 RS-232 Serial Ports

The National Super I/O chip contains two independant 16550 compatible UARTs. A Startech 16C2552 chip (DUART) brings the serial port total to four. These ports all supports hardware handshaking.

Features List

Real-Time Clock

The SGS/Thomson M48T18 provides time-of-day clock and one-hundred year calendar.

Features List

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8 Kbytes of Battery-Backed NVRAM

The SGS/Thomson M48T18 also provides 8K x 8 of NVRAM backed by a 10 year lithium battery.

Features List

PMC Expansion Connector

The PMC connector allows for future expansion using an IEEE 1386 compatible, single size, PMC expansion module.

Features List

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bus and the VME bus. Extensive use of FIFOs allows this chip to decouple the two buses for increased throughput. A flexible mailbox-type interrupt mechanism, including both broadcast and unique addressing modes, enhances the standard VME interrupt structure.

Features List

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