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Find the *VIAVI Solutions / JDSU / SDL Communications Aries 725* at our website: *Click HERE*



ARIES 720/750 Line

Intelligent, hot swap compliant T3 CompactPCI adapter with HSSI and DS3 interface options

ARIES SERIES

[ARIES 720/750 Line]

The ARIES 720/750 WAN adapter is a 6-U CompactPCI featuring full hot swap compliance per PICMG 2.1 R1.0 standards. The 720 Line features a high-performance architecture with support for two modular physical interfaces. The available interface module options include front and rear panel HSSI, DS3 or E3. Typical applications for this adapter are as an intelligent T3 or FT3 interface for system level platforms. By supporting the HSSI, DS3 or E3 interface, the ARIES 720 is an ideal candidate for developers of telecommunications access products and bandwidth intensive multimedia applications. The ARIES 720 is a 6-U sized CompactPCI adapter that is based on a 32-bit PCI interface chip supporting bus mastering at a burst rate of 132 MBps per second.

The main functional components on the ARIES 720 are the i960HD66 RISC processor, the CPCI interface and the DXI HDLC Controller. The i960 processor manages packet flow between the DXI Controller, Packet RAM and Host RAM. The i960 can act as a direct PCI bus master with direct access into Host RAM or initiate DMA transfers between Packet RAM and host buffers via the PCI Interface Controller. The VLSI DXI Controller module supports both HDLC and transparent modes of operation at speeds of up to 52 Mbps. Dual 8/16 KB FIFOs are connected directly to the DXI HDLC Controller for both transmit and receive directions to provide additional packet transfer latencies. As a product of our CP/2 Architecture™, the ARIES 720 is designed with two modular electrical interfaces that support RS422, V.35 (10 Mbps maximum), HSSI (34, 45 and 52 Mbps), E3 (34 Mbps) and DS3 (44.736 Mbps).

Features/Benefits

Outstanding Performance:
- Full Hot Swap Compliance per PICMG 2.1 R1.0
- Supports data rates up 52 Mbps
- Supports Front or Rear Panel HSSI, E3 and DS3 Interfaces
- Receive and Transmit Packet Time Stamping Option

Product Reliability:
- High MTBF
- One Year Warranty

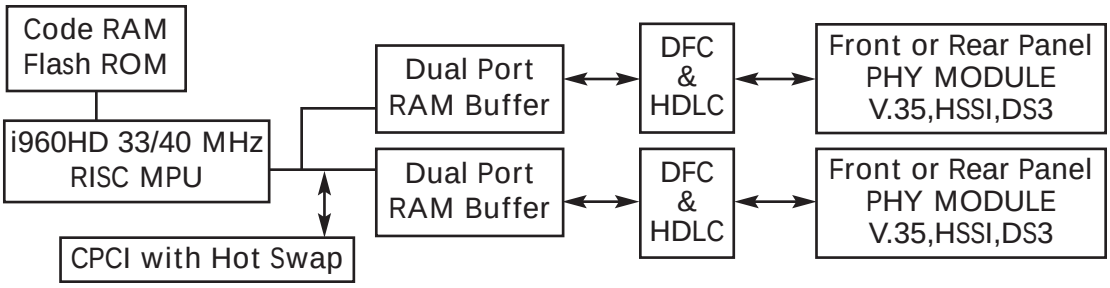
Software Support:
- Designed for CompactPCI Bus "Plug and Play"
- Windows NT® Driver Support
- Software Drivers for popular versions of UNIX
- Device Driver Development Kit (DDK) available for OEM developers
- WAN Protocol Stacks available include Frame Relay and PPP

Applications:
- Router Products
- Satellite Receivers
- Firewalls/Gateways
- Server Platforms
- WAN Monitoring Equipment
- Integrated Access Devices (IADs)

SDL Communications, Inc.
The uplink company

[ARIES 720/750 Line]

ARIES 720/750 Block Diagram



Specifications

Technical Highlights:
- Intel i960HD66 Based Processor
- 1 MB Packet RAM
- 1 MB Code RAM
- VLSI HDLC Controller
- Time Stamping Option with 1 MicroSecond Resolution
- 32-Bit CPCI Interface with Full Hot Swap Compliance per PICMG 2.1 R1.0
- OEM I.D. Support
- 6-U CompactPCI

Interface Options:
- V.35, X.21, EIA-530 and RS-449
- HSSI: TIA/EIA-613
- DS3: Per ANSI T1.404, T1.102, T1.107, TR-TSY-000009 and G.703 Interface
- E3: CCITT G.742, G.745, G.703 Interface

Environmental Requirements:
- Operating Temperature 0 to 50 degrees C (32 to 131 degrees F)
- Relative Humidity to 90% without condensation

Power Requirements:
- 2.2 Amps @ 5 Volts
- 0.2 Amps @ -12 Volts
- 0.4 Amps @ +12 Volts

Certification:
- FCC Class B
- CE Mark

Model Number	Interface Configuration
720	Single HSSI Interface
725	Dual HSSI Interface
750	Single DS3 Interface*
755	Dual DS3 Interface*

** Please note that ARIES 750 & 755 products currently offer front panel I/O only.*

SDL Communications, Inc.

The uplink company

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