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Specifications for DPIO

Max. input data rate:

25 MHz at 32-bits (-FI/FO versions)
40 MHz at 16-bits with 2:1 packing
(-FI/FO versions with PECL strobe)
50 MHz at 16-bits with 2:1 packing
(-EI/EO versions only)
75 MHz at 10-bits with 3:1 packing
(-EI/EO versions only)
75 MHz at 8-bits with 4:1 packing
(-EI/EO versions only)
49 MHz at 16-bits (-LI/LO versions)

Max. PCI transfer rate: 108 Mb/s

FIFO size: 8Kx32 or 32Kx32

Local SRAM: 512 Kbytes, 70ns

Cable lengths:
1.0m(max), 0.3m, 0.1m (TTL)
10m(max), 5m, 2m (RS422/LVDS)

Power consumption:
Typical 1.0A / max 1.5A at +5V

Weight: 100 grams

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Specifications subject to change without notice.

Ordering Information

DPIO-FI	DPIO PMC Module FPDP Input (32-bits)
DPIO-FO	DPIO PMC Module, FPDP Output (32-bits)
DPIO-CI	DPIO PMC Module, Custom Input (32-bits) (Personality module not included)
DPIO-CO	DPIO PMC Module, Custom Output (32-bits) (Personality module not included)
DPIO-EI	DPIO PMC Module, Diff. ECL Input (16-bits).
DPIO-EO	DPIO PMC Module, Diff. ECL Output (16-bits).
DPIO-DI	DPIO PMC Module, RS-422 Diff. TTL Input (32-bits).
DPIO-DO	DPIO PMC Module, RS-422 Diff. TTL Output (32-bits).
DPIO-LI	DPIO PMC Module, LVDS Diff. Input.
DPIO-LO	DPIO PMC Module, LVDS Diff. Output.

Extension to DPIO part numbers:

/32K	32K words FIFO option
/16-bit	2x16-bits packing option
/10	3x10-bits packing option
/8	4x8-bits packing option
/4	8x4-bits packing option

DES-KIT-DPIO DPIO Hardware Design Kit (Contains documentation and schematics on how to build custom personality module, based on FPDP as example.)

PLD-KIT-DPIO PLD Reprogramming Kit for DPIO (Contains binary files for data path PLD for 4:1, 3:1, 2:1 and 1:1 packing. Includes programming SW for PC and cable for PC parallel port.)

Software drivers are ordered separately. Please contact VMETRO for the part numbers and latest versions.

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