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VMIC

VMICPCI-7593 SINGLE-SLOT Pentium PROCESSOR-BASED SYSTEM SLOT CompactPCI CPU

- Single-slot Pentium processor-based CPU
- Special features for embedded applications include:
 - 16 Mbyte bootable flash on secondary IDE
 - Remote Ethernet booting using Lanworks Technologies, Inc.'s BootWare®
 - Three programmable 16-bit timers
 - 32 Kbyte of battery-backed SRAM
 - Software-selectable watchdog timer with reset
 - PMC mezzanine expansion site (IEEE-P1386 common mezzanine card standard, 5 V)
 - [VMISFT-9421](#) IOWorks generic device driver and other IOWorks family software
 - [VMISFT-7418](#) board support for VxWorks
- Standard features include:
 - 32-bit CompactPCI single-slot Pentium processor-based CPU - 166 (classic), 200 MHz MMX, and 233 MHz MMX versions
 - External (L2) cache - 512 Kbyte synchronous burst SRAM
 - Up to 128 Mbyte SDRAM using 144-pin SODIMM DRAM
 - Two on-board Fast Ethernet controllers supporting 10BaseT and 100BaseTX
 - On-board Ultra-IDE hard drive and

Operating System
Windows NT

Driver Software
IOWorks generic
device driver
([VMISFT-9421](#))
CPU Software Support
Products

Operating System
VxWorks

Driver Software
VxWorks BSP
([VMISFT-7418](#))
[CPU Software Support
Products](#)

Operating System
Solaris

No Driver Software
Required for
Solaris

Operating System
QNX

No Driver Software
Required for
QNX

Operating System

- floppy drive controllers
- High-performance 16550-compatible serial port through J3
- Enhanced parallel port with ECP/EPP modes supported through J3
- PS/2-style keyboard and mouse ports on front panel
- Real-time clock and miniature speaker included

Linux

No Driver Software
Required for Linux

| Ordering Options | A | B | C | - | D | E | F |
|---|---|---|----------|---|---|---|---|
| VMICPCI-7593 | - | | 0 | - | | | |
| <p>A = Processor</p> <ul style="list-style-type: none"> 0 = Not Used 1 = Reserved 2 = Reserved 3 = Reserved 4 = 200 MHz Pentium Processor with MMX 5 = Reserved 6 = Reserved 7 = 166 MHz Pentium Processor 8 = Reserved 9 = 233 MHz Pentium Processor with MMX <p>B = DRAM Memory</p> <ul style="list-style-type: none"> 0 = Reserved 1 = 8 Mbyte 2 = 16 Mbyte 3 = 32 Mbyte 4 = 64 Mbyte 5 = 128 Mbyte <p>C = 0 (Option reserved for future use)</p> | | | | | | | |
| ACCESSORIES | | | | | | | |
| Utility rear panel transition board. | | | | | | | |



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