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VMIVME-7686
100 Mbit Ethernet Fast/Wide SCSI USB
Pentium Pro PROCESSOR-BASED VMEbus CPU

- Pentium Pro processor-based CPU
- Special features for embedded applications include:
  - PMC mezzanine expansion site (IEEE-P1386 common mezzanine card standard, 5 V)
  - PMC expansion option (VMIVME-7434)
  - VME64 modes supported: A32/A24/D64/D32/D16/D08(EO)/ MBLT64/BLT32
  - VMEbus interrupt handler, interrupter, and system controller
  - Includes byte-swapping hardware for little-endian and big-endian data interfacing
  - VMISFT-9420 IOWorks Access and other IOWorks family software
  - VMISFT-7417 board support for QNX
  -
  - VMISFT-7418 board support for VxWorks
  - VMISFT-7419 board support for LynxOS
- Standard features include:
  - Pentium Pro processors with speeds up to 180 and 200 MHz
  - Pentium Pro internal (L2) cache sizes - 256 and 512 Kbyte (200 MHz only)
  - Up to 256 Mbyte DRAM with or without parity using 72-pin SIMMs
  - 64-bit PCI SVGA controller with 2 Mbyte video DRAM
  - On-board Fast Ethernet controller with options for 10BaseT, 10Base2, and 100BaseTX interfaces
  - PCI fast/wide SCSI-2 controller with front panel interface
  - On-board enhanced IDE hard drive and floppy drive controllers
  - Two high-performance 16550-compatible serial ports
  - Enhanced parallel port with ECP/EPP modes supported
  - PS/2-style keyboard and mouse ports on front panel
  - Real-time clock and miniature speaker included
  - Universal Serial Bus (USB) on front panel

Ordering Options		A	B	C	-	D	E	F
VMIVME-7686	-				-			

A = Processor

0 = Reserved

1 = Reserved

2 = 200 MHz Pentium Pro Processor with 256 Kbyte Secondary Cache

3 = 200 MHz Pentium Pro Processor with 512 Kbyte Secondary Cache

B = DRAM Memory

0 = Reserved	7 = 16 Mbyte, No Parity
1 = 8 Mbyte with Parity	8 = 32 Mbyte, No Parity
2 = 16 Mbyte with Parity	9 = 64 Mbyte, No Parity
3 = 32 Mbyte with Parity	J = 128 Mbyte with Parity
4 = 64 Mbyte with Parity	K = 256 Mbyte with Parity
5 = Reserved	L = 128 Mbyte, No Parity
6 = 8 Mbyte, No Parity	M = 256 Mbyte, No Parity

C = Interface

0 = VMEbus with Universe, 10Base2

1 = VMEbus with Universe, 10Base2, 10BaseT/100BaseTX

2 = Reserved

3 = No VMEbus, 10Base2

4 = No VMEbus, 10Base2, 10BaseT/100BaseTX

5 = Reserved

6 = VMEbus with Universe II, 10Base2

7 = VMEbus with Universe II, 10Base2, 10BaseT/100BaseTX

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