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Find the *VX Instruments PXCe4006* at our website: *Click HERE*

PXCe4006
6-slot PXIe Chassis



TECHNICAL DATA SHEET

PXI
VXI
LAN
cPCI
PXIe
GPIB
USB
RS232
485
external
PCIe

Features

- 6-slot PXI Express chassis
- 1 system slot, 1 PXI Express slot and 4 PXI/PXI Express full hybrid peripheral slots
- Optional: complex FPGA trigger controller with 8 freely configurable trigger IOs on the rear side
- Very compact 1U 19" design
- Four link PXI Express chassis
- Easily replacable fan unit

Product Information

To reduce space in modern test systems the PXCe4006 features a very compact 1U 19" design.

The following slots are available:

- 1 PXI Express System Controller Slot,
- 1 PXI Express Peripheral Slot,
- 4 PXI/PXI Express Hybrid Peripheral Slots.

This provides the user the highest flexibility to configure his test system with PXI and PXI Express devices.

Optionally available is an internal complex FPGA trigger controller board, which provides 8 freely configurable trigger IOs on the rear side (SMB connector).

Also available is the PXIe3110 PXI Express Embedded Controller. Equipped with a modern Intel® Core™ i5 CPU and a big variety of interfaces, the controller is a perfect addition to the chassis. For detailed specifications, please check the data sheet of the PXIe3110.

General	Specification	Comment
Device size	≈445 mm x 312 mm x 44 mm (w x d x h)	
Device weight	≈6 kg	
Operating temperature	0 ... 55°C	
Operating altitude	<2 000 m	
Relative humidity	Up to 85% at 35°C	
Storage temperature range	-20 ... 70°C	

Power	Specification	Comment
AC Input		
Input voltage range	90 ... 264 V_{AC}	Active PFC
Input voltage frequency	47 ... 63 Hz	
DC Output[1]		
+5 V	14.0 A	
+12 V	15.5 A	
+3.3 V	12.0 A	
-12 V	0.5 A	
+5 V AUX	3.0 A	
Usable power	<200 W	
Modes	Auto power on mode	Selectable on the backpanel
	Auto fan mode	Selectable on the backpanel

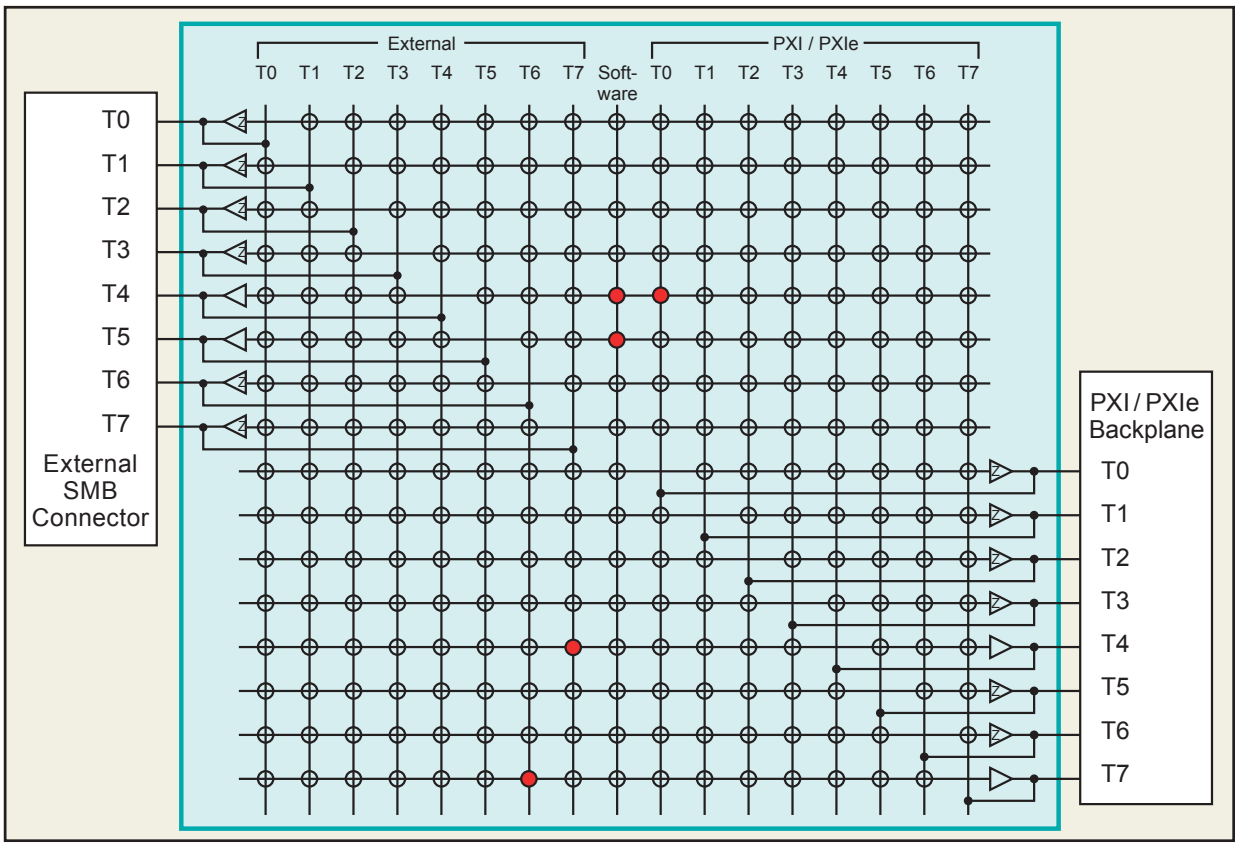
Chassis Cooling	Specification	Comment
Cooling capacity	30 W per Slot	
Cooling principle	4x cooling fans, horizontal cooling	

Interface	Specification	Comment
System bandwidth	PCIe Gen 2 X1	

[1] Maximum ripple and noise: ±12 V: 120 mV, other: 50 mV; Load regulation: 5% (@ -12 V: ±10%).

Trigger (Only with Option PXTe5408 embedded)

Trigger System	Specification	Comment
Input from		
Internal function module	Module can trigger itself	
Software	Via software command	
SMB connector	Trigger input (5 V TTL level) 0 ... 7	50 Ω mode selectable
PXI trigger	Trigger 0 ... 7	From the PXI backplane
Output to		
Internal function module	Module can trigger itself	
SMB connector	Trigger output (5 V TTL level) 0 ... 7	50 Ω mode selectable
PXI trigger	Trigger 0 ... 7	To the PXI backplane
Trigger delay	0 ... 200 s	Programmable delay, 100 ns resolution
Trigger slope	Positive or negative	
Trigger Mode	Asynchronous, synchronous level, synchronous slope	



● = Connection Example

Slot Assignment



Ordering Options

Accessories	Ordering Information
PXI Express Embedded Controller	PXIe3110
PCIe ExpressCard34 (Laptop)	PCIe7801
ePCIe cable 1 m	ZK1103-0
ePCIe cable 2 m	ZK1104-0
ePCIe cable 3 m	ZK1105-0
ePCIe cable 7 m	ZK1106-0

Ordering Options	Comment
PXTe5408 embedded	8 ch. internal trigger controller

Notes: All product data are specified for 1 year at an ambient temperature of 23°C ±5°C (after 1 hour warm-up time).
Product specification and description in this document are subject to change without notice.

FOR YOUR NOTES

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