

EST VisionICE II
PowerPC MIPS JTAG Emulator



\$650.00

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VISIONICE II

SOURCE-LEVEL DEBUGGERS



WIND RIVER

Optimal use of on-chip debugging capability

visionICE II is a hardware-assisted tool that uses the on-chip debugging services embedded in the micro-processor. **visionICE II** comes standard with a software debugger for a fully-integrated hardware and software solution, and provides the industry's most feature-rich tool for board bring-up, flash programming, and production/test.

visionICE II also provides a high-speed Ethernet connection between a host debugger and target micro-processor for development. It is the second generation release of Wind River's proven, high-performance emulation system, offering industry leading download speeds, overall debug throughput, and unprecedented debug efficiency.

visionICE II allows developers to perform common source-level debug activities, such as watching memory and controlling large numbers of registers in a matter of seconds. Developers are able to shorten edit/compile/download/debug cycles and make more turns, even with the most complex applications.

High-performance JTAG

visionICE II eliminates slow download times and run control when developing with on-chip debugger (OCD) microprocessors. With new hardware logic that caches common JTAG scan chains, **visionICE II** dramatically improves performance in development.



Features

- High-speed Ethernet connection
- RTOS awareness
- Built-in hardware diagnostics
- Flash memory programming
- External triggering
- Source-level debugging
- Internal register configuration
- Modular hardware design

Supported targets*

- ARM
 - ARM7TDMI, ARM710, ARM720T, ARM740T
 - ARM9TDMI, ARM920T, ARM922T, ARM940T
- MIPS32
 - 4Kc/4Kp/4Km/4KEc
 - Alchemy Semiconductor AU1000
 - Broadcom BCM1100/1101, BCM3350/3351/3352, BCM4710, BCM7100
 - IDT RC32332/334/364/355
 - Philips PR3930, PR3940, PNX852X, PNX832X
- MIPS64 (32-bit addressing)
 - 5Kc
 - NEC VR5432, VR5500
 - Toshiba TX4927, TX4955
 - Wintegra WinPath 777
- Motorola/IBM PowerPC
 - Motorola MPC5xx/74xx/8xx/82xx
 - Motorola/IBM PPC6xx/7xx
 - IBM PPC4xx
- Motorola ColdFire
 - Motorola MCF52xx/530x/540x
- INTEL XScale
 - 80200, IOP310, IOP321, PXA210, PXA250, IXP425, IXC1100

*Please contact your local Wind River account manager for current target availability.

Feature summary

- High-speed Ethernet download: Download up to 750KB/second with certain targets. Typically 4 to 10 times faster than other products.
- Modular design and target versatility: **visionICE II** features a modular design that allows developers to expand and integrate with **visionTRACE** logic analyzers, and to move from one target to another using the same debugging hardware.
- RTOS awareness: Obtain detailed information on any real-time operating systems' (RTOS) object.
- On-chip debug target control: Provides capability to start/stop target, set internal hardware/software breakpoints, take a target snapshot, reset the target, step one statement/instruction into function calls, and step over or out of a function.
- Built-in hardware diagnostics: Includes a comprehensive suite of RAM tests, scope loops, and CRC checks.
- Flash memory programming: A complete library of turn-key algorithms for programming flash devices.
- External triggering: A trigger in/out port and cable for triggering connected external devices.
- Source-level debugging: **visionCLICK** and **SINGLE STEP** source-code debuggers. Integrated with **TORNADO**,[®] **VxWORKS**,[®] and third-party operating systems.

- Internal register configuration: An automated internal register configuration utility with bit-level detail and online help.
- Additional custom registers: Supports 32 groups – a total of 960 custom registers.
- Target console port: Permits remote monitoring of applications and the serial port, channeling the serial port up through the network.

Embedded Development Solutions

Wind River offers fully integrated hardware and software solutions designed for all phases of embedded development, from early board bring-up to debug and production/test. This tool suite includes reference designs with schematics and BSPs, software debuggers, hardware-assisted on-chip debuggers, compilers, and other software tools.

RTOS independent

Wind River provides an extensive line of embedded development tools for all embedded RTOSs, including in-house proprietary RTOSs, Wind River RTOSs, and other commercial embedded operating systems. Published APIs are provided to support easy integration of Wind River tools with other embedded operating systems.

TORNADO integration

visionICE II is tightly integrated with **TORNADO**, and enables **TORNADO TOOLS** to communicate with the target through JTAG. This provides board bring-up and debug support, crash

diagnosis, as well as a download and debug channel if the Ethernet and serial channels are used by the application.

Source-level debuggers

visionCLICK and **SINGLE STEP** source-level debuggers run under Windows and UNIX, and are fully integrated with Wind River's hardware-assisted tools. In addition to point-and-click capability for major functions, these debuggers offer support for hardware, software, and complex breakpoints, a register configuration utility, built-in diagnostics, complete run-control, and many other features invaluable to developers.

Targets Supported

All **visionICE II** supported targets can utilize the **visionEXPAND** product.



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